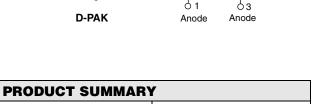
Vishay High Power Products

Schottky Rectifier, 5.5 A



Base cathode

Q 4, 2

PRODUCT SUMMARY			
I _{F(AV)}	5.5 A		
V _R	60 V		

FEATURES

- · Popular D-PAK outline
- · Small foot print, surface mountable
- · Low forward voltage drop
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- · Designed and qualified for AEC Q101 level

DESCRIPTION

The 50WQ06FNPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	5.5	А		
V _{RRM}		60	V		
I _{FSM}	$t_p = 5 \ \mu s \ sine$	320	А		
V _F	5 Apk, T _J = 125 °C	0.54	V		
TJ	Range	- 40 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	50WQ06FNPbF	UNITS	
Maximum DC reverse voltage	V _R	60	V	
Maximum working peak reverse voltage	V _{RWM}		v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_{C} = 132 °C, rectangular waveform		5.5	
Maximum peak one cycle non-repetitive surge current	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	320	A
See fig. 7		1 GW	rated V _{RRM} applied	105	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 1.2 \text{ A}, L = 10 \text{ mH}$		7	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.8	А

* Pb containing terminations are not RoHS compliant, exemptions may apply





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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	5 A	T _{.1} = 25 °C	0.57	V
		10 A	1j=25 C	0.74	
		5 A	T _J = 125 °C	0.54	
		10 A		0.68	
Maximum reverse leakage current	I (1)	$T_J = 25 \ ^{\circ}C$	 V_R = Rated V_R 	3	mA
See fig. 2	'RM \''	T _J = 125 °C		35	
Threshold voltage	V _{F(TO)}	$T_{\rm J} = T_{\rm J} \text{ maximum} \qquad \qquad$		0.35	V
Forward slope resistance	r _t			mΩ	
Typical junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		360	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

⁽¹⁾ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 40 to 150	°C	
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	3.0	°C/W	
Approximate weight			0.3	g	
		0.01	oz.		
Marking device		Case style D-PAK (similar to TO-252AA)	50WQ	06FN	

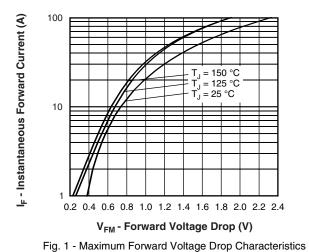
Note

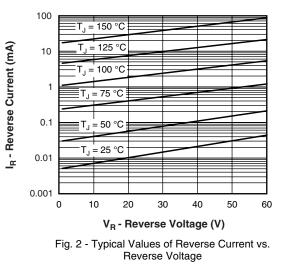
⁽¹⁾ $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink



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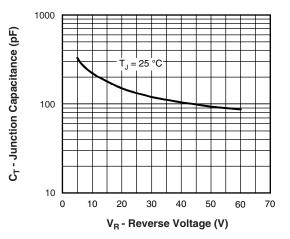


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

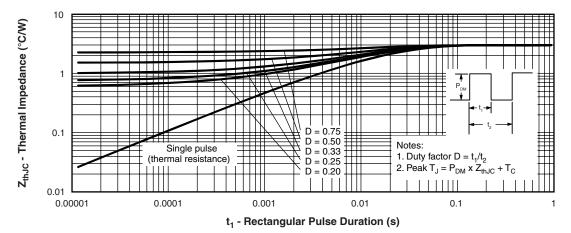


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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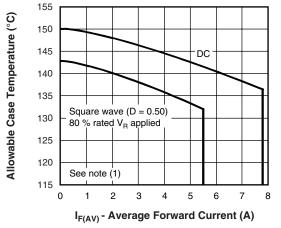
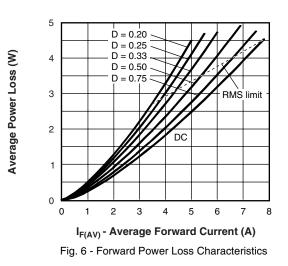


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



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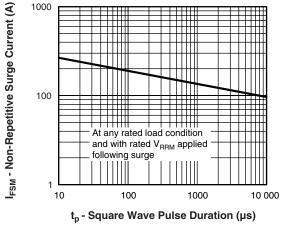


Fig. 7 - Maximum Non-Repetitive Surge Current

Note

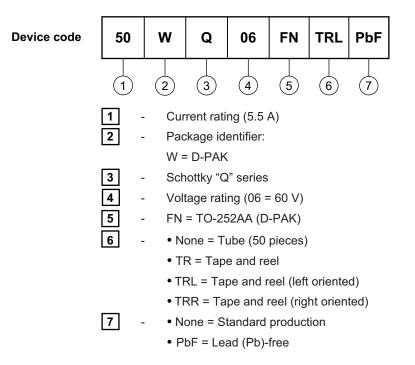
- (1) Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC};$ $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 D); I_R at V_{R1} = 80 \% rated V_R$



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95016			
Part marking information	http://www.vishay.com/doc?95059		
Packaging information	http://www.vishay.com/doc?95033		



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