



DDTC (R1-ONLY SERIES) E

NPN PRE-BIASED SMALL SIGNAL SURFACE MOUNT TRANSISTOR

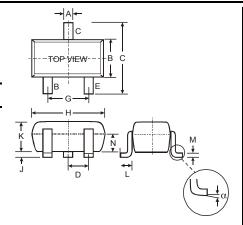
Features

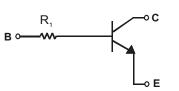
- Epitaxial Planar Die Construction
- Complementary PNP Types Available (DDTA)
- · Built-In Biasing Resistor, R1 only
- Lead Free/RoHS Compliant (Note 2)
- "Green" Device (Note 3 and 4)

Mechanical Data

- Case: SOT-523
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- Terminal Connections: See Diagram
- Marking: Date Code information: See Diagrams & Page 4
- Ordering Information: See Page 4
- Weight: 0.002 grams (approximate)

P/N	R1 (NOM)	Marking
DDTC113TE	1ΚΩ	N01
DDTC123TE	2.2ΚΩ	N03
DDTC143TE	4.7ΚΩ	N07
DDTC114TE	10ΚΩ	N12
DDTC124TE	22Κ Ω	N16
DDTC144TE	47KΩ	N19
DDTC115TE	100KΩ	N23
DDTC125TE	200ΚΩ	N25





SCHEMATIC DIAGRAM

SOT-523 Dim Min Max Тур Α 0.15 0.30 0.22 В 0.75 0.85 0.80 С 1.45 1.75 1.60 D 0.50 G 0.90 1.10 1.00 Н 1.50 1.70 1.60 J 0.00 0.10 0.05 Κ 0.60 0.80 0.75 0.10 0.30 0.22 0.20 М 0.10 0.12 N 0.45 0.65 0.50 0° 8° All Dimensions in mm

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	
Collector Base Voltage	V _{CBO}	50	V	
Collector-Emitter Voltage	V _{CEO}	50	V	
Emitter-Base Voltage	V _{EBO}	5	V	
Collector Current	I _C (Max)	100	mA	
Power Dissipation	P _d	150	mW	
Thermal Resistance, Junction to Ambient Air (Note 1)	$R_{\theta JA}$	833	°C/W	
Operating and Storage Temperature Range	T_{j},T_{STG}	-55 to +150	°C	

Notes:

- 1. Mounted on FR4 PC Board with recommended pad layout at http://www.diodes.com/datasheets/ap02001.pdf.
- 2. No purposefully added lead.
- 3. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
- Product manufactured with Date Code UO (week 40, 2007) and newer are built with Green Molding Compound. Product manufactured prior to Date Code UO are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.



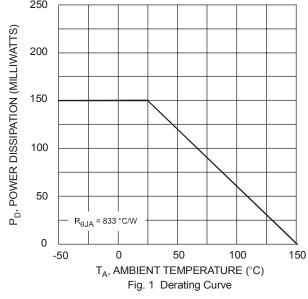
Electrical Characteristics @T_A = 25°C unless otherwise specified

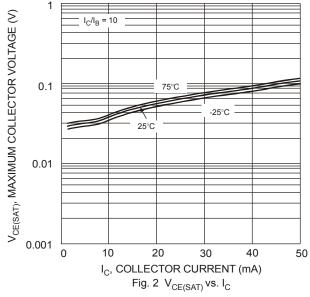
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
Collector-Base Breakdown Voltage	BV _{CBO}	50	_	_	V	I _C = 50mA		
Collector-Emitter Breakdown Voltage	BV_CEO	50	_	_	V	I _C = 1mA		
Emitter-Base Breakdown Voltage	BV _{EBO}	5	_	_	V	I _E = 50μA		
Collector Cutoff Current	I _{CBO}	_		0.5	μΑ	V _{CB} = 50V		
Emitter Cutoff Current	I _{EBO}	_	_	0.5	μΑ	V _{EB} = 4V		
Collector-Emitter Saturation Voltage	V _{CE(sat)}	_	-	0.3	V	$\begin{array}{ll} I_{C}/I_{B} = 10 \text{mA}/1 \text{mA} & \text{DDTC113TE} \\ I_{C}/I_{B} = 5 \text{mA}/0.5 \text{mA} & \text{DDTC123TE} \\ I_{C}/I_{B} = 2.5 \text{mA}/.25 \text{mA} & \text{DDTC143TE} \\ I_{C}/I_{B} = 1 \text{mA}/.1 \text{mA} & \text{DDTC114TE} \\ I_{C}/I_{B} = 5 \text{mA}/0.5 \text{mA} & \text{DDTC124TE} \\ I_{C}/I_{B} = 2.5 \text{mA}/.25 \text{mA} & \text{DDTC144TE} \\ I_{C}/I_{B} = 1 \text{mA}/0.1 \text{mA} & \text{DDTC115TE} \\ I_{C}/I_{B} = .5 \text{mA}/.05 \text{mA} & \text{DDTC125TE} \\ \end{array}$		
DC Current Transfer Ratio	h _{FE}	100	250	600	_	I_C = 1mA, V_{CE} = 5V		
Input Resistor (R₁) Tolerance	ΔR_1	-30	_	+30	%	_		
Gain-Bandwidth Product*	f _T	_	250	_	MHz	$V_{CE} = 10V, I_{E} = -5mA, f = 100MHz$		

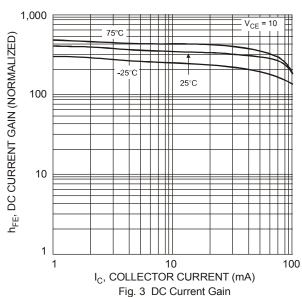
^{*} Transistor – For Reference Only

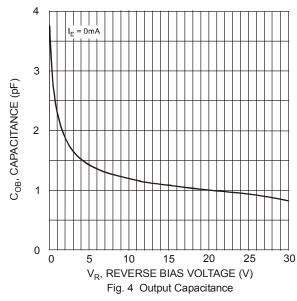


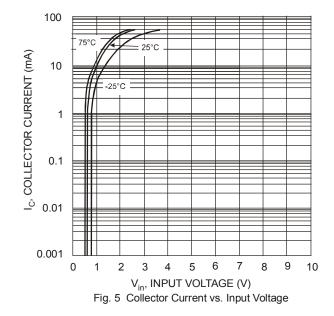
TYPICAL CURVES - DDTC114TE

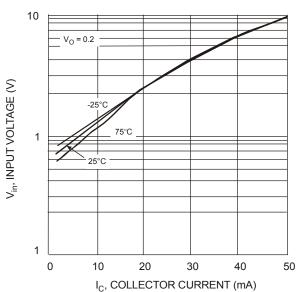












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Fig. 6 Input Voltage vs. Collector Current

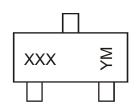


Ordering Information (Note 5)

Device	Packaging	Shipping
DDTC113TE-7-F	SOT-523	3000/Tape & Reel
DDTC123TE-7-F	SOT-523	3000/Tape & Reel
DDTC143TE-7-F	SOT-523	3000/Tape & Reel
DDTC114TE-7-F	SOT-523	3000/Tape & Reel
DDTC124TE-7-F	SOT-523	3000/Tape & Reel
DDTC144TE-7-F	SOT-523	3000/Tape & Reel
DDTC115TE-7-F	SOT-523	3000/Tape & Reel
DDTC125TE-7-F	SOT-523	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



Xxx = Product Type Marking Code See Page 1 Diagrams YM = Date Code Marking Y = Year ex: T = 2006 M = Month ex: 9 = September

Date Code Key

Year	200	6	2007		2008	20	09	2010		2011	2	2012
Code	Т		U		V	V	٧	Х		Υ		Z
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

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