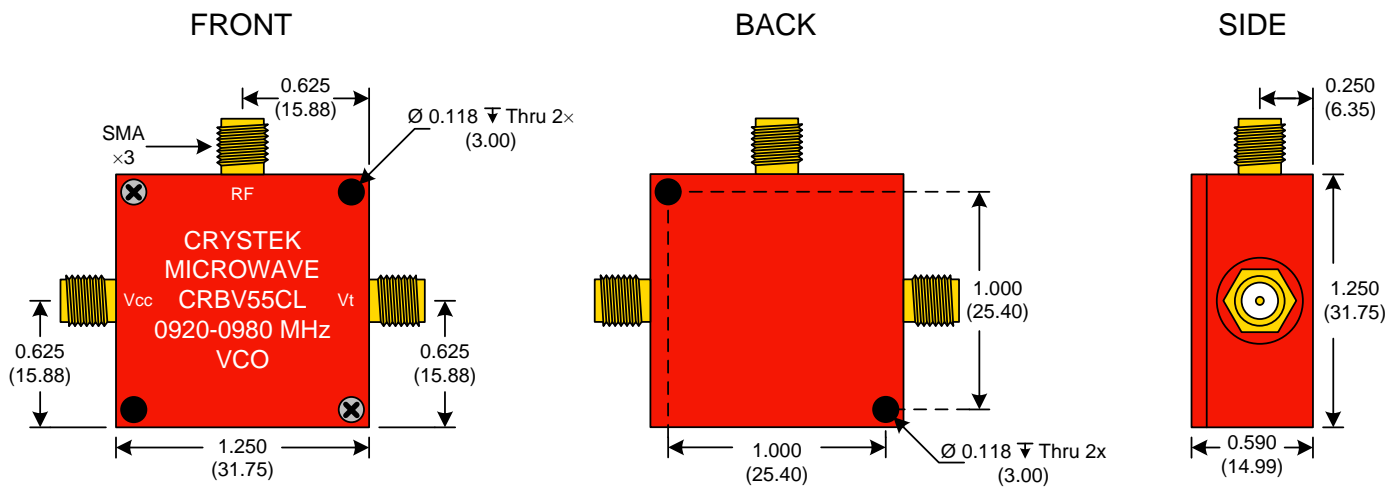




PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			920	MHz
Upper Frequency:	980			MHz
Tuning Voltage:	1.0		5.0	VDC
Supply Voltage:	4.75	5.0	5.25	VDC
Output Power:	+3.0	+6.0	+9.0	dBm
Supply Current:			35	mA
Harmonic Suppression (2 <sup>nd</sup> Harmonic):		-12		dBc
Pushing:		13.0		MHz/V
Pulling, all Phases:		3.0		MHz pk-pk
Tuning Sensitivity:		30		MHz/V
Phase Noise @ 10kHz offset:		-105	-100	dBc/Hz
Phase Noise @ 100kHz offset:		-128	-123	dBc/Hz
Load Impedance:		50		$\Omega$
Input Capacitance:			30	pF
Operating Temperature Range:	-40		+85	$^{\circ}\text{C}$
Storage Temperature Range:	-45		+90	$^{\circ}\text{C}$



● Unless otherwise specified, Dimensions are in:  $\frac{\text{IN}}{\text{(mm)}}$

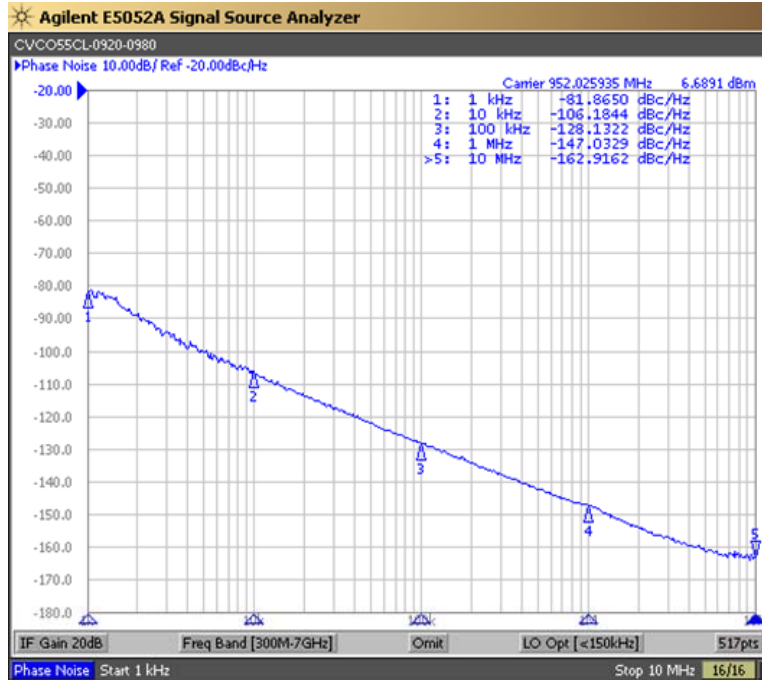
**Product Control:**

Crystek Part Number:	CRBV55CL-0920-0980	Release Date:	06-Nov-08
Revision Level:	A	Responsible:	C. Vales

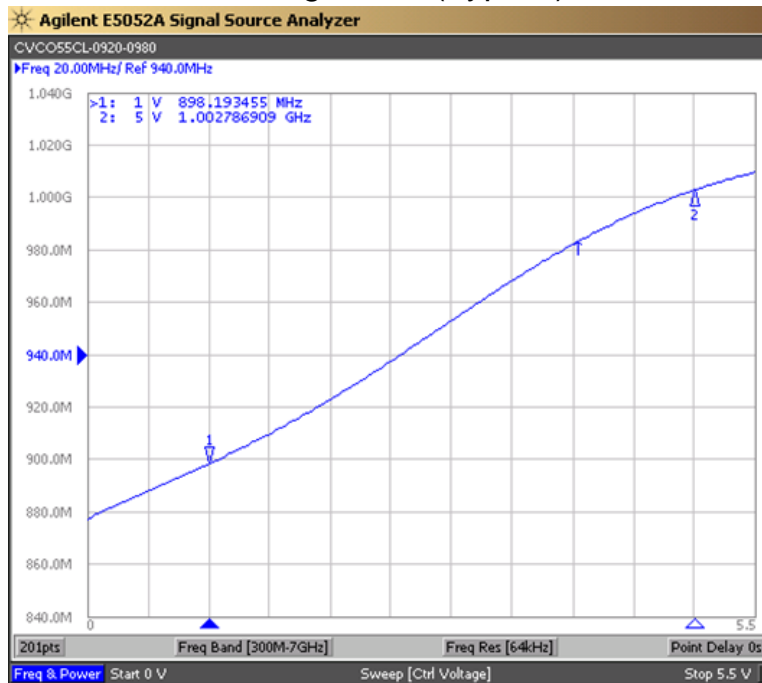




Phase Noise (1 Hz BW, Typical)



Tuning Curve (Typical)



**Product Control:**

Crystek Part Number:	CRBV55CL-0920-0980	Release Date:	06-Nov-08
Revision Level:	A	Responsible:	C. Vales

Specification is subject to change without notice

