

Single P-channel MOSFET

ELM34405AA-N

■ General description

ELM34405AA-N uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance.

■ Features

- $V_{ds} = -40V$
- $I_d = -5.5A$
- $R_{ds(on)} < 55m\Omega$ ($V_{gs} = -10V$)
- $R_{ds(on)} < 94m\Omega$ ($V_{gs} = -4.5V$)

■ Maximum absolute ratings

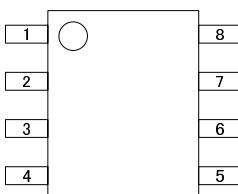
Parameter	Symbol	Limit	Unit	Note
Drain-source voltage	V_{ds}	-40	V	
Gate-source voltage	V_{gs}	± 20	V	
Continuous drain current	I_d	-5.5	A	
		-4.5		
Pulsed drain current	I_{dm}	-20	A	3
Power dissipation	P_d	2.5	W	
		1.3		
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	°C	

■ Thermal characteristics

Parameter	Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$R_{\theta ja}$		50	°C/W	

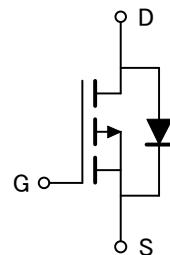
■ Pin configuration

SOP-8 (TOP VIEW)



Pin No.	Pin name
1	SOURCE
2	SOURCE
3	SOURCE
4	GATE
5	DRAIN
6	DRAIN
7	DRAIN
8	DRAIN

■ Circuit



Single P-channel MOSFET

ELM34405AA-N

■ Electrical characteristics

$T_a=25^\circ C$

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BVdss	$I_d=-250\ \mu A$, $V_{gs}=0V$	-40			V	
Zero gate voltage drain current	Idss	$V_{ds}=-32V$, $V_{gs}=0V$ $V_{ds}=-30V$, $V_{gs}=0V$, $T_j=125^\circ C$			-1 -10	μA	
Gate-body leakage current	Igss	$V_{ds}=0V$, $V_{gs}=\pm 20V$			± 250	nA	
Gate threshold voltage	Vgs(th)	$V_{ds}=V_{gs}$, $I_d=-250\ \mu A$	-1.0	-1.5	-2.5	V	
On state drain current	Id(on)	$V_{gs}=-10V$, $V_{ds}=-5V$	-20			A	1
Static drain-source on-resistance	Rds(on)	$V_{gs}=-10V$, $I_d=-5.5A$ $V_{gs}=-4.5V$, $I_d=-4.5A$		38 65	55 94	$m\Omega$ $m\Omega$	1
Forward transconductance	Gfs	$V_{ds}=-10V$, $I_d=-5.5A$		11		S	1
Diode forward voltage	Vsd	$I_s=I_f$, $V_{gs}=0V$			-1	V	1
Max. body-diode continuous current	Is				-1.3	A	
Pulsed body-diode current	Ism				-2.6	A	3
DYNAMIC PARAMETERS							
Input capacitance	Ciss	$V_{gs}=0V$, $V_{ds}=-10V$, $f=1MHz$			690		pF
Output capacitance	Coss				310		pF
Reverse transfer capacitance	Crss				75		pF
SWITCHING PARAMETERS							
Total gate charge	Qg	$V_{gs}=-10V$, $V_{ds}=-20V$ $I_d=-5.5A$			14.0		nC
Gate-source charge	Qgs				2.2		nC
Gate-drain charge	Qgd				1.9		nC
Turn-on delay time	td(on)	$V_{gs}=-10V$, $V_{ds}=-20V$ $I_d \approx -1A$, $R_{gen}=6\ \Omega$			6.7	13.4	ns
Turn-on rise time	tr				9.7	19.4	ns
Turn-off delay time	td(off)				19.8	35.6	ns
Turn-off fall time	tf				12.3	22.2	ns
Body diode reverse recovery time	trr	$I_f=-5A$, $dI/dt=100A/\ \mu s$			15.5		ns
Body diode reverse recovery charge	Qrr	$I_f=-5A$, $dI/dt=100A/\ \mu s$			7.9		nC

NOTE :

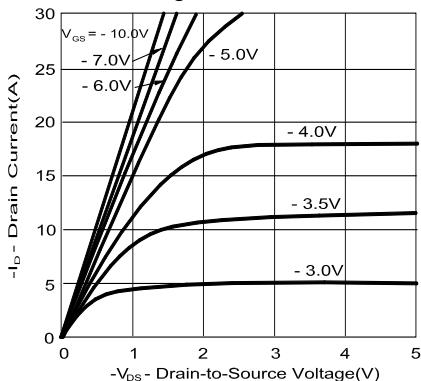
1. Pulsed width $\leq 300\ \mu s$ sec and Duty cycle $\leq 2\%$.
2. Independent of operating temperature.
3. Pulsed width limited by maximum junction temperature.
4. Duty cycle $\leq 1\%$.

Single P-channel MOSFET

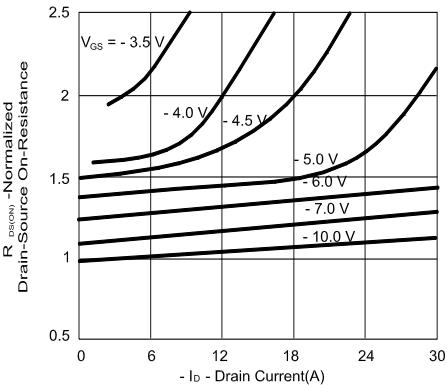
ELM34405AA-N

■ Typical electrical and thermal characteristics

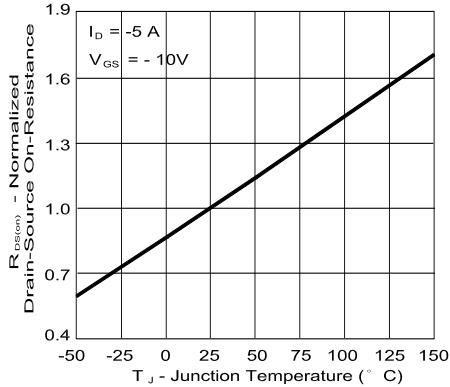
On-Region Characteristics



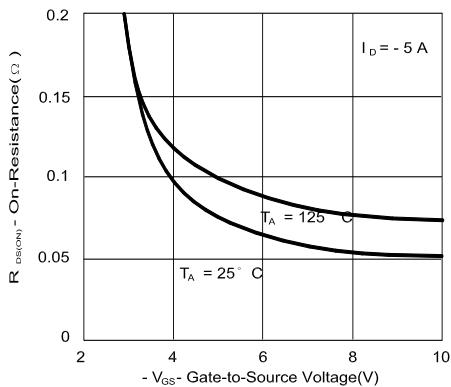
On-Resistance Variation with Drain Current and Gate Voltage



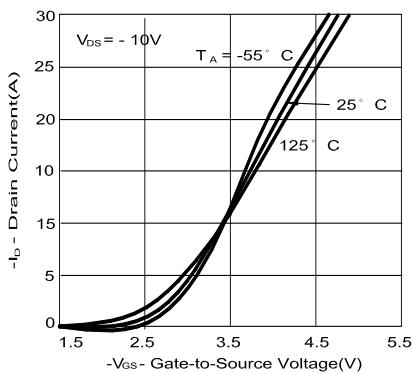
On-Resistance Variation with Temperature



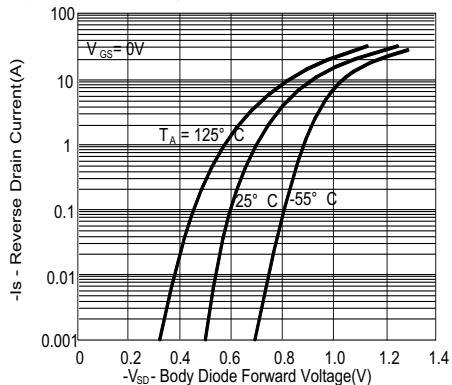
On-Resistance Variation with Gate-to-Source Voltage



Transfer Characteristics



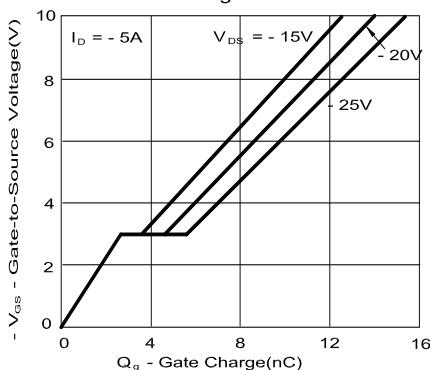
Body Diode Forward Voltage Variation with Source Current and Temperature



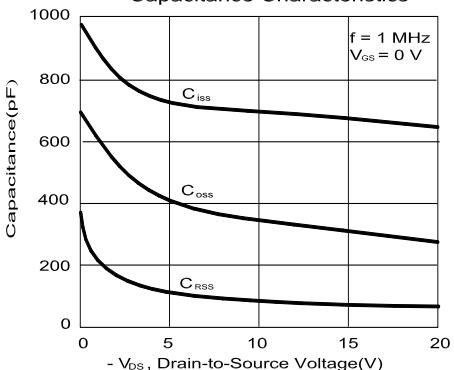
Single P-channel MOSFET

ELM34405AA-N

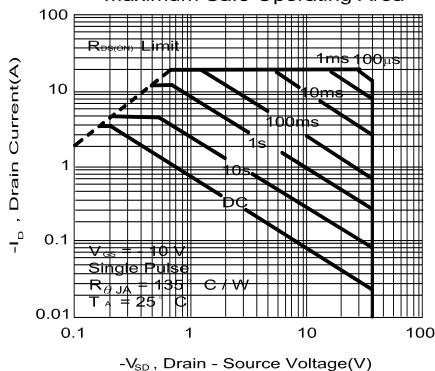
Gate Charge Characteristics



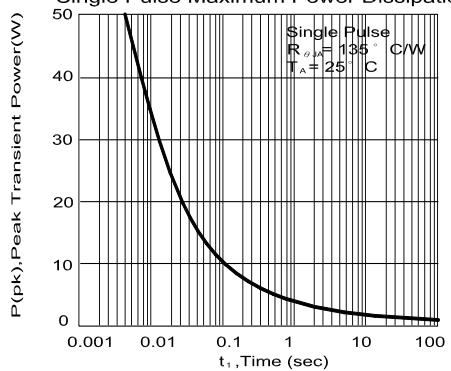
Capacitance Characteristics



Maximum Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

