

MOC8111M, MOC8112M, MOC8113M

6-Pin DIP Optocoupler for Power Supply Applications (No Base Connection)



Features

- High isolation voltage
7500 VAC Peak—1 second
- High BV_{CEO} minimum 70 Volts
- Current transfer ratio in selected groups:
MOC8111M: 20% min.
MOC8112M: 50% min.
MOC8113M: 100% min.
- Maximum switching time in saturation specified
- Underwriters Laboratory (UL) recognized
(File #E90700, Vol. 2)
- IEC60747-5-2 approved (ordering option V)

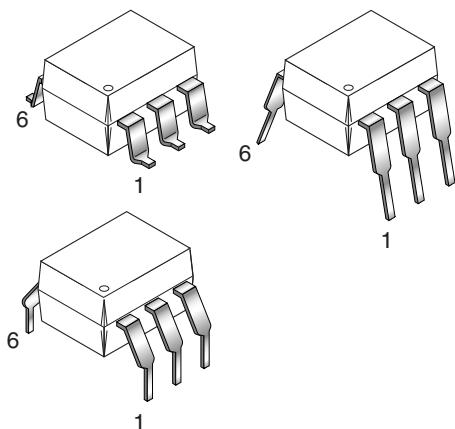
Applications

- Power supply regulators
- Digital logic inputs
- Microprocessor inputs
- Appliance sensor systems
- Industrial controls

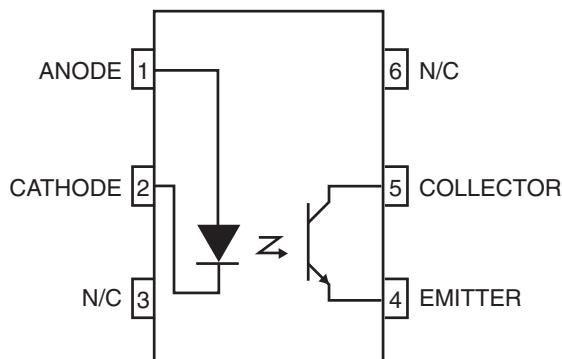
Description

The MOC811XM series consists of a Gallium Arsenide IRED coupled with an NPN phototransistor. The base of the transistor is not bonded to an external pin for improved noise immunity.

Packages



Schematic



Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ Unless otherwise specified.)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Value	Unit
TOTAL DEVICE			
P_D	Total Device Power Dissipation @ $T_A = 25^\circ\text{C}$	260	mW
	Derate above 25°C	3.5	mW/ $^\circ\text{C}$
T_{OPR}	Ambient Operating Temperature Range	-40 to +100	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-40 to +150	$^\circ\text{C}$
T_{SOL}	Lead Soldering Temperature (Wave Solder) (1/16" from case, 10 sec. duration)	260	$^\circ\text{C}$
INPUT LED			
I_F	Forward Current – Continuous	90	mA
$I_F(pk)$	Forward Current – Peak (PW = 1 μs , 300pps)	3	A
V_R	Reverse Voltage	6	V
P_D	LED Power Dissipation @ $T_A = 25^\circ\text{C}$	135	mW
	Derate above 25°C	1.8	mW/ $^\circ\text{C}$
OUTPUT TRANSISTOR			
P_D	Detector Power Dissipation @ $T_A = 25^\circ\text{C}$	200	mW
	Derate above 25°C	2.67	mW/ $^\circ\text{C}$

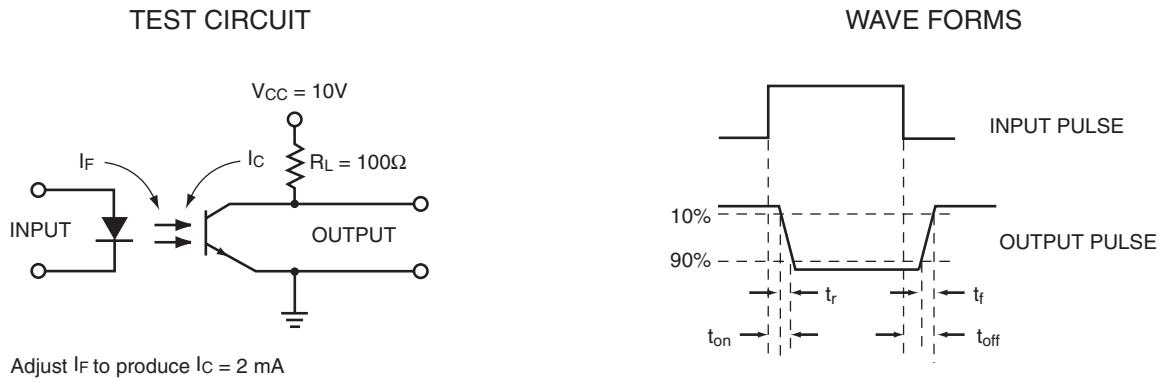
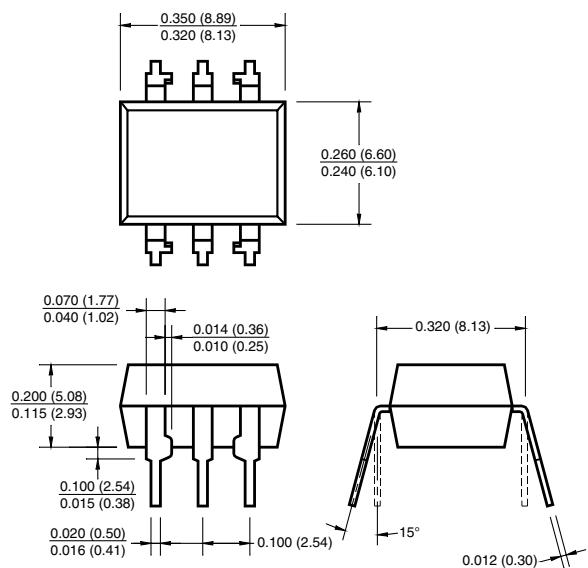


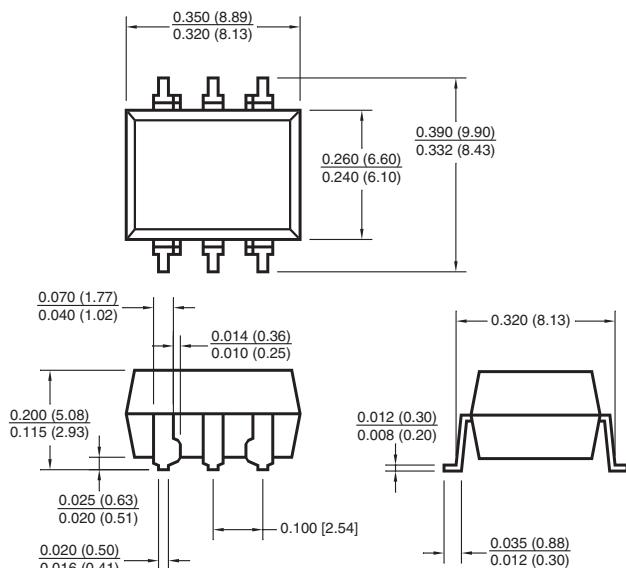
Figure 7. Switching Time Test Circuit and Waveforms

Package Dimensions

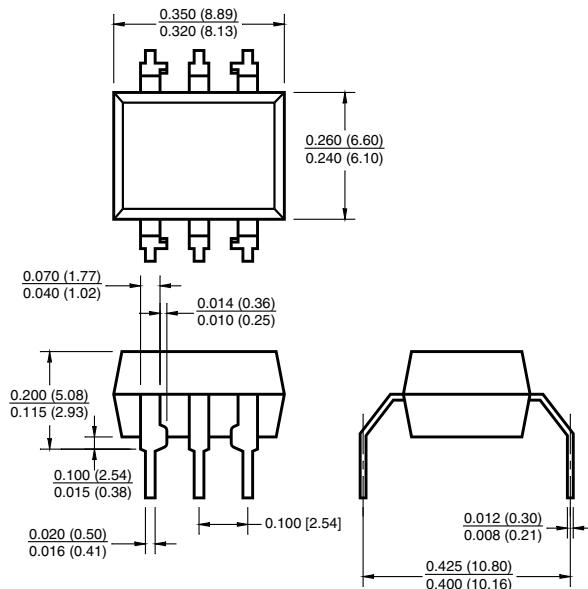
Through Hole



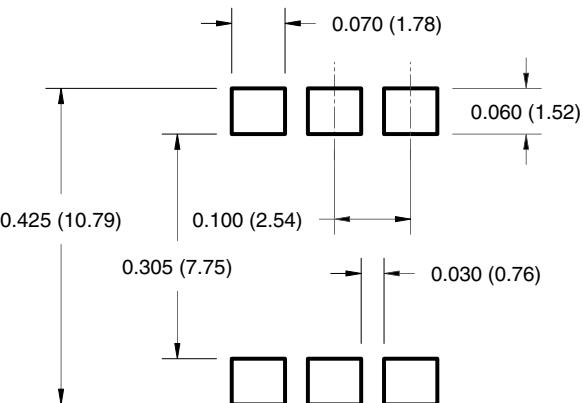
Surface Mount



0.4" Lead Spacing



Recommend Pad Layout for Surface Mount Leadform



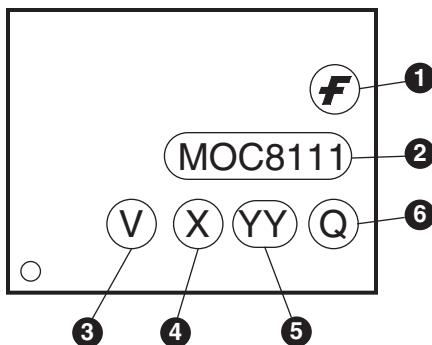
Note:

All dimensions are in inches (millimeters).

Ordering Information

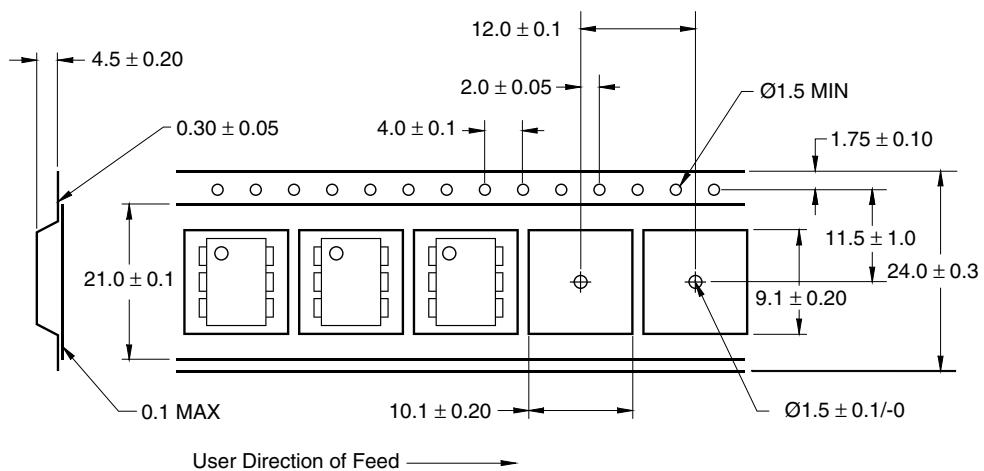
Option	Example Part Number	Description
No Suffix	MOC8111M	Through Hole
S	MOC8111SM	Surface Mount Lead Bend
SR2	MOC8111SR2M	Surface Mount; Tape and Reel
T	MOC8111TM	0.4" Lead Spacing
V	MOC8111VM	VDE 0884
TV	MOC8111TVM	IEC60747-5-2 (VDE), 0.4" Lead Spacing
SV	MOC8111SVM	IEC60747-5-2 (VDE), Surface Mount
SR2V	MOC8111SR2VM	IEC60747-5-2 (VDE), Surface Mount, Tape and Reel

Marking Information



Definitions	
1	Fairchild logo
2	Device number
3	VDE mark (Note: Only appears on parts ordered with VDE option – See order entry table)
4	One digit year code, e.g., '8'
5	Two digit work week ranging from '01' to '53'
6	Assembly package code

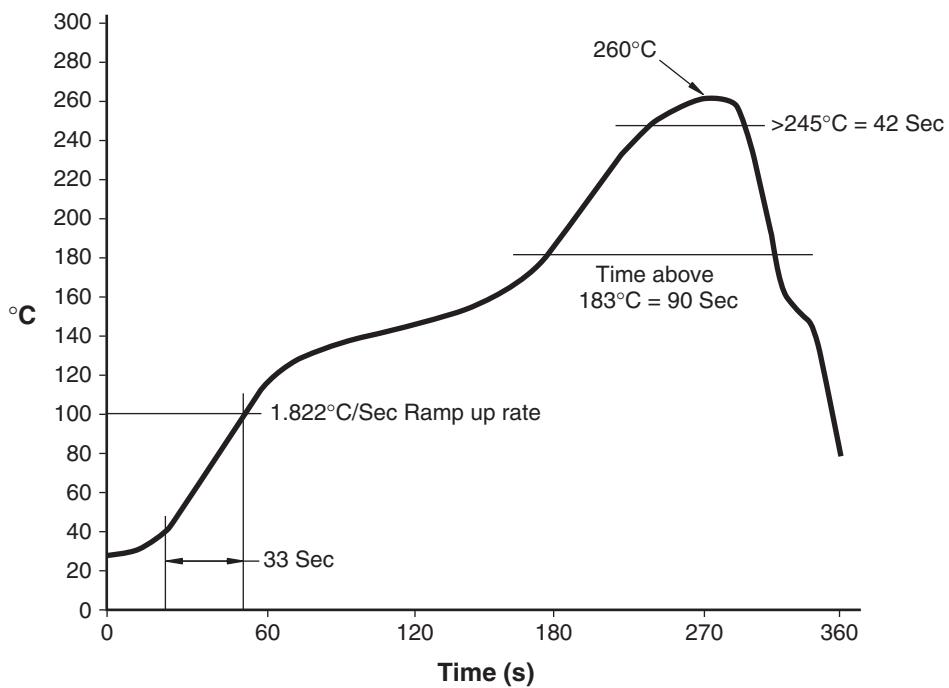
Tape Dimensions



Note:

All dimensions are in millimeters.

Reflow Profile





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Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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