

## isc Silicon NPN Power Transistor

MJW16010A

**DESCRIPTION**

- Low Collector Saturation Voltage
- Collector-Emitter Sustaining Voltage-  
:  $V_{CEO(SUS)} = 500V$ (Min)
- Wide Area of Safe Operation

**APPLICATIONS**

- Designed for high-voltage, high-speed,power switching in inductive circuits where fall time is critical. They are particularly suited for line-operated switchmode applications.

Typical applications:

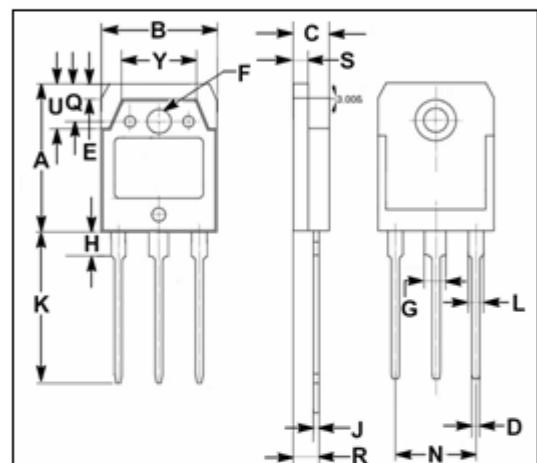
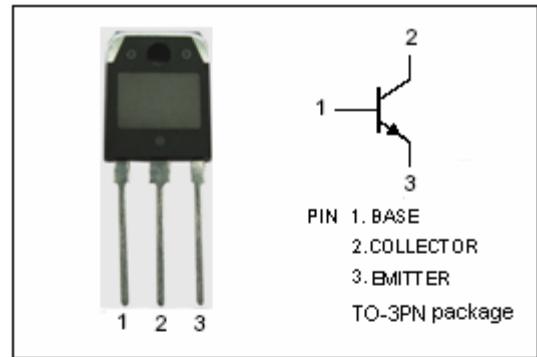
- Switching regulators
- Inverters
- Solenoids
- Relay drivers
- Motor controls
- Deflection circuits

**ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ C$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CEV}$	Collector-EmitterVoltage	1000	V
$V_{CEO}$	Collector-Emitter Voltage	500	V
$V_{EBO}$	Emitter-Base Voltage	6	V
$I_C$	Collector Current-Continuous	15	A
$I_{CM}$	Collector Current-Peak	20	A
$I_B$	Base Current	10	A
$I_{BM}$	Base Current-Peak	15	A
$P_c$	Collector Power Dissipation @ $T_c=25^\circ C$	135	W
$T_J$	Junction Temperature	150	°C
$T_{stg}$	Storage Temperature Range	-55~150	°C

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
$R_{th j-c}$	Thermal Resistance, Junction to Case	0.92	°C/W



DIM	mm	
	MIN	MAX
A	19.90	20.10
B	15.50	15.70
C	4.70	4.90
D	0.90	1.10
E	1.90	2.10
F	3.40	3.60
G	2.90	3.10
H	3.20	3.40
J	0.595	0.605
K	20.50	20.70
L	1.90	2.10
N	10.89	10.91
Q	4.90	5.10
R	3.35	3.45
S	1.995	2.005
U	5.90	6.10
Y	9.90	10.10

**isc Silicon NPN Power Transistor****MJW16010A****ELECTRICAL CHARACTERISTICS**T<sub>c</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>CEO(sus)</sub>	Collector-Emitter Sustaining Voltage	I <sub>C</sub> = 100mA ; I <sub>B</sub> =0	500			V
V <sub>CE(sat)-1</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 5A; I <sub>B</sub> = 1A			0.7	V
V <sub>CE(sat)-2</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 10A; I <sub>B</sub> = 2A I <sub>C</sub> = 10A; I <sub>B</sub> = 2A; T <sub>c</sub> =100°C			1.0 1.5	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = 10A; I <sub>B</sub> = 2A I <sub>C</sub> = 10A; I <sub>B</sub> = 2A; T <sub>c</sub> =100°C			1.5	V
I <sub>CEV</sub>	Collector Cutoff Current	V <sub>CEV</sub> =1000V; V <sub>BE(off)</sub> =1.5V V <sub>CEV</sub> =1000V; V <sub>BE(off)</sub> =1.5V; T <sub>c</sub> =100°C			0.15 1.0	mA
I <sub>CER</sub>	Collector Cutoff Current	V <sub>CE</sub> = 1000V; R <sub>BE</sub> = 50Ω ; T <sub>c</sub> =100°C			1.0	mA
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = 6V; I <sub>C</sub> =0			0.15	mA
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = 15A ; V <sub>CE</sub> = 5V	5	8		
C <sub>OB</sub>	Output Capacitance	I <sub>E</sub> = 0; V <sub>CB</sub> = 10V, f <sub>test</sub> = 1.0kHz			400	pF

Switching times; Resistive load(P<sub>w</sub>= 30 μ s; Duty Cycle≤2%)

t <sub>d</sub>	Delay Time	I <sub>C</sub> = 10A; I <sub>B1</sub> = 1.3A; I <sub>B2</sub> = 2.6A; R <sub>B2</sub> = 1.6Ω ; V <sub>CC</sub> = 250V			0.1	μ s
t <sub>r</sub>	Rise Time				0.6	μ s
t <sub>stg</sub>	Storage Time				3.0	μ s
t <sub>f</sub>	Fall Time				0.4	μ s