TOSHIBA Field Effect Transistor Silicon P Channel MOS Type ($L^2-\pi$ -MOSIV)

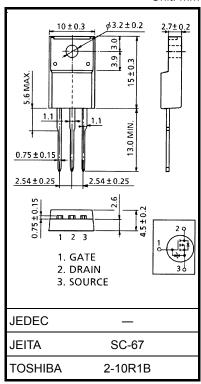
2SJ304

DC–DC Converter, Relay Drive and Motor Drive Applications

- 4-V gate drive
- Low drain-source ON resistance $R_{DS}(ON) = 80 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance $|Y_{fs}| = 8.0 \text{ S} (typ.)$
- Low leakage current $: I_{DSS} = -100 \ \mu A \ (max) \ (V_{DS} = -60 \ V)$
- Enhancement mode $: V_{th} = -0.8 \text{ to } -2.0 \text{ V} (V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA})$

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	-60	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V _{DGR}	-60	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	۱ _D	-14	А	
	Pulse(Note 1)	I _{DP}	-56	~	
Drain power dissipation (Tc = 25°C)		PD	40	W	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

Absolute Maximum Ratings (Ta = 25°C)



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch−c)}	3.125	°C / W
Thermal resistance, channel to ambient	R _{th (ch−a)}	62.5	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

This transistor is an electrostatic-sensitive device. Please handle with caution. Unit: mm

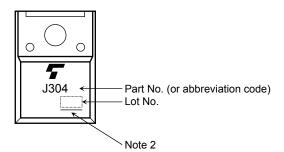
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cu	ırrent	I _{GSS}	V_{GS} = ±16 V, V_{DS} = 0 V	_	_	±10	μA	
Drain cut-off cu	rrent	IDSS	V_{DS} = -60 V, V_{GS} = 0 V		_	-100	μA	
Drain-source br voltage	eakdown	V (BR) DSS	I _D = -10 mA, V _{GS} = 0 V	-60	_	_	V	
Gate threshold v	voltage	V _{th}	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$	-0.8	_	-2.0	V	
		_	V _{GS} = -4 V, I _D = -5 A		130	190		
Drain-source ON resistance		R _{DS (ON)}	V _{GS} = -10 V, I _D = -7 A		80	120	mΩ	
Forward transfe	r admittance	Y _{fs}	V _{DS} = -10 V, I _D = -7 A	5.0	8.0		S	
Input capacitance	æ	C _{iss}			1200			
Reverse transfer capacitance		C _{rss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz	_	220	_	pF	
Output capacitance		C _{oss}		_	550	_		
Switching time	Rise time	tr	$V_{GS} \stackrel{0V}{\xrightarrow{-10V}} \stackrel{I_{D} = -7A}{} \stackrel{V_{OUT}}{} \stackrel{V_{OUT}}{} \stackrel{I_{D} = -7A}{} \stackrel{V_{OUT}}{} \stackrel{R_{L} = }{} \stackrel{A.3\Omega}{} \stackrel{V_{DD} = -30V}$	-	20	-		
	Turn-on time	t _{on}			30		. ns	
	Fall time	t _f			25			
	Turn-off time	t _{off}	Duty $\leq 1\%$, t _w =10 μ s	—	100	—		
Total gate charge (Gate-source plus gate-drain)		Qg		_	45	_	nC	
Gate-source charge		Q _{gs}	V _{DD} ≈ −48 V, V _{GS} = −10 V, I _D = −14 A	—	30	_		
Gate-drain ("miller") charge		Q _{gd}]		15	—		

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	-14	A
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	-56	A
Forward voltage (diode)	V _{DSF}	I _{DR} = -14 A, V _{GS} = 0 V	-	_	1.7	V
Reverse recovery time	t _{rr}	I _{DR} = -14 A, V _{GS} = 0 V	_	110	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} / dt = 50 A / μs		0.18	-	μC

Marking

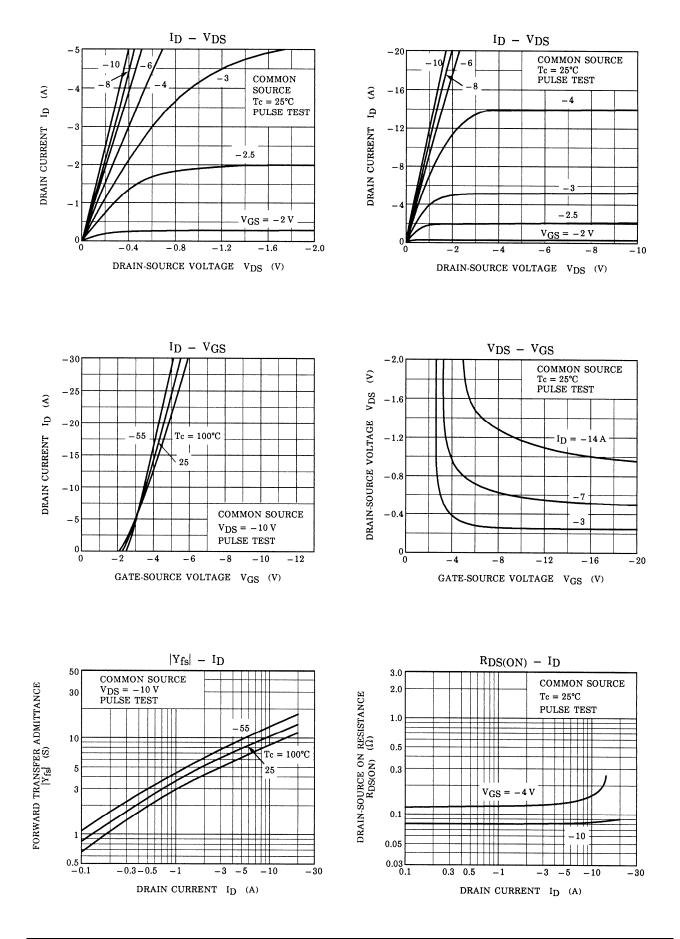


Note 2: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV

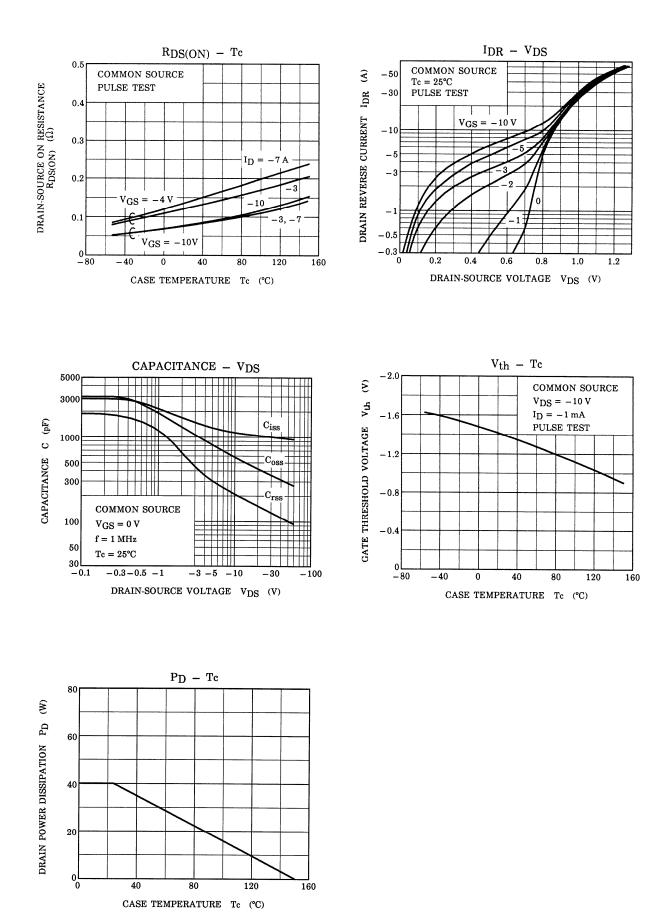
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

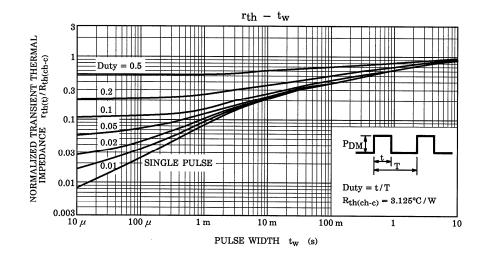
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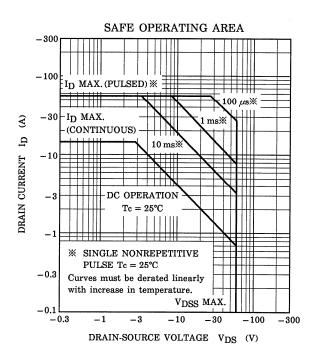
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