



CHENMKO ENTERPRISE CO.,LTD

Lead free devices

SURFACE MOUNT

SCHOTTKY BARRIER RECTIFIER

VOLTAGE RANGE 70 - 100 Volts CURRENT 4.0 Amperes

SPL470CTPT

THRU

SPL4100CTPT

PROVISIONAL SPEC.

APPLICATION

- * DC to DC Converters
- * Switch- Mode Power Supplies
- * Notebook PC

FEATURE

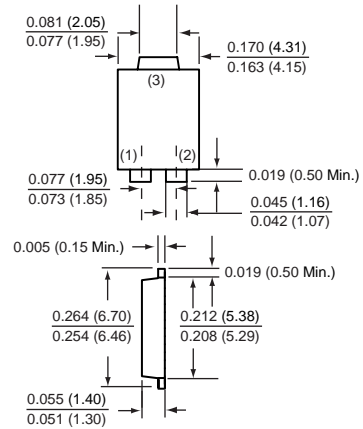
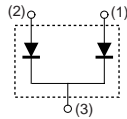
- * Small Surface Mounting Type. (SMP)
- * Low Power Loss, High Efficiency
- * Low Forward Voltage Drop
- * Peak Forward Surge Current Is 50A.
- * Schottky Diode Array

WEIGHT

MARKING

SMP

CIRCUIT



Dimensions in inches and (millimeters)

SMP

MAXIMUM RATINGS (At TA = 25°C unless otherwise noted)

RATINGS	SYMBOL	SPL470CTPT	SPL480CTPT	SPL490CTPT	SPL4100CTPT	UNITS
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	70	80	90	100	Volts
Maximum RMS Voltage	V _{RMS}	49	56	63	70	Volts
Maximum DC Blocking Voltage	V _{DC}	70	80	90	100	Volts
Maximum Average Forward Rectified Current	I _O	4.0				Amps
Peak Forward Surge Current 8.3 ms single half sine-wave superimposed on rated load (JEDEC method)	I _{FSM}	50				Amps
Typical Junction Capacitance (Note 2)	C _J	110				pF
Typical Thermal Resistance (Note 1)	R _{θJL}	17				°C / W
Operating and Storage Temperature Range	T _J ,T _{STG}	-65 to +125				°C

ELECTRICAL CHARACTERISTICS (At TA = 25°C unless otherwise noted)

CHARACTERISTICS	SYMBOL	SPL470CTPT	SPL480CTPT	SPL490CTPT	SPL4100CTPT	UNITS
Maximum Instantaneous Forward Voltage at 2.0 A DC	V _F	0.75		0.80		Volts
Maximum Average Reverse Current at Rated DC Blocking Voltage	@ TA = 25°C	0.5				mAmps
	@ TA = 100°C	10				mAmps

NOTES : 1. Thermal Resistance (Junction to Lead) : PC Board Mounted on 0.2 X 0.2" (5 X 5mm) copper pad area.
2. Measured at 1.0 MHz and applied reverse voltage of 4.0 volts.

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RATING CHARACTERISTIC CURVES (SPL470CTPT THRU SPL4100CTPT)

FIG. 1 - TYPICAL FORWARD CURRENT DERATING CURVE

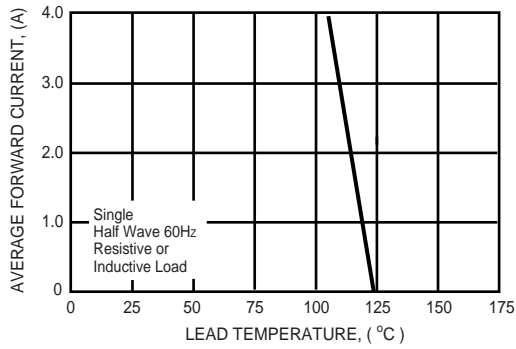


FIG. 2 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

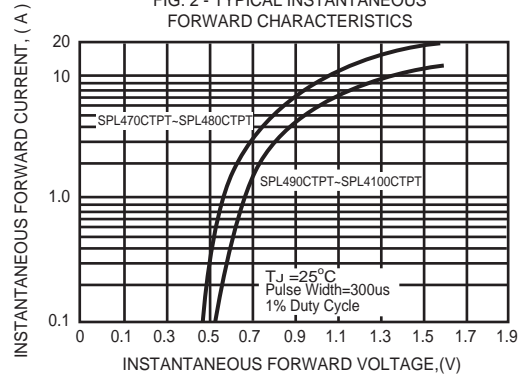


FIG. 3 - TYPICAL REVERSE CHARACTERISTICS

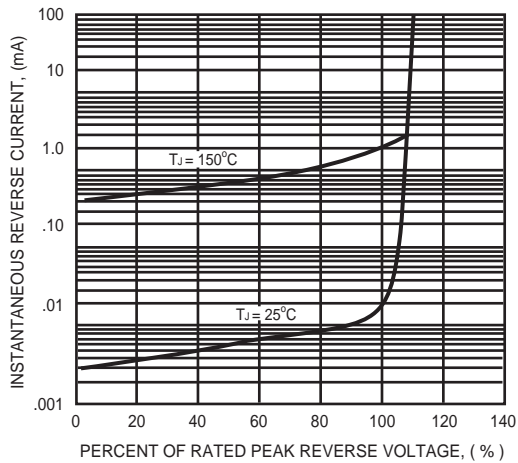


FIG. 4 - MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

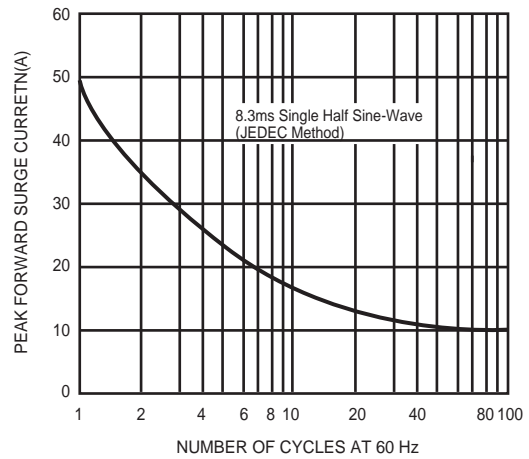


FIG. 5 - TYPICAL JUNCTION CAPACITANCE

