



CHENMKO ENTERPRISE CO.,LTD

CHEMB2PT

SURFACE MOUNT

Dual Digital Silicon Transistor

VOLTAGE 50 Volts CURRENT 30 mAmpere

Lead free devices

APPLICATION

* Switching circuit, Inverter, Interface circuit, Driver circuit.

FEATURE

- * Small surface mounting type. (SOT-563)
- * High current gain.
- * Suitable for high packing density.
- * Low collector-emitter saturation.
- * High saturation current capability.
- * Two CHDTA144E chips in a package.
- * Built in bias resistor(R1=47kΩ, Typ.)

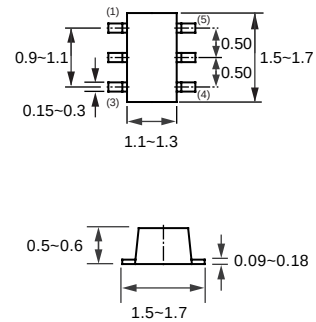
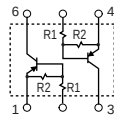
MARKING

* B2



SOT-563

CIRCUIT



Dimensions in millimeters

SOT-563

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CC}	Supply voltage		—	-50	V
V _{IN}	Input voltage		-40	+10	V
I _O	DC Output current		—	-30	mA
I _{C(Max.)}			—	-100	
P _{TOT}	Total power dissipation	T _{amb} ≤ 25 °C, Note 1	—	150	mW
T _{STG}	Storage temperature		-55	+150	°C
T _J	Junction temperature		—	150	°C
R _{θJ-S}	Thermal resistance	junction - soldering point	—	140	°C/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

RATING CHARACTERISTIC (CHEMB2PT)

CHARACTERISTICS

T_{amb} = 25 °C unless otherwise specipied.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{loff}	Input off voltage	I _o =-100uA; V _{cc} =-5.0V	-0.5	–	–	V
V _{l(on)}	Input on voltage	I _o =-5mA; V _o =-0.2V	–	–	-3.0	V
V _{o(on)}	Output voltage	I _o =-10mA; I _i =-0.5mA	–	-0.1	-0.3	V
I _i	Input current	V _i =-5V	–	–	-0.18	mA
I _{c(off)}	Output current	V _i =0V; V _{cc} =-50V	–	–	-0.5	uA
h _{FE}	DC current gain	I _o =-5mA; V _o =-5.0V	68	–	–	
R ₁	Input resistor		32.9	47	61.1	KΩ
R ₂ /R ₁	Resistor ratio		0.8	1.0	1.2	
f _T	Transition frequency	I _c =-5mA, V _{CE} =-10.0V f =100MHz	–	250	–	MHz

Note

1.Pulse test: t_p≤300uS; δ≤0.02.

RATING CHARACTERISTIC CURVES (CHEMB2PT)

Typical Electrical Characteristics

Fig.1 Input voltage vs. output current (ON characteristics)

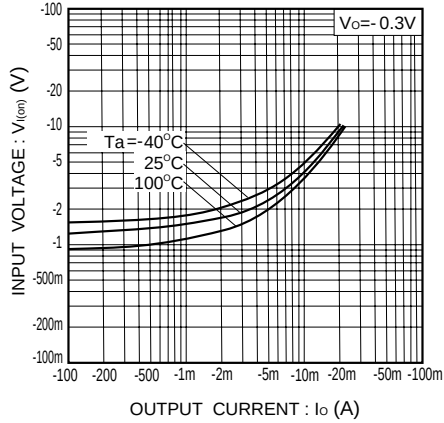


Fig.2 Output current vs. input voltage (OFF characteristics)

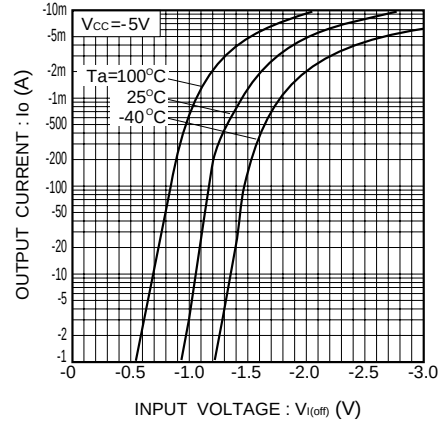


Fig.3 DC current gain vs. output current

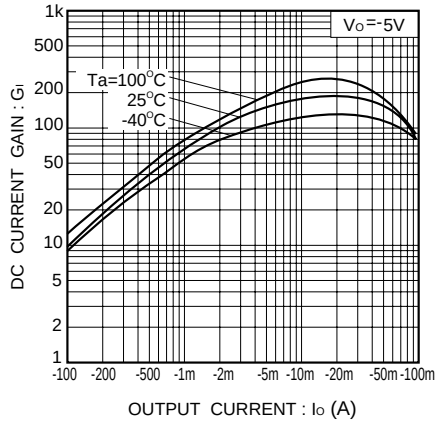


Fig.4 Output voltage vs. output current

