

# Strap Type, 12 V

### Standard

UL 1434 1st Edition CSA C22.2 No. 0 CSA TIL No. CA-3A

### **Approvals**

cULus Recognition ΤÜV

### **Features**

The new VLD product series feature a slim, low profile and low resistance design. These devices are ideal to install directly onto the latest generation batteries including cylindrical and prismatic cells. The VLD product offers protection against both overcurrent and overtemperature fault conditions.

## **Specifications**

Packaging A small pack standard

### Materials

Insulating material: Polyester Tape Terminals:

Operating / Storage Temperature -40 °C to +85 °C (consider de-rating)

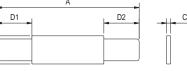
Humidity Ageing +60 °C, 95 % R.H., 1000 hours,  $\pm$  10 % typical resistance change

### Vibration

MIL-STD-883C, Condition A, no change

### Thermal Shock

MIL-STD-202F, Method 107G +85 °C to -40 °C 10 times ±5 % typical resistance change





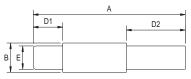


Figure 2

Α		"P", Part Code, identification, lot numbe
	<b>D</b> 0	r , ran Code, identification, for numbe
	D2	
		II n
		П

Dimensi	ons (mr	1)							
Model		Α	В	С	D1	D2	Е	packaging quantity	
	Fig	Min Max	Min Max		Min Max	Min Max	Min Max	small pack	standard
VLD170	1	20.8 23.2	3.5 3.9	0.8	4.5 6.5	4.5 6.5	2.4 2.6	500	10,000
VLD175L	2	29.3 31.7	2.9 3.3	0.8	5.2 6.8	10.0 12.5	2.4 2.6	500	10,000
VLD175XL	2	25.5 28.2	3.5 3.9	0.8	8.7 10.3	5.7 7.3	2.4 2.6	500	10,000
VLD230	1	20.9 23.1	4.9 5.3	0.8	4.1 5.8	4.1 5.8	3.9 4.1	500	10,000

Permissible	Permissible continuous operating current is ≤ 100 % at ambient temperature of 25 °C (77 °F).										
Model	I <sub>hold</sub> (A)	I <sub>Trip</sub> (A)	V <sub>max. dc</sub> (V)	I <sub>max.</sub> (A)	max. time to trip (s @ A)	P <sub>d max</sub> (W)		Resistanc R <sub>I max.</sub> ( )	e R <sub>I max.</sub> ( )	CURus CURus ) Approvals	
VLD170 VLD175L VLD175XL VLD230	1.70 1.75 1.75 2.30	4.10 4.20 4.20 5.00	12 12 12 12	100 100 100 100	5.0 @ 8.50 5.0 @ 8.75 5.0 @ 8.75 5.0 @ 10.0	1.40 1.40 1.40 2.50	0.018 0.017 0.017 0.012	0.032 0.031 0.031 0.018	0.064 0.062 0.062 0.036	• •	

Hold current: maximum current device will pass without tripping in 25 °C still air.

Trip current: minimum current at which the device will trip in 25  $^{\circ}$ C still air. Maximum voltage device can withstand without damage at rated current (I  $_{\rm m}$ 

Maximum fault current device can withstand without damage at rated voltage (V<sub>max</sub>)

Power dissipated from device when in the tripped state at 25 °C still air. Minimum resistance of device in initial (un-soldered) state.

Maximum resistance of device at 25  $^{\circ}\text{C}$  measured one hour after tripping for 20 s.

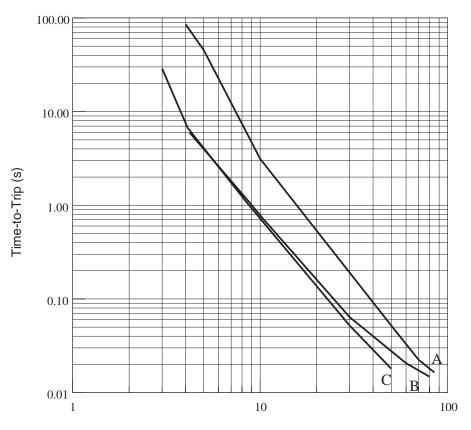
Caution:Operation beyond the specified rating may result in damage and possible arcing and flame. Specifications are subject to change without notice

Order Information

Ωty.	Order-	Model	Packaging
	Number		



## **VLD**



A: VLD170 B: VLD175 C: VLD230

Fault Current (A)

## **Thermal Derating Chart**

Model	Ambient	Ambient Operation Temperature - I <sub>hold</sub> (A)								
	-40 °C	-20 °C	0 °C	25 °C	40 °C	50 °C	60 °C	70 °C		
VLD170	3.50	2.90	2.40	1.70	1.20	1.00	0.70	0.30		
VLD175L	3.50	2.90	2.40	1.75	1.30	1.00	0.80	0.30		
VLD175XL	3.50	2.90	2.40	1.75	1.30	1.00	0.80	0.30		
VLD230	5.00	4.20	3.40	2.30	1.70	1.30	0.90	0.40		