M3H & MH Series

8 pin DIP, 5.0 or 3.3 Volt, HCMOS/TTL Clock Oscillator

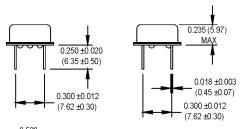


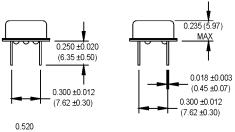


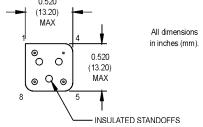




- 5.0 Volt or 3.3 Volt Versions
- RoHs Compliant Version available
- Low Jitter







Pin Connections

PIN	FUNCTION					
1	N/C or Tristate					
4	Circuit/Case Ground					
5	Output					
8	+Vdd					

Ordering Information	on							00.000
	M3H / MH	1	3	F	Α	D	-R	MHz
Product Series M3H = 3.3 Volt MH = 5.0 Volt								
Temperature Range —								
1: 0°C to +70°C								
	4: -55°C to +125°C							
5 : -10°C to +85°C	6: -20°C to +70°C							
7: 0°C to +85°C								
Stability———			_					
1: ±1000 ppm								
3 : ±100 ppm	4: ±50 ppm							
5 : ±35 ppm								
7 : +0/-200 ppm	*8: ±20 ppm							
Output Type ———								
F : Fixed	T: Tristate							
Symmetry/Logic Comp	oatibility———							
A: 40/60 HCMOS/TT	L B: 45/55 TTL (M	H ser	ies onl	y)				
	D: 45/55 HCMOS	5/ I I L	(MH to	o 50 M	Hz only	')		
Package/Lead Configu D: DIP; Nickel Heade	irations————————————————————————————————————	kel F	leader					
RoHS Compliance -								
Blank: non-RoHS c								
R: RoHS comp								
Frequency (customer	specified) ————							

	PARAMETER	Symbol	Min.	Тур.	Max.	Units	Condition/Notes
	Frequency Range	F	1.5		100	MHz	МЗН
			1.0		80	MHz	MH See Note 1
	Operating Temperature	TA	(See Ordering Information)				
	Storage Temperature	Ts	-55		+125	°C	
	Frequency Stability	∆F/F	(See Ordering Information)				
	Aging						
	1st Year			±3		ppm	
	Thereafter (per year)			±2		ppm	
	Input Voltage	Vdd	3.135	3.3	3.465	٧	МЗН
us			4.5	5.0	5.5	٧	MH
Electrical Specifications	Input Current (M3H)	ldd			25	mA	1.500 to 50.000 MHz
lice					35	mA	50.001 to 67.000 MHz
Seci					55	mA	67.001 to 100.000 MHz
Ş	Input Current (MH)	ldd			40	mA	1.000 to 40.000 MHz
rica					60	mA	40.001 to 80.000 MHz
ect	Output Type						HCMOS/TTL
╗	Load		2 TTL or 15 pF				M3H See Note 2
			10 TTL or 50 pF				мн
	Symmetry (Duty Cycle)		(See Ordering Information)				See Note 3
	Logic "1" Level	Voh	90% Vdd	90% Vdd		V	HCMOS Load
			Vdd -0.5			٧	TTL Load
	Logic "0" Level	Vol			10% Vdd	٧	HCMOS Load
					0.5	٧	TTL Load
	Output Current				± 4	mA	мзн
					±16	mA	MH
	Rise/Fall Time	Tr/Tf			10	ns	See Note 4
	Tristate Function		Input Logic "1" or floating; output active Input Logic "0"; output disables to high-Z				
	Start up Time			5		ms	
	Random Jitter	Rj		5	12	ps RMS	1-Sigma

MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.