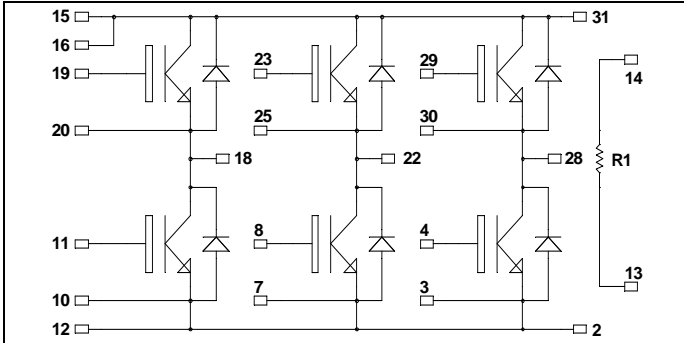
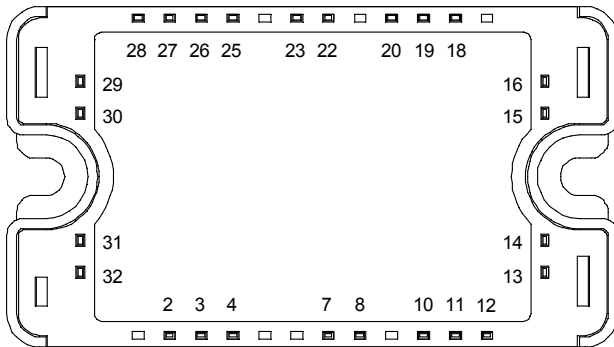


**3 Phase bridge
Trench + Field Stop IGBT4
Power Module**

**$V_{CES} = 1200V$
 $I_C = 30A @ T_c = 80^\circ C$**



It is recommended to connect a decoupling capacitor between pins 31 & 2 to reduce switching overvoltages, if DC Power is connected between pins 15, 16 & 12. Pins 15 & 16 must be shorted together.



Application

- Motor control

Features

- Trench + Field Stop IGBT 4 Technology
 - Low voltage drop
 - Low leakage current
 - Low switching losses
 - Low tail current
 - Soft recovery parallel diodes
 - Low diode VF
 - RBSOA and SCSOA rated
- Kelvin emitter for easy drive
- Very low stray inductance
- High level of integration
- Internal thermistor for temperature monitoring

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS compliant

Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
V_{CES}	Collector - Emitter Breakdown Voltage	1200	V
I_C	Continuous Collector Current	$T_C = 25^\circ C$	45
		$T_C = 80^\circ C$	30
I_{CM}	Pulsed Collector Current	$T_C = 25^\circ C$	50
V_{GE}	Gate - Emitter Voltage	± 20	V
P_D	Maximum Power Dissipation	$T_C = 25^\circ C$	170
RBSOA	Reverse Bias Safe Operating Area	$T_j = 150^\circ C$	50A @ 1100V

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

All ratings @ $T_j = 25^\circ\text{C}$ unless otherwise specified

Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
I_{CES}	Zero Gate Voltage Collector Current	$V_{GE} = 0\text{V}, V_{CE} = 1200\text{V}$			250	μA
$V_{CE(sat)}$	Collector Emitter saturation Voltage	$V_{GE} = 15\text{V}$ $I_C = 25\text{A}$	$T_j = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$	1.85 2.25	2.25	V
$V_{GE(th)}$	Gate Threshold Voltage	$V_{GE} = V_{CE}, I_C = 0.8\text{mA}$	5.0	5.8	6.5	V
I_{GES}	Gate – Emitter Leakage Current	$V_{GE} = 20\text{V}, V_{CE} = 0\text{V}$			400	nA

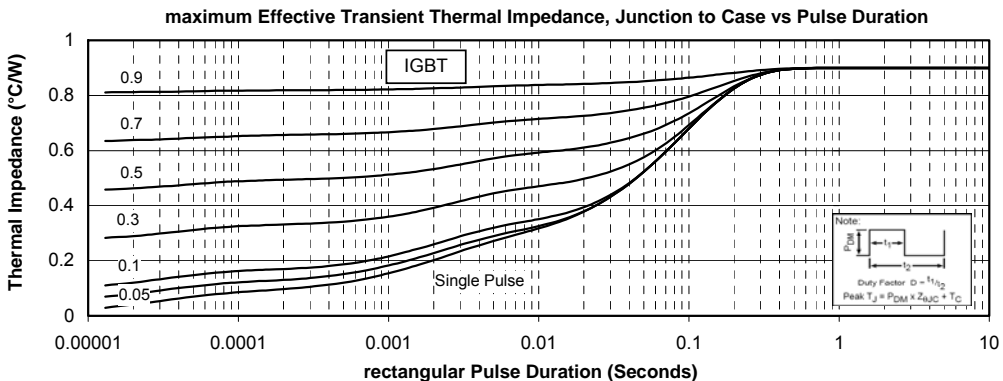
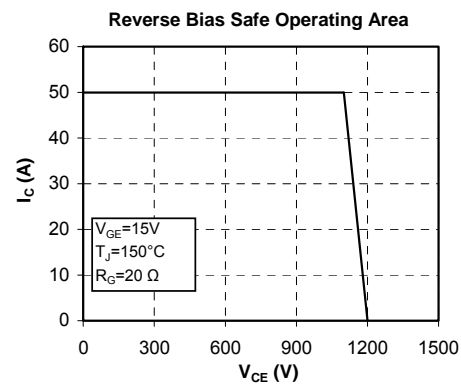
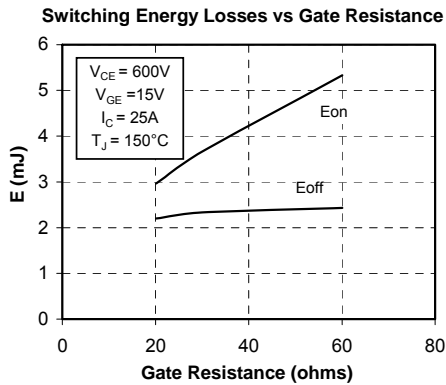
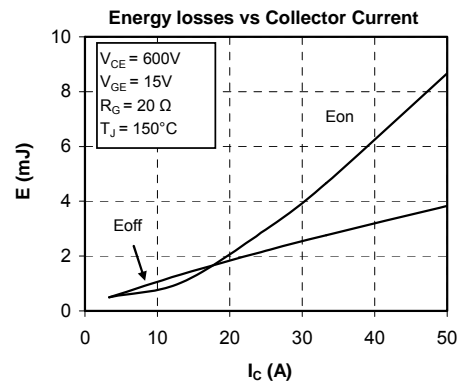
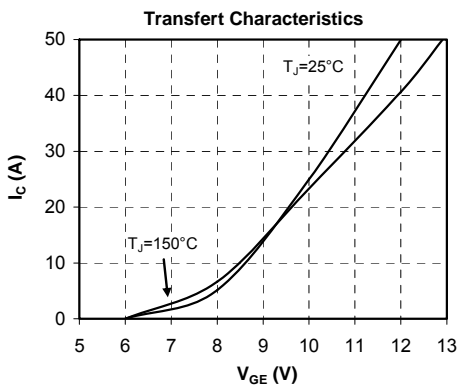
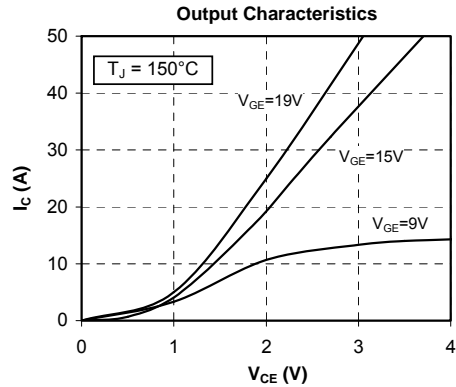
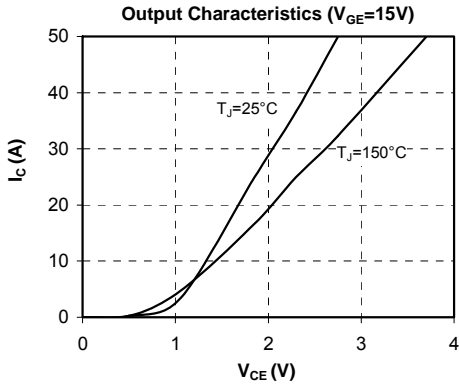
Dynamic Characteristics

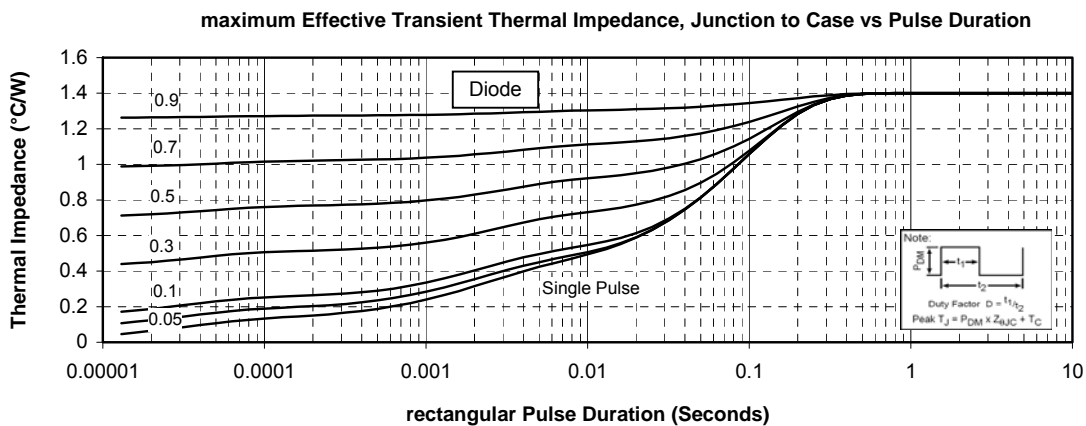
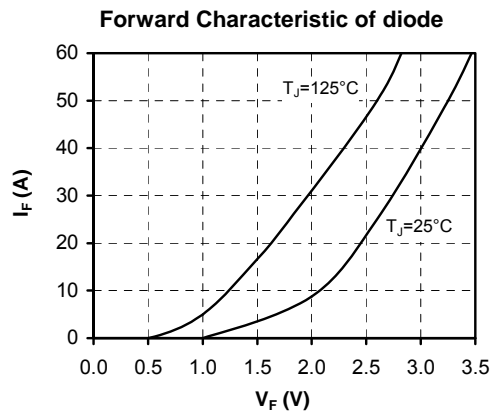
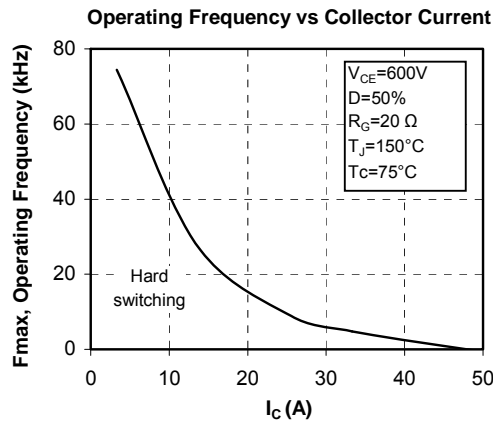
Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C_{ies}	Input Capacitance	$V_{GE} = 0\text{V}$		1430		pF
C_{oes}	Output Capacitance	$V_{CE} = 25\text{V}$		115		
C_{res}	Reverse Transfer Capacitance	$f = 1\text{MHz}$		85		
Q_G	Gate charge	$V_{GE} = \pm 15\text{V}; V_{CE} = 600\text{V}$ $I_C = 25\text{A}$		0.2		μC
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching (25°C) $V_{GE} = \pm 15\text{V}$ $V_{CE} = 600\text{V}$ $I_C = 25\text{A}$ $R_G = 20\Omega$		130		ns
T_r	Rise Time			20		
$T_{d(off)}$	Turn-off Delay Time			300		
T_f	Fall Time			45		
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching (150°C) $V_{GE} = \pm 15\text{V}$ $V_{CE} = 600\text{V}$ $I_C = 25\text{A}$ $R_G = 20\Omega$		150		ns
T_r	Rise Time			35		
$T_{d(off)}$	Turn-off Delay Time			350		
T_f	Fall Time			80		
E_{on}	Turn-on Switching Energy	$V_{GE} = \pm 15\text{V}$ $V_{CE} = 600\text{V}$ $I_C = 25\text{A}$	$T_j = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$	2 3		mJ
E_{off}	Turn-off Switching Energy	$R_G = 20\Omega$	$T_j = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$	1.5 2.2		mJ
I_{sc}	Short Circuit data	$V_{GE} \leq 15\text{V}; V_{Bus} = 900\text{V}$ $t_p \leq 10\mu\text{s}; T_j = 150^\circ\text{C}$		100		A

Reverse diode ratings and characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
V_{RRM}	Maximum Peak Repetitive Reverse Voltage		1200			V
I_{RM}	Maximum Reverse Leakage Current	$V_R = 1200\text{V}$	$T_j = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$		100 500	μA
I_F	DC Forward Current			25		A
V_F	Diode Forward Voltage	$I_F = 25\text{A}$ $I_F = 50\text{A}$ $I_F = 25\text{A}$	$T_j = 125^\circ\text{C}$	2.6 3.2 1.8	3.1	V
t_{rr}	Reverse Recovery Time	$I_F = 25\text{A}$ $V_R = 667\text{V}$ $di/dt = 200\text{A}/\mu\text{s}$	$T_j = 25^\circ\text{C}$ $T_j = 125^\circ\text{C}$	320 360		ns
Q_{rr}	Reverse Recovery Charge		$T_j = 25^\circ\text{C}$ $T_j = 125^\circ\text{C}$	480 1800		nC

Typical Performance Curve





Microsemi reserves the right to change, without notice, the specifications and information contained herein

Microsemi's products are covered by one or more of U.S. patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 6,939,743 7,352,045 5,283,201 5,801,417 5,648,283 7,196,634 6,664,594 7,157,886 6,939,743 7,342,262 and foreign patents. U.S. and Foreign patents pending. All Rights Reserved.