

# Single N-channel MOSFET

## ELM17400FA-S

### ■ General description

ELM17400FA-S uses advanced trench technology to provide excellent  $R_{ds(on)}$ , low gate charge and operation with gate voltages as low as 2.5V.

### ■ Features

- $V_{ds}=30V$
- $I_d=1.7A$  ( $V_{gs}=10V$ )
- $R_{ds(on)} < 85m\Omega$  ( $V_{gs}=10V$ )
- $R_{ds(on)} < 100m\Omega$  ( $V_{gs}=4.5V$ )
- $R_{ds(on)} < 140m\Omega$  ( $V_{gs}=2.5V$ )

### ■ Maximum absolute ratings

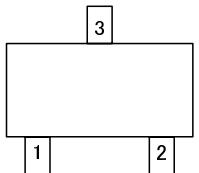
Parameter	Symbol	Limit	Unit	Note
Drain-source voltage	$V_{ds}$	30	V	
Gate-source voltage	$V_{gs}$	$\pm 12$	V	
Continuous drain current Ta=25°C	$I_d$	1.7	A	1
Ta=70°C		1.3		
Pulsed drain current	$I_{dm}$	10	A	2
Power dissipation Ta=25°C	$P_d$	0.35	W	1
Ta=70°C		0.22		
Junction and storage temperature range	$T_j, T_{stg}$	-55 to 150	°C	

### ■ Thermal characteristics

Parameter		Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	t≤10s	$R_{\theta ja}$	300	360	°C/W	1
Maximum junction-to-ambient	Steady-state		340	425	°C/W	
Maximum junction-to-lead	Steady-state	$R_{\theta jl}$	280	320	°C/W	3

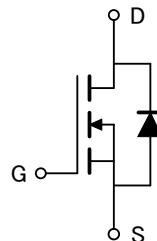
### ■ Pin configuration

SC-70 (TOP VIEW)



Pin No.	Pin name
1	GATE
2	SOURCE
3	DRAIN

### ■ Circuit



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### ■ Electrical characteristics

T<sub>a</sub>=25°C

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
<b>STATIC PARAMETERS</b>						
Drain-source breakdown voltage	BVdss	Id=250 μA, Vgs=0V	30			V
Zero gate voltage drain current	Idss	Vds=24V			1	μ A
		Vgs=0V	T <sub>j</sub> =55°C		5	
Gate-body leakage current	Igss	Vds=0V, Vgs=±12V			100	nA
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=250 μ A	0.6	1.0	1.4	V
On state drain current	Id(on)	Vgs=4.5V, Vds=5V	10			A
Static drain-source on-resistance	Rds(on)	Vgs=10V		70	85	m Ω
		Id=1.5A	T <sub>j</sub> =125°C	100	125	
		Vgs=4.5V, Id=1.5A		81	100	m Ω
		Vgs=2.5V, Id=1A		114	140	m Ω
Forward transconductance	Gfs	Vds=5V, Id=1.5A		4		S
Diode forward voltage	Vsd	Is=1A, Vgs=0V		0.81	1.00	V
Max. body-diode continuous current	Is				0.5	A
<b>DYNAMIC PARAMETERS</b>						
Input capacitance	Ciss	Vgs=0V, Vds=15V, f=1MHz		390.0		pF
Output capacitance	Coss			54.5		pF
Reverse transfer capacitance	Crss			41.0		pF
Gate resistance	Rg	Vgs=0V, Vds=0V, f=1MHz		3		Ω
<b>SWITCHING PARAMETERS</b>						
Total gate charge	Qg	Vgs=4.5V, Vds=15V, Id=1.7A		4.82		nC
Gate-source charge	Qgs			0.62		nC
Gate-drain charge	Qgd			1.58		nC
Turn-on delay time	td(on)	Vgs=10V, Vds=15V R <sub>l</sub> =10 Ω, R <sub>gen</sub> =3 Ω		2.5		ns
Turn-on rise time	tr			2.3		ns
Turn-off delay time	td(off)			22.0		ns
Turn-off fall time	tf			3.0		ns
Body diode reverse recovery time	trr	I <sub>f</sub> =1.7A, dI/dt=100A/μ s		10.0		ns
Body diode reverse recovery charge	Qrr	I <sub>f</sub> =1.7A, dI/dt=100A/μ s		3.6		nC

### NOTE :

- The value of R<sub>θja</sub> is measured with the device mounted on 1in<sup>2</sup> FR-4 board of 2oz. Copper, in still air environment with T<sub>a</sub>=25°C. The value in any given applications depends on the user's specific board design, The current rating is based on the t ≤ 10s thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The R<sub>θja</sub> is the sum of the thermal impedance from junction to lead R<sub>θjl</sub> and lead to ambient.
- The static characteristics in Figures 1 to 6 are obtained using 80μs pulses, duty cycle 0.5%max.
- These tests are performed with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>a</sub>=25°C. The SOA curve provides a single pulse rating.

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## ■ Typical electrical and thermal characteristics

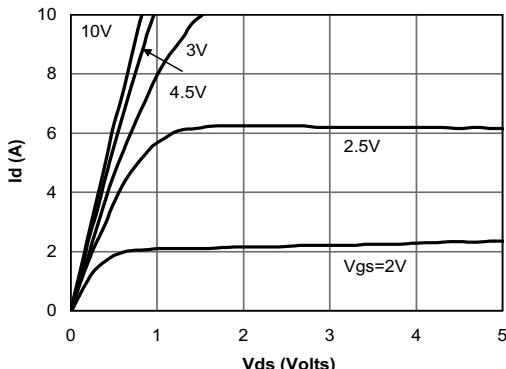


Fig 1: On-Region Characteristics

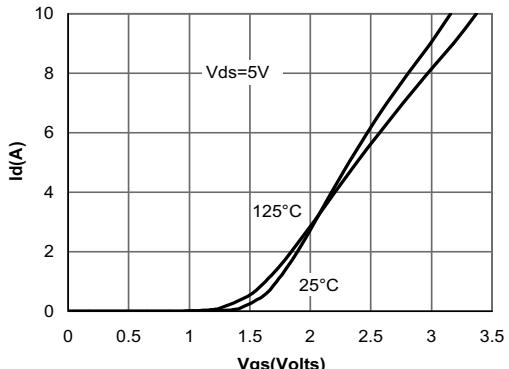


Figure 2: Transfer Characteristics

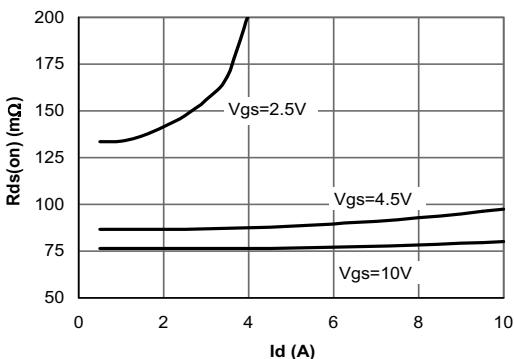


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

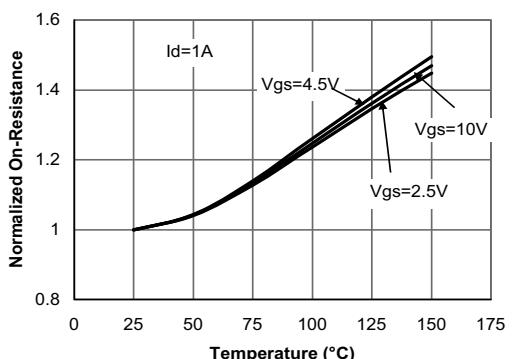


Figure 4: On-Resistance vs. Junction Temperature

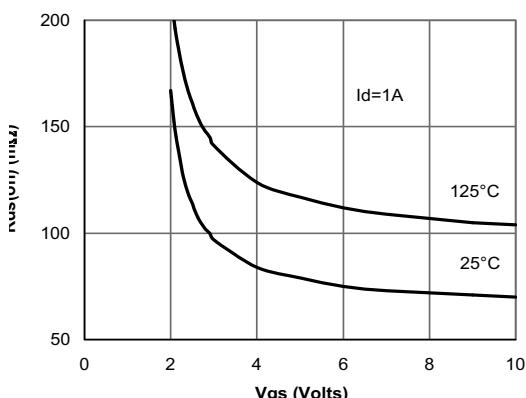


Figure 5: On-Resistance vs. Gate-Source Voltage

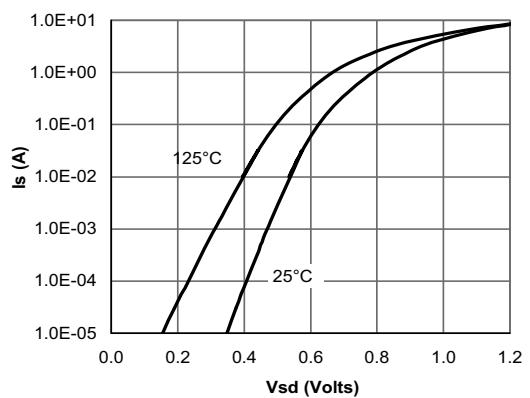


Figure 6: Body-Diode Characteristics

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