

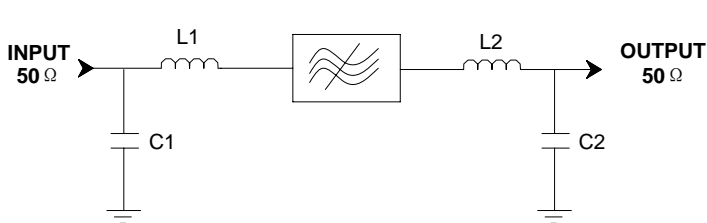
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	73.92	74	74.08
Insertion Loss	dB	-	27.4	29
1 dB Bandwidth	MHz	3.7	3.72	-
3 dB Bandwidth	MHz	-	3.95	-
30 dB Bandwidth	MHz	-	4.45	4.5
40 dB Bandwidth	MHz	-	4.51	5
Passband Variation	dB	-	0.7	0.9
Absolute Delay	usec	-	4.48	-
Group Delay Variation($f_0 \pm 1.8\text{MHz}$)	nsec	-	100	300
Ultimate Rejection(Outside $f_0 \pm 3\text{MHz}$)	dB	45	50	-
Material Temperature coefficient	KHz/°C	0.074		
Ambient Temperature	°C	25		
Package Size	DIP3512 (35.0x12.8x4.7mm3)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



L1=280nH L2=270nH
C1=C2=56pF
Source/Load Impedance=50 ohm

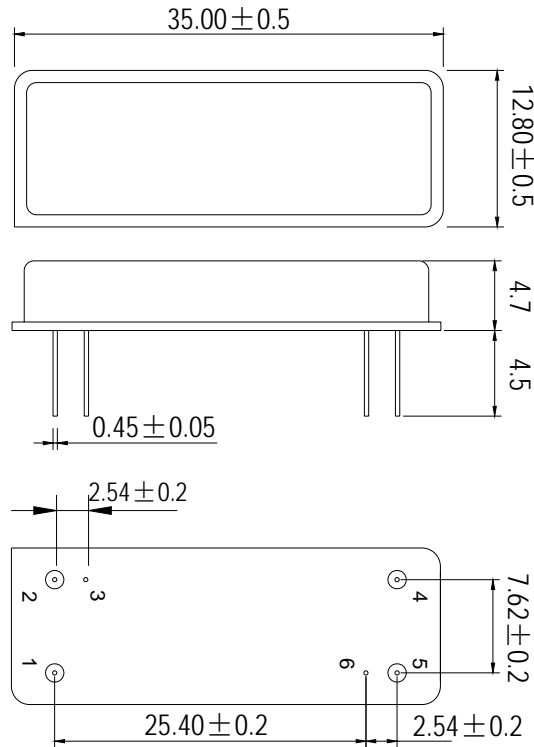
Notes - Component values may change depending on board layout.



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Part Number	LBS07405		
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Package Dimension



Input	1
Output	5
Ground	2,3,4,6

Package: DIP3512

Unit: mm

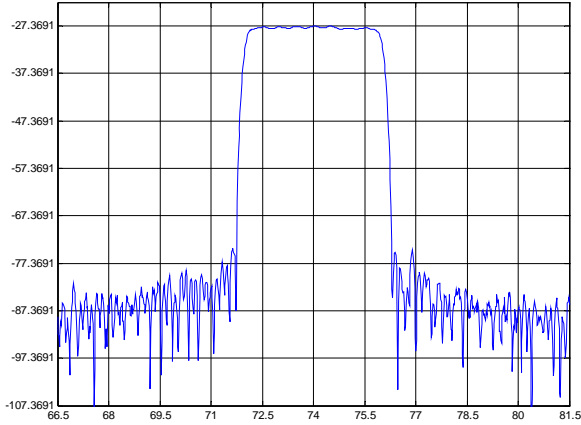


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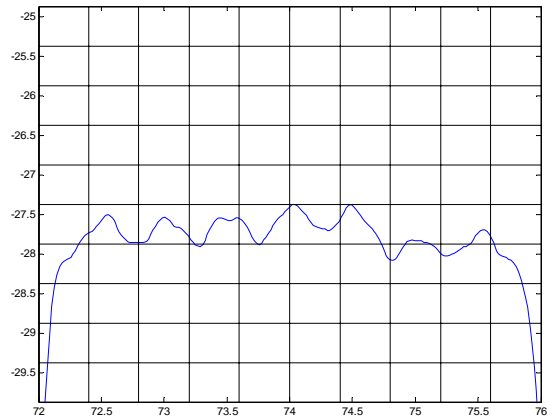
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Typical Performance

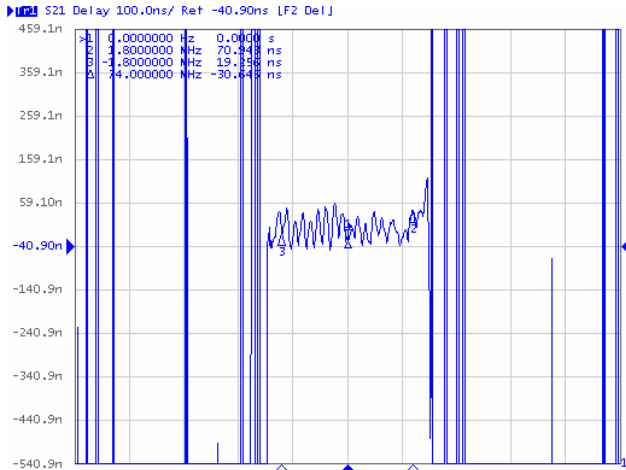
Frequency Respond



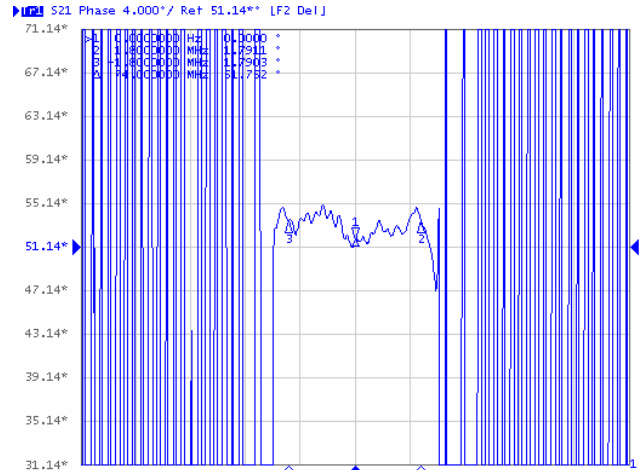
Passband Respond



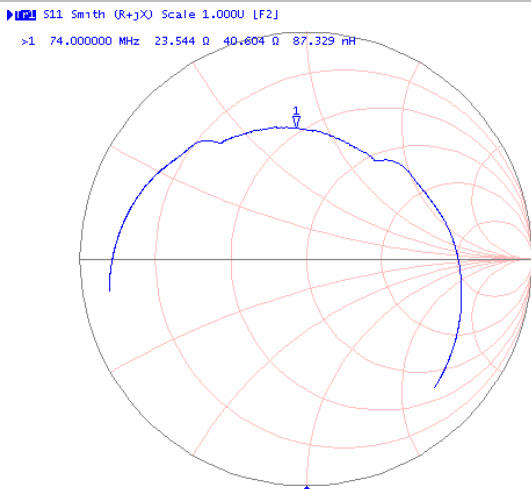
Group Delay Variation(f0±1.8MHz)



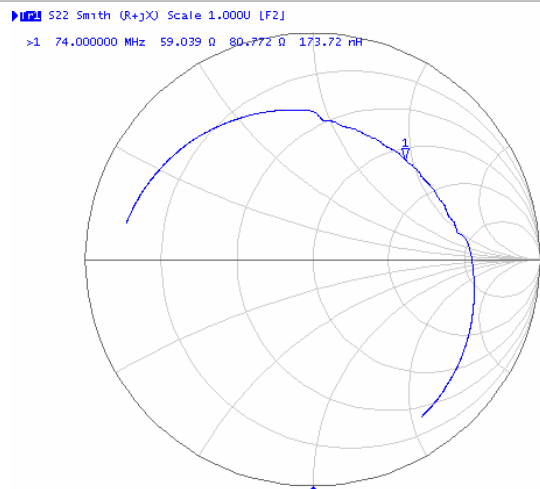
Phase Linearity(f0±1.8MHz)



Smith Chart S11



Smith Chart S22



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