

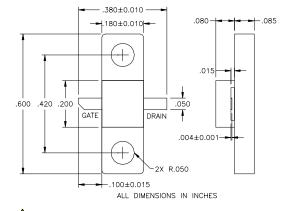
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EPA680AV-180F

High Efficiency Heterojunction Power FET

FEATURES

- Non-Hermetic 180mil Metal Flange Package
- +36.5 dBm Typical Output Power
- 17.5 dB Typical Power Gain at 2GHz •
- 0.4 x 6800 Micron Recessed "Mushroom" Gate .
- Si₃N₄ Passivation
- **Advanced Epitaxial Heterojunction Profile Provides Extra High Power Efficiency and High Reliability**



Caution! ESD sensitive device.

ELECTRICAL CHARACTERISTICS ($T_a = 25^{\circ}C$)

SYMBOL	PARAMETERS/TEST CONDITIONS ¹		MIN	ТҮР	MAX	UNITS	
P_{1dB}	Output Power at 1dB Compress		35.0	36.5		dBm	
	V _{DS} = 8 V, I _{DS} ≈ 50% I _{DSS}	f = 4GHz		36.5			
G _{1dB}	Gain at 1dB Compression	f = 2GHz	16.0	17.5	dB	dB	
	V _{DS} = 8 V, I _{DS} ≈ 50% I _{DSS}	f = 4GHz		12.5		UD	
PAE	Power Added Efficiency at 1dB Compression			45		%	
	V _{DS} = 8 V, I _{DS} ≈ 50% I _{DSS}	f = 2GHz					
I _{DSS}	Saturated Drain Current	V_{DS} = 3 V, V_{GS} = 0 V	1250	2050	2690	mA	
G _M	Transconductance	V_{DS} = 3 V, V_{GS} = 0 V	1360	2150		mS	
VP	Pinch-off Voltage	V_{DS} = 3 V, I_{DS} = 20 mA		-1.0	-2.5	V	
\mathbf{BV}_{GD}	Drain Breakdown Voltage	I _{GD} = 6.8 mA	-13	-15		V	
BV _{GS}	Source Breakdown Voltage	I _{GS} = 6.8 mA	-7	-14		V	
R _{TH}	Thermal Resistance			7*		°C/W	

* Overall Rth depends on case mounting.

MAXIMUM RATINGS AT 25°C

SYMBOLS	PARAMETERS		CONTINUOUS ²	
Vds	Drain-Source Voltage	12V	8V	
Vgs	Gate-Source Voltage	-5V	-3V	
lgsf	Forward Gate Current	30.6 mA	10.2 mA	
lgsr	Reversed Gate Current	-5.1 mA	-1.7 mA	
Pin	Input Power	33.5 dBm	@ 3dB Compression	
Tch	Channel Temperature	175°C	175°C	
Tstg	Storage Temperature	-65/175°C	-65/175°C	
Pt	Total Power Dissipation	21 W	21 W	

Note: 1. Exceeding any of the above ratings may result in permanent damage.

2. Exceeding any of the above ratings may reduce MTTF below design goals.

Specifications are subject to change without notice. Excelics Semiconductor, Inc. 310 De Guigne Drive, Sunnyvale, CA 94085 Phone: 408-737-1711 Fax: 408-737-1868 Web: www.excelics.com