Vishay Semiconductors

Standard SMD LED PLCC2



94 8553

DESCRIPTION

This device has been designed for applications requiring narrow brightness and color selection.

The package of this device is the PLCC-2.

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled up with clear epoxy.

PRODUCT GROUP AND PACKAGE DATA

- Product series: LED
- Package: SMD PLCC-2
- Product group: standard
- Angle of half intensity: ± 60°

FEATURES

- · Lead (Pb)-free product RoHS compliant
- · SMD LED with exceptional brightness
- Luminous intensity categorized
- · Compatible with automatic placement equipment
- · EIA and ICE standard package
- · Compatible with infrared, vapor phase and wave solder processes according to CECC
- Available in 8 mm tape
- Low profile package
- · Non-diffused lens: excellent for coupling to light pipes and backlighting
- Low power consumption
- · Luminous intensity ratio in one packaging unit $I_{Vmax}/I_{Vmin} \le 1.6$
- · Lead (Pb)-free device
- Preconditioning: acc. to JEDEC level 2 a

APPLICATIONS

- Automotive: backlighting in dashboards and switches
- · Telecommunication: indicator and backlighting in telephone and fax
- · Indicator and backlight for audio and video equipment
- · Indicator and backlight in office equipment
- Flat backlight for LCDs, switches and symbols
- General use

PARTS TABLE		
PART	COLOR, LUMINOUS INTENSITY	TECHNOLOGY
VLMY3191-GS08	Yellow, $I_V = (7.1 \text{ to } 18) \text{ mcd}$	GaAsP on GaP
VLMY3191-GS18	Yellow, $I_V = (7.1 \text{ to } 18) \text{ mcd}$	GaAsP on GaP

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VLMY3191

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ABSOLUTE MAXIMUM RATINGS ¹⁾ VLMY3191				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage ²⁾		V _R	6	V
DC Forward current	$T_{amb} \le 60 \ ^{\circ}C$	١ _F	30	mA
Surge forward current	$t_p \le 10 \ \mu s$	I _{FSM}	0.5	A
Power dissipation	$T_{amb} \le 60 \ ^{\circ}C$	P _V	100	mW
Junction temperature		Тj	100	°C
Operating temperature range		T _{amb}	- 40 to + 100	°C
Storage temperature range		T _{stg}	- 55 to + 100	°C
Soldering temperature	t ≤ 5 s	T _{sd}	260	°C
Thermal resistance junction/ ambient	mounted on PC board (pad size > 16 mm ²)	R _{thJA}	400	K/W

Note:

1) $T_{amb} = 25$ °C, unless otherwise specified

2) Driving the LED in reverse direction is suitable for short term application

OPTICAL AND ELECTRICAL CHARACTERISTICS ¹⁾ VLMY3191, YELLOW							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN	TYP.	MAX	UNIT
Luminous intensity	I _F = 10 mA	VLMY 3191	Ι _V	7.1		18	mcd
Dominant wavelength	I _F = 10 mA		λ_d	583	588	592	nm
Peak wavelength	I _F = 10 mA		λ _p		590		nm
Angle of half intensity	I _F = 10 mA		φ		± 60		deg
Forward voltage	I _F = 20 mA		V _F		2.4	3.0	V
Reverse voltage	I _R = 10 μA		V _R	6	15		V
Junction capacitance	V _R = 0, f = 1 MHz		Cj		15		pF

Note:

1) $T_{amb} = 25 \degree C$, unless otherwise specified

LUMINOUS INTENSITY CLASSIFICATION			
GROUP	GROUP LUMINOUS INTENSITY (MCD)		
	MIN.	MAX.	
K1	7.10	9.00	
K2	9.00	11.20	
L1	11.20	14.00	
L2	14.00	18.00	

Note:

Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of \pm 11 %.

The above Type Numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel). In order to ensure availability, single brightness groups will not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel.

In order to ensure availability, singe wavelength groups will not be orderable.

COLOR CLASSIFICATION		
GROUP	DOMINANT WAY	VELENGTH (NM)
	YELLOW	
	MIN.	MAX.
1	581	584
2	583	586
3	585	588
4	587	590
5	589	592
6	591	594



TYPICAL CHARACTERISTICS

 T_{amb} = 25 °C, unless otherwise specified

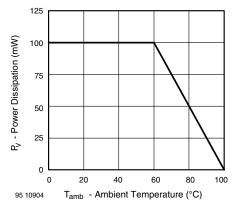


Figure 1. Power Dissipation vs. Ambient Temperature

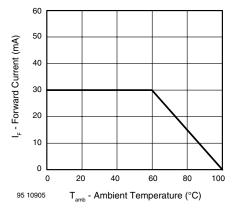


Figure 2. Forward Current vs. Ambient Temperature

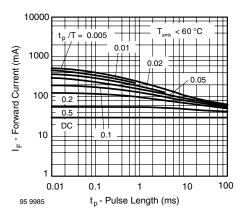


Figure 3. Forward Current vs. Pulse Length

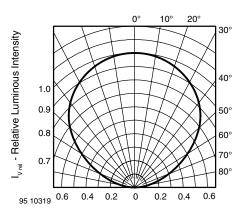


Figure 4. Rel. Luminous Intensity vs. Angular Displacement

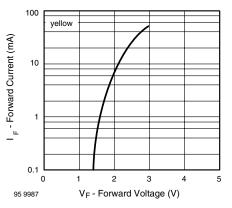


Figure 5. Forward Current vs. Forward Voltage

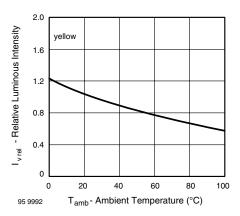


Figure 6. Rel. Luminous Intensity vs. Ambient Temperature

VLMY3191

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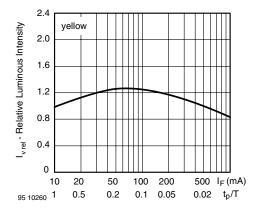


Figure 7. Rel. Lumin. Intensity vs. Forw. Current/Duty Cycle

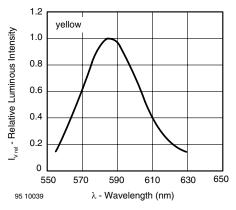


Figure 9. Relative Luminous Intensity vs. Wavelength

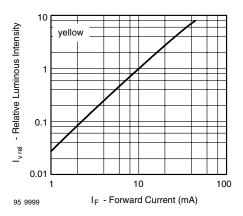
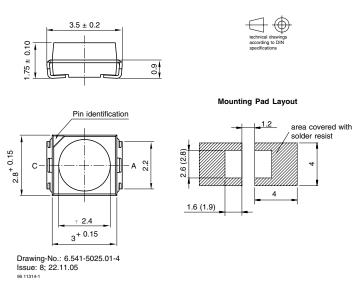


Figure 8. Relative Luminous Intensity vs. Forward Current

PACKAGE DIMENSIONS in millimeters



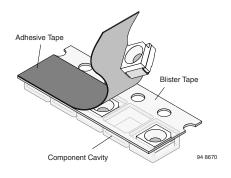


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METHOD OF TAPING/POLARITY AND TAPE AND REEL

SMD LED (VLM.3 - SERIES)

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertation. The blister tape is a plastic strip with impressed component cavaties, covered by a top tape.



TAPING OF VLM.3...

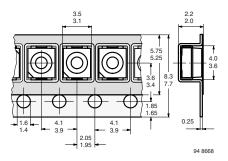


Figure 10. Tape Dimensions in mm for PLCC-2

REEL PACKAGE DIMENSION IN MM FOR SMD LEDS, TAPE OPTION GS08 (=1500 PCS.)

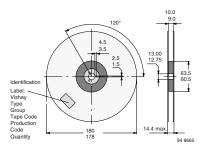


Figure 11. Reel Dimensions - GS08

REEL PACKAGE DIMENSION IN MM FOR SMD LEDS, TAPE OPTION GS18 (=8000 PCS.) PREFERRED

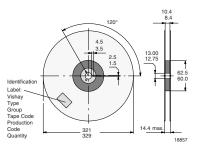


Figure 12. Reel Dimensions - GS18

SOLDERING PROFILE

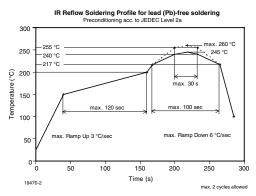


Figure 13. Vishay Lead (Pb) -free Reflow Soldering Profile (acc. to J-STD-020B)

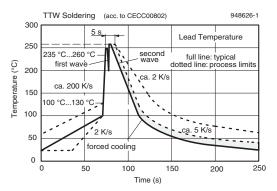
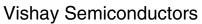
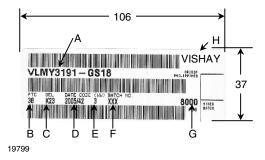


Figure 14. Double Wave Soldering of Opto Devices (all packages)





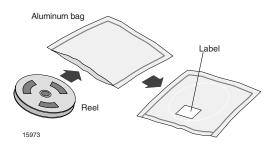
BAR CODE PRODUCT LABEL



- A) Type of componenent
- B) Manufacturing plant
- C) SEL selection code (bin):
 - e.g.: K2 = code for luminous intensity group 3 = code for color group
- D) Date code year/week
- E) Day code (e.g. 3: Wednesday)
- F) Batch no.
- G) Total quantity
- H) Company code

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminium bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity \leq 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C/ - 0 °C and < 5 % RH (dry air/ nitrogen) or

96 h at 60 $^{\circ}\text{C}$ + 5 $^{\circ}\text{C}$ and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.

CAUT This bag of MOISTURE - SENS	
1. Shelf life in sealed bag 12 months at <4	0°C and < 90% relative humidity (RH)
 2. After this bag is opened devices that wi vapor-phase reflow, or equivalent proc 260°C) must be: a) Mounted within 672 hours at fab) Stored at ≤10% RH. 	
 3. Devices require baking before mounting a) Humidity Indicator Card is >10° b) 2a or 2b is not met. 	
4. If baking is required, devices may be ba 192 hours at 40°C + 5°C/-0°C and 96 hours at 60±5°Cand <5%RH 24 hours at 100±5°C	<5%RH (dry air/nitrogen) or
Bag Seal Date:(If blank, see bar co	ode label)
Note: LEVEL defined by EIA J	EDEC Standard JESD22-A113

Example of JESD22-A112 level 2a label

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



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OZONE DEPLETING SUBSTANCES POLICY STATEMENT

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany



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