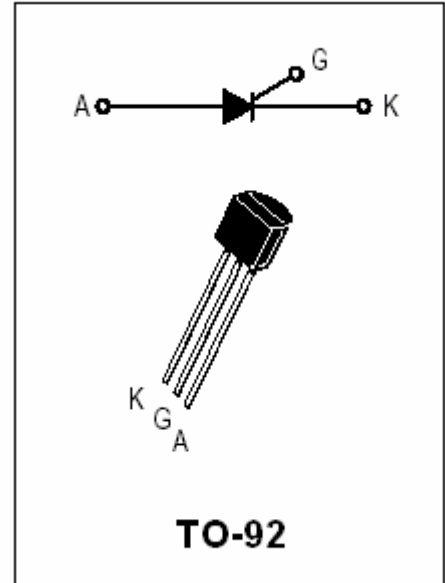


MCR100-8◆ **FEATURES**

- With TO-92 package
- Sensitive gate trigger current
- Low reverse and forward blocking current
- Low holding current

◆ **QUICK REFERENCE DATA**

| SYMBOL | PARAMETER | MIN | UNIT |
|-------------|--------------------------------------|---------|------|
| V_{DRM} | Repetitive peak off-state voltage | 600 | V |
| V_{RRM} | Repetitive peak off-state voltage | 600 | V |
| $I_{T(AV)}$ | Average on-state current | 1 | A |
| P_{GM} | Peak gate power | 0.1 | W |
| $P_{G(AV)}$ | Average gate power | 0.01 | W |
| I_{TSM} | Non-repetitive peak on-state current | 10 | A |
| T_{stg} | Storage temperature range | -45-150 | °C |

◆ **LIMITING VALUES**

Limiting values in accordance with the absolute maximum system(IEC 134)

| SYMBOL | PARAMETER | CONDITIONS | MIN | MAX | UNIT |
|-----------|-----------------------------------|---------------------------------------|-----|-----|---------------|
| V_{DRM} | Repetitive peak off-state voltage | $I_D=0.1\text{mA}$ | 600 | | V |
| V_{RRM} | Repetitive peak reverse voltage | $I_D=0.5\text{mA}$ | 600 | | V |
| I_{RRM} | Repetitive peak reverse current | $V_{RRM}=600\text{V}$ | | 10 | μA |
| I_{DRM} | Repetitive peak off-state current | $V_{DRM}=600\text{V}$ | | 10 | μA |
| I_{GT} | Gate trigger current | $V_D=6\text{V}; R_L=100\ \Omega$ | 10 | 120 | μA |
| V_{TM} | On-state voltage | $I_T=1\text{A}$ | | 1.7 | V |
| I_H | Holding current | $I_T=0.1\text{A}, I_{GT}=12\text{mA}$ | | 5 | mA |
| V_{GT} | Gate trigger voltage | $V_D=6\text{V}; R_L=100\ \Omega$ | 0.4 | 0.8 | V |