

# SANYO Semiconductors DATA SHEET

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### Overview

This LA73065VA is HD video driver. This IC output Component and RGB. It built in the 12MHz/30MHz-LPF. It is the best for the filter to remove the digital clock noise of the Component or RGB Analog video signal.

#### **Functions**

- Three channel output
- 12MHz or 30MHz low pass filter
- 6dB amplifier
- Output mute
- D\_DC\_Output
- Standby mode

## **Specifications**

### **Maximum Ratings** at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max		6.0	V
Allowable power dissipation	Pd max	Ta ≤ 75°C *	500	mW
Operating temperature	Topr		-20 to +75	°C
Storage temperature	Tstg		-40 to +150	°C

<sup>\*:</sup> Mounted on a board: 114.3mm×76.1mm×1.6mm, glass epoxy board.

### **Recommended Operating Conditions** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	Vcc		5.0	٧
Operating supply voltage range	V <sub>CC</sub> opg		4.75 to 5.25	V
Input pin voltage application range	V <sub>IN</sub>		-0.3 to V <sub>CC</sub> opg+0.3	

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# Electrical Characteristics at Ta = 25°C, $V_{CC} = 5.0V$

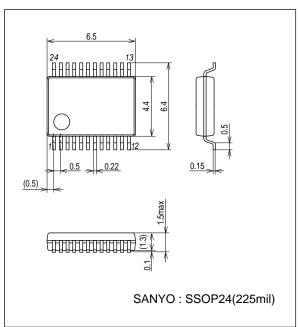
_		Input	signal		Out					
Parameter	Point	Signal	Level [Vp-p]	Freq [Hz]	Point	Conditions	min	typ	max	uni
Current consumption 1	-	-	-	-	-	At no signal.	36	45	54	m/
Current consumption 2	-	-	-	-	-	At no signal. Standby mode.	0.0	0.5	1.0	m/
Internal reference regulator										
REG3V	-	-	-	-	T24		2.8	3.0	3.2	V
Video Driver part		u.	l .	l .			1 1	<u> </u>		
Voltage gain 1	T9A	SIG1	0.3	100k	T16	Output gain	5.5	6.0	6.5	dE
Py, R, G, B	T7A				T18					
	T9A				T16					
	T11A				T14					
Voltage gain 2	T7A	SIG3	0.3	100k	T18	Output gain	5.5	6.0	6.5	d
Pr, Pb	T11A				T14					
Frequency response 1	T9A	SIG1	0.3	12M	T16	12MHz LPF is selected.	-3	0.0	3	d
Py, R, G, B	T7A				T18	f = 12MHz/100kHz				
	T9A				T16					
	T11A				T14					
Frequency response 2	T7A	SIG3	0.3	12M	T18	12MHz LPF is selected.	-3	0.0	3	d
Pr, Pb	T11A	6.5			T14	f = 12MHz/100kHz				
Frequency response 3	T9A	SIG1	0.3	75M	T16	12MHz LPF is selected.		-40	-30	d
Py, R, G, B	T7A				T18	f = 75MHz/100kHz				
	T9A				T16					
Fraguesey reesence 4	T11A	SICO	0.2	7514	T14	12MUz LDC is selected		40	20	- 4
Frequency response 4 Pr, Pb	T7A T11A	SIG3	0.3	75M	T18 T14	12MHz LPF is selected. f = 75MHz/100kHz		-40	-30	d
Frequency response 5	T9A	SIG1	0.3	20M	T16	30MHz LPF is selected.	-1	0.0	1	d
Py, R, G, B	T7A	3131	0.3	ZUIVI	T16	f = 20MHz/100kHz	-1	0.0	'	a
r y, N, G, B	T9A				T14	1 = 201VII 12/100KI 12				
	T11A				114					
Frequency response 6	T7A	SIG3	0.3	20M	T18	30MHz LPF is selected.	-1	0.0	1	dl
Pr, Pb	T11A				T14	f = 20MHz/100kHz				
Frequency response 7	T9A	SIG1	0.3	30M	T16	30MHz LPF is selected.	-3.5	-1.0	1.5	dl
Py, R, G, B	T7A				T16	f = 30MHz/100kHz				
	T9A				T14					
	T11A									
Frequency response 8	T7A	SIG3	0.3	30M	T18	30MHz LPF is selected.	-3.5	-1.0	1.5	d
Pr, Pb	T11A				T14	f = 30MHz/100kHz				
Frequency response 9	T9A	SIG1	0.3	75M	T16	30MHz LPF is selected.		-40	-30	d
Py, R, G, B	T7A				T16	f = 75MHz/100kHz				
	T9A				T14					
	T11A									
Frequency response 10	T7A	SIG3	0.3	75M	T18	30MHz LPF is selected.		-40	-30	d
Pr, Pb	T11A	ļ			T14	f = 75MHz/100kHz				_
2nd order distortion 1	T9A	SIG1	0.7	10M	T16	30MHz LPF is selected.		-40	-30	d
Py, R, G, B	T7A				T18					
	T9A				T16					
0-1	T11A	0100	0.7	4014	T14	20MU-1 DE :		40		-
2nd order distortion 2 (HD)	T7A	SIG3	0.7	10M	T18	30MHz LPF is selected.		-40	-30	d
Pr, Pb	T11A	0104	0.7	484	T14			60	F^	
Amount of mute attenuation 1	T9A	SIG1	0.7	4M	T16			-60	-50	d
Py, R, G, B	T7A T9A				T18 T16					
	T11A				T14					
Amount of muto attanuation 2		8103	0.7	484				60	FΛ	d
Amount of mute attenuation 2	T7Α T11Δ	SIG3	0.7	4M	T18			-60	-50	d
Pr, Pb Crosstalk between channels 1	T11A	8104	0.7	484	T14			60	FΛ	ام
Crosstalk between channels 1	T9A	SIG1	0.7	4M				-60	-50	d
Py, R, G, B	T7A T9A									
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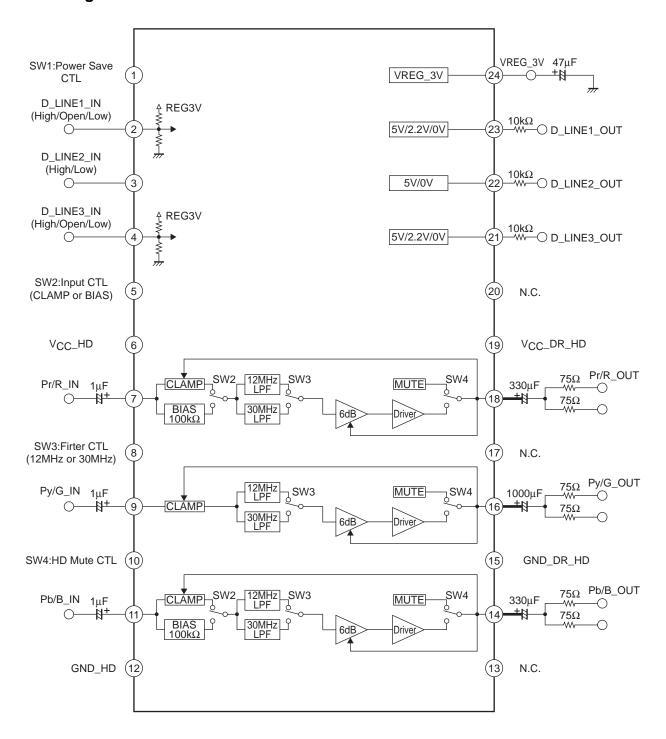
1 27 37 37		Input	signal		Out					
Parameter	Point	Signal	Level [Vp-p]	Freq [Hz]	Point	Conditions	min	typ	max	unit
Crosstalk between channels 2	T7A	SIG3	0.7	4M				-60	-50	dB
Pr, Pb	T11A									
Video S/N	T9A	SIG2	0.65		T16	V <sub>IN</sub> = Video (50% White)		-60	-50	dB
Py, R, G, B	T7A				T18	30MHz LPF is selected.				
	T9A				T16	The band is between 100kHz				
	T11A				T14	and 30MHz.				
G.D.1	T9A	SIG1	0.3	12M	T16	12MHz LPF is selected.		10	20	ns
Py, R, G, B	T7A				T18	f = 12MHz/100kHz				
	T9A				T16					
	T11A				T14					
G.D.2	T7A	SIG3	0.3	12M	T18	12MHz LPF is selected.		10	20	ns
Pr, Pb	T11A				T14	f = 12MHz/100kHz				
G.D.3	T9A	SIG1	0.3	30M	T16	30MHz LPF is selected.		10	20	ns
Py, R, G, B	T7A				T18	f = 30MHz/100kHz				
	T9A				T16					
	T11A				T14					
G.D.4	T7A	SIG3	0.3	30M	T18	30MHz LPF is selected.		10	20	ns
Pr, Pb	T11A				T14	f = 30MHz/100kHz				

# Package Dimensions unit: mm (typ)

3287



## **Block Diagram**



Please shorten the distance of the bold line to prevent oscillation.

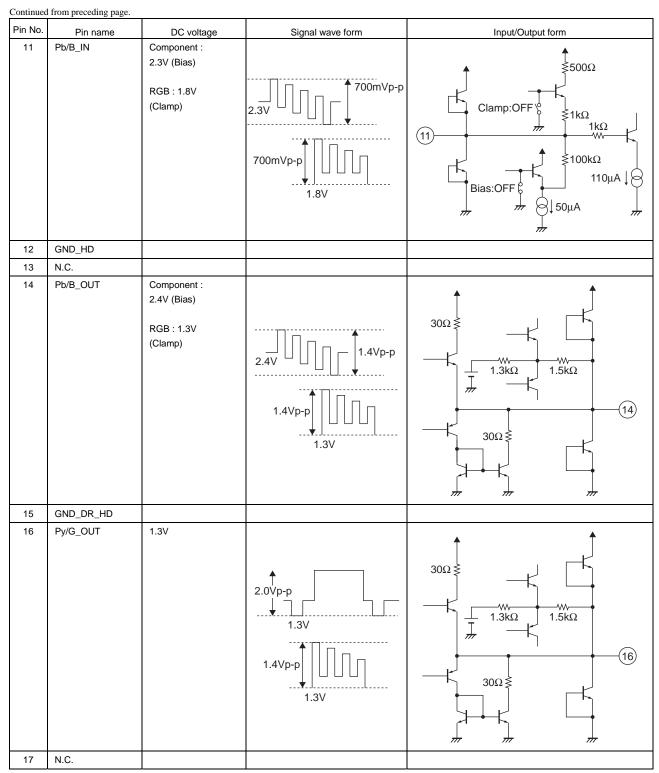
#### Pin control table

SW No.	IN Dia No	OUT Die No	CM/ function name		Control voltage	
SW NO.	IN_Pin No.	OUT_Pin No.	SW function name	High (2.3V to V <sub>CC</sub> )	Open	Low (0 to 0.7V)
SW1	Pin1		Power_Save_CTL	Power_Save_OFF		Power_Save_ON
SW2	Pin5		Input_CTL	CLAMP_ON (RGB_Mode)		BIAS_ON (Component_Mode)
SW3	Pin8		Filter_CTL	12MHz_LPF_ON		30MHz_LPF_ON
SW4	Pin10		HD_MUTE_CTL	HD_MUTE_OFF		HD_MUTE_ON
D_L1	Pin2	Pin23	D_LINE1	High (4.0V to V <sub>CC</sub> )	Midd (1.8 to 2.4V)	Low (0 to 0.5V)
D_L2	Pin3	Pin22	D_LINE2	High (4.0V to V <sub>CC</sub> )		Low (0 to 0.5V)
D_L3	Pin4	Pin21	D_LINE3	High (4.0V to V <sub>CC</sub> )	Midd (1.8 to 2.4V)	Low (0 to 0.5V)

## **Pin Functions**

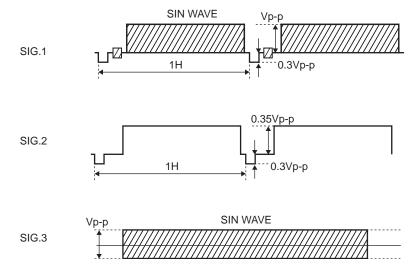
FIII	unctions			
Pin No.	Pin name	DC voltage	Signal wave form	Input/Output form
1	Power_Save_CTL	5V: Power_Save_OFF  0V: Power_Save_ON		1 \$10kΩ
2	D_LINE1_IN	5V : 1125 (1080)		
2	D_LINE I_IN	OPEN: 750 (720)		3V_REG
		0V : 525 (480)		2 10kΩ
3	D_LINE2_IN	5V : 59.94p/60p 0V : 59.94i/60i		\$10kΩ ""
				3 \$100kΩ
4	D_LINE3_IN	5V:16:9  OPEN:4:3  Letter Box  0V:4:3		\$200kΩ 10kΩ W \$200kΩ m
5	INPUT_CTL	5V : Clamp 0V : Bias		5 \$10kΩ m

Continued from preceding page. Pin No. Signal wave form Pin name DC voltage Input/Output form 6 V<sub>CC</sub>\_HD Pr/R\_IN Component: 2.3V (Bias) [ ≶500Ω ↑ 700mVp-p **RGB**: 1.8V (Clamp) Clamp:OFF ≸1kΩ 7 \$100kΩ 110μΑ 📗 Bias:OFF 1.8V **⊝**↓50μΑ 8 FIL\_CTL 5V: 12MHz\_LPF 0V:30MHz\_LPF \$10kΩ (8) \$100kΩ Py/G\_IN 1.8V \$500Ω 1.0Vp-p 1.8V ∮1kΩ 1kΩ 700mVp-p 1.8V 110μΑ | િ 10 HD\_MUTE\_CTL 5V: HD\_MUTE\_OFF 0V: HD\_MUTE\_ON ≸10kΩ (10) \$100kΩ



	from preceding page.	1		
Pin No.	Pin name	DC voltage	Signal wave form	Input/Output form
18	Pr/R_OUT	Component: 2.4V (Bias)  RGB: 1.3V (Clamp)	1.4Vp-p	30Ω \$ 1.3kΩ 1.5kΩ  30Ω \$ 1.80Ω \$ 1.80
19	V <sub>CC</sub> _DR_HD			
20	N.C.			
21	D_LINE3_OUT	5V: 16: 9  2.2V: 4: 3Letter Box  0V: 4: 3		OPEN:OFF 6 300Ω 21 Low:OFF 6 m m m
22	D_LINE2_OUT	5V : 59.94p/60p 0V : 59.94i/60i		300Ω (22)
23	D_LINE1_OUT	5V: 1125 (1080) 2.2V: 750 (720) 0V: 525 (480)		OPEN:OFF 6 300Ω  500μA   S Low:OFF 6 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7
24	REG3V	3.0V		50Ω ≥ 100Ω 24 30kΩ ≥ 24kΩ ≥ /// // // // // // // // // // // //

### Test Input Signal



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