TOSHIBA Diode Silicon Epitaxial Planar Type

# HN1D04FU

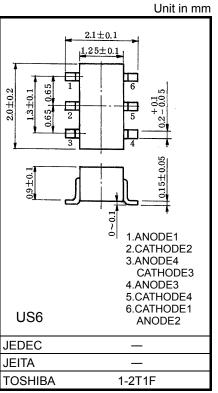
### **Ultra High Speed Switching Application**

Low forward voltage : V<sub>F(3)</sub> = 0.90V (typ.)
 Fast reverse recovery time : t<sub>rr</sub> = 1.6ns (typ.)
 Small total capacitance : C<sub>T</sub> = 0.9pF (typ.)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Maximum (peak) reverse voltage	$V_{RM}$	85	V	
Reverse voltage	V <sub>R</sub>	80	V	
Maximum (peak) forward current	I <sub>FM</sub>	300*	mA	
Average forward current	IO	100*	mA	
Surge current (10ms)	I <sub>FSM</sub>	2*	Α	
Power dissipation	Р	200**	mW	
Junction temperature	Tj	150	°C	
Storage temperature	T <sub>stg</sub>	-55~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.



Weight: 6.8mg (typ.)

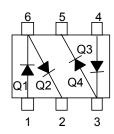
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- \*: Where Q1 and Q2 or Q3 and Q4 are used independently or simultaneously, the Absolute Maximum Ratings per diode are 50% of those of the single diode.
- \*\*: Total rating

#### Electrical Characteristics (Q1, Q2, Q3, Q4 Common; Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V <sub>F (1)</sub>	_	I <sub>F</sub> = 1mA	ı	0.60	_	V
	V <sub>F (2)</sub>	_	I <sub>F</sub> = 10mA	ı	0.75	_	
	V <sub>F (3)</sub>	_	I <sub>F</sub> = 100mA	1	0.90	1.20	
Reverse current	I <sub>R (1)</sub>	_	V <sub>R</sub> = 30V	ı	_	0.1	μA
	I <sub>R (2)</sub>	_	V <sub>R</sub> = 80V	_	_	0.5	
Total capacitance	C <sub>T</sub>	_	V <sub>R</sub> = 0, f = 1MHz	ı	0.9	_	pF
Reverse recovery time	t <sub>rr</sub>	_	I <sub>F</sub> = 10mA (fig.1)	_	1.6	_	ns

## **Pin Assignment (Top View)**



## Marking

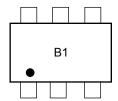
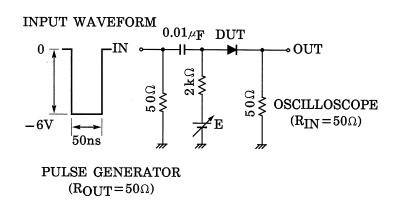
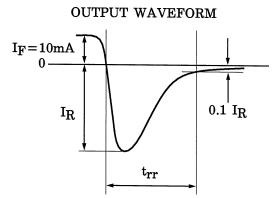
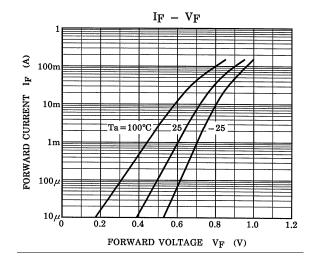


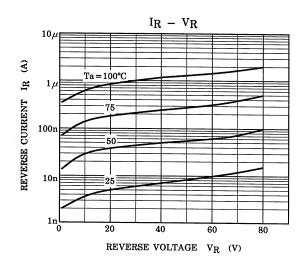
Fig. 1 Reverse Recovery Time (t<sub>rr</sub>) Test Circuit

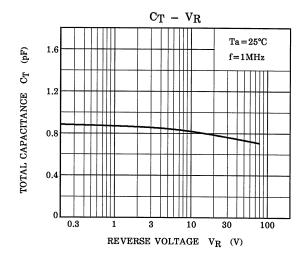


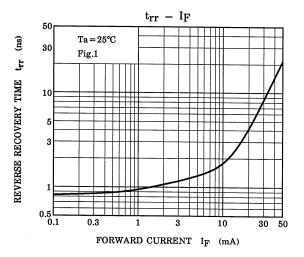


# **Q1, Q2, Q3, Q4 Common**









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20070701-EN GENERAL

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