

## General Description

The AOZ8014 is an 4-line device integrating EMI filtering with ESD protection for each line. It is designed to suppress unwanted EMI/RFI signals and provide electrostatic discharge (ESD) protection in portable electronic equipment. This state-of-the-art device utilizes AOS leading edge Trench Vertical Structure [TVS]<sup>2</sup>™ technology for superior clamping performance and filter attenuation over the full operating display range. The AOZ8014 has been optimized for protection of color LCD displays and CCD camera lines in cellular phones and other portable consumer electronic devices.

The AOZ8014 consists of four identical circuits comprised of TVS diodes for ESD protection, and a resistor–capacitor network for EMI/RFI filtering. A series resistor value of 100Ω and a capacitance value of 17pF are used to achieve -25dB minimum attenuation from 800MHz to 3.0GHz. The TVS diodes provide effective suppression of ESD voltages in excess of ±17kV (air discharge) and ±17kV (contact discharge). This exceeds IEC 61000-4-2, level 4 ESD immunity test.

The AOZ8014 comes in an RoHS compliant, 2.0mm x 2.0mm DFN package and is rated over a -40°C to +85°C ambient temperature range.

## Features

- 4 lines for EMI filtering and ESD protection:
  - Exceeds IEC 61000-4-2, level 4 (ESD) immunity test
  - ±17kV (air discharge) and ±17kV (contact discharge)
- Trench Vertical Structure [TVS]<sup>2</sup>™ based technology used to achieve excellent ESD clamping & filter performance over the full operating display range
- Filter performance: -25db attenuation from 800MHz to 3.0GHz
- Low operating voltage: 5.0V
- Capacitance stability over wide range of voltages and temperatures
- DFN package, 2.0 x 2.0 mm
- Pb-Free device

## Applications

- EMI filtering and ESD protection for data lines
- LCD displays, camera interface, I/O interface
- Portable handheld devices, cell phones, PDA phones



## Electrical Schematic (each channel)

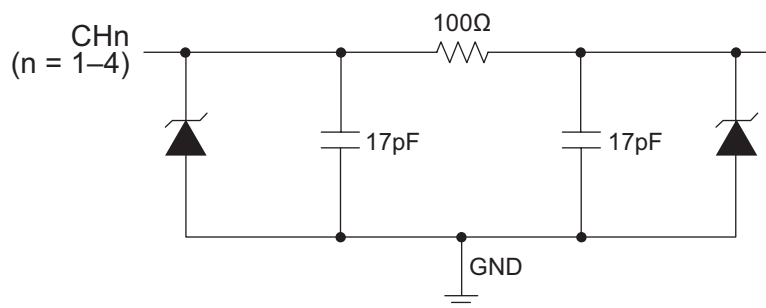


Figure 1.

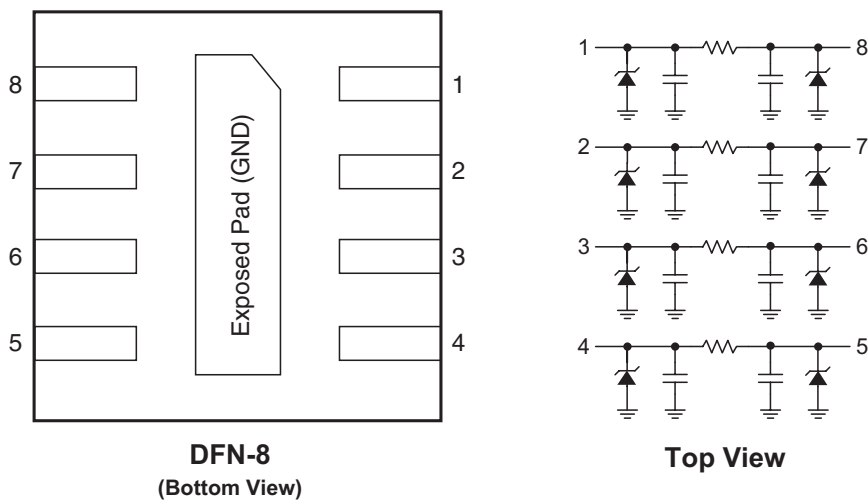
### Ordering Information

Part Number	Ambient Temperature Range	Package	Environmental
AOZ8014DI	-40°C to +85°C	DFN-8	RoHS Compliant



All AOS products are offered in packages with Pb-free plating and compliant to RoHS standards. Parts marked as Green Products (with “L” suffix) use reduced levels of Halogens, and are also RoHS compliant. Please visit [www.aosmd.com/web/quality/rohs\\_compliant.jsp](http://www.aosmd.com/web/quality/rohs_compliant.jsp) for additional information.

### Pin Configuration



### Pin Description

Pin Number	Pin Name	Pin Function
1, 8	CH 1	Channel 1 Connections
2, 7	CH 2	Channel 2 Connections
3, 6	CH 3	Channel 3 Connections
4, 5	CH 4	Channel 4 Connections
Exposed Pad	GND	Common Ground Connection

## Absolute Maximum Ratings

Exceeding the Absolute Maximum ratings may damage the device.

Parameter	Rating
Storage Temperature (T <sub>S</sub> )	-65°C to +150°C
ESD Rating per IEC61000-4-2, contact <sup>(1)</sup>	±17kV
ESD Rating per IEC61000-4-2, air <sup>(1)</sup>	±17kV
ESD Rating per Human Body Model <sup>(2)</sup>	±30kV

### Notes:

- IEC 61000-4-2 discharge with C<sub>Discharge</sub> = 150pF, R<sub>Discharge</sub> = 330Ω.
- Human Body Discharge per MIL-STD-883, Method 3015 C<sub>Discharge</sub> = 100pF, R<sub>Discharge</sub> = 1.5kΩ.

## Electrical Characteristics

T<sub>A</sub> = 25°C unless otherwise specified.

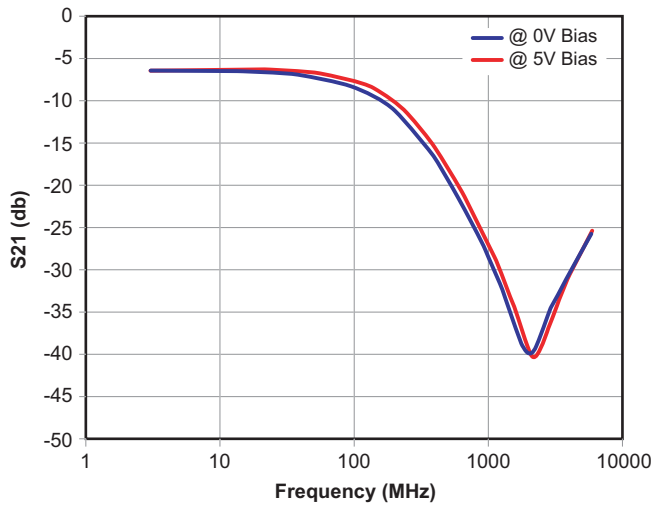
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
V <sub>RWM</sub>	Reverse Working Voltage	<sup>(3)</sup> (8)			5.0	V
V <sub>BR</sub>	Reverse Breakdown Voltage	I <sub>T</sub> = 1mA <sup>(4)</sup>	6	7	8	V
I <sub>R</sub>	Reverse Leakage Current	V <sub>RWM</sub> = 3.3V			0.1	μA
V <sub>CL</sub>	Signal Clamp Voltage	I <sub>LOAD</sub> = 12A, positive clamp <sup>(5)</sup> (8) I <sub>LOAD</sub> = 12A, negative clamp <sup>(5)</sup> (8)		7.5 -6.5	8.5 -7.5	V
R <sub>CH</sub>	Total Series Resistance	I <sub>R</sub> = 20mA	85	100	115	Ω
C <sub>CH</sub>	Channel Capacitance	Input to Ground <sup>(6)</sup> (7)(8)	15	17	19	pF
f <sub>C</sub>	Cut-off Frequency	Measured with 50Ω source and 50Ω load termination		125		MHz
	Attenuation from 800MHz to 3.0GHz	Measured with 50Ω source and 50Ω load termination		-25		dB

### Notes:

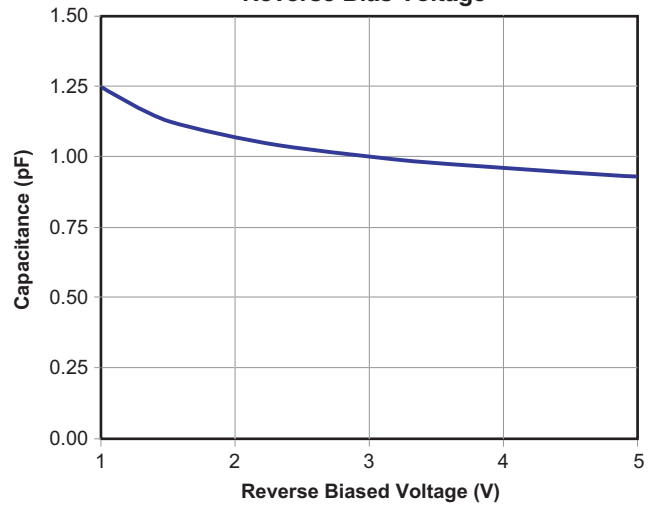
- The working peak reverse voltage, V<sub>RWM</sub>, should be equal to or greater than the DC or continuous peak operating voltage level.
- V<sub>BR</sub> is measured at the pulse test current I<sub>T</sub>.
- Measurements performed using a 100ns Transmission Line Pulse (TLP) system.
- Total capacitance is equal to 2 x C<sub>CH</sub>.
- Measured at 25°C, V<sub>R</sub> = 2.5V, f = 1.0MHz.
- Guaranteed by design.

## Typical Performance Characteristics

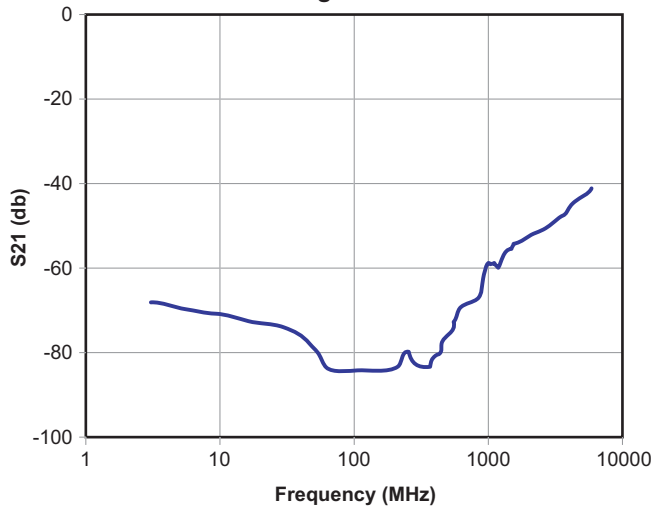
Insertion Loss Characteristics



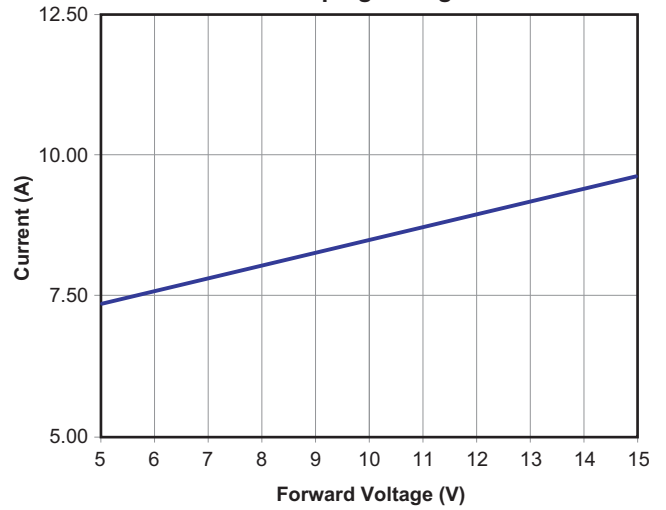
Typical Line Capacitance vs. Reverse Bias Voltage



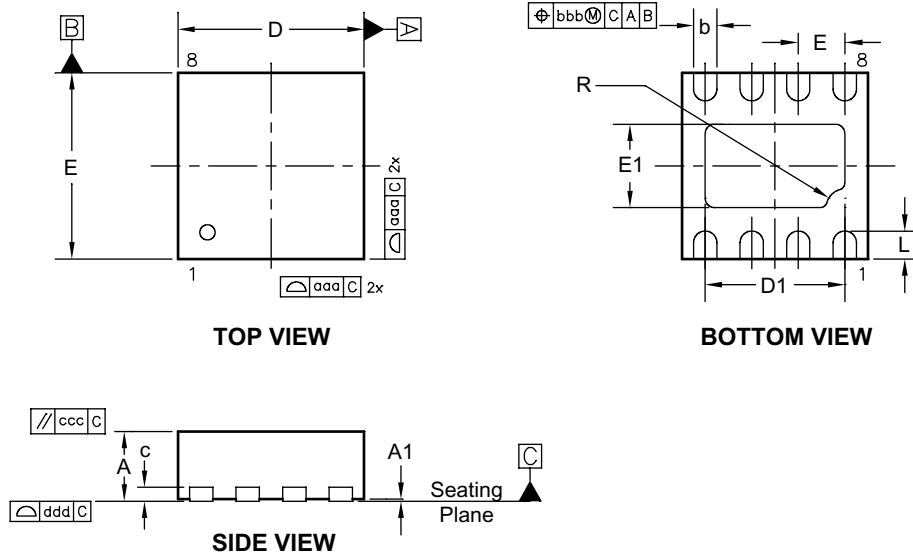
Analog Crosstalk



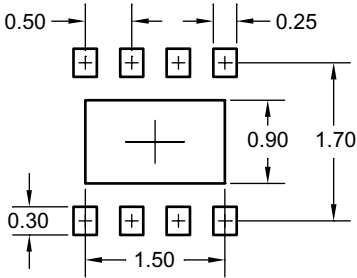
Clamping Voltage



**Package Dimensions, DFN 2.0 x 2.0 8L**



**RECOMMENDED LAND PATTERN**



UNIT: mm

**Dimensions in millimeters**

Symbols	Min.	Nom.	Max.
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
b	0.18	0.25	0.30
c	0.15	0.20	0.25
D	2.00 BSC		
D1	1.35	1.50	1.60
E	2.00 BSC		
E1	0.75	0.90	1.00
e	0.50 BSC		
L	0.20	0.30	0.40
R	0.20		
aaa	0.15		
bbb	0.10		
ccc	0.10		
ddd	0.08		

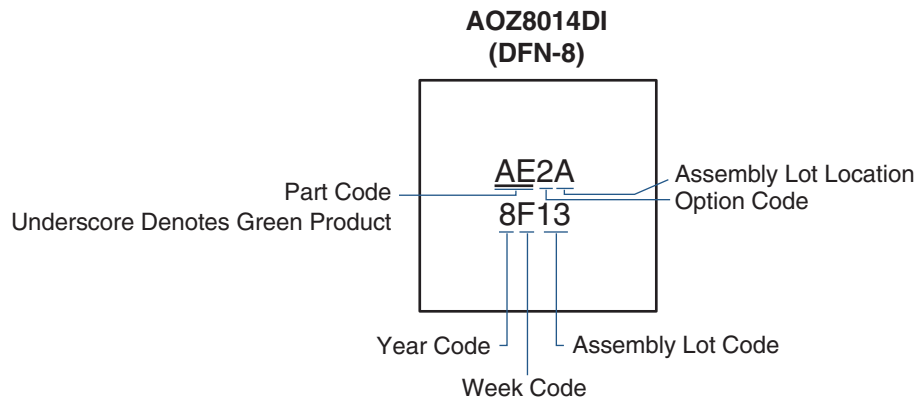
**Dimensions in inches**

Symbols	Min.	Nom.	Max.
A	0.028	0.030	0.031
A1	0.000	0.001	0.002
b	0.007	0.010	0.012
c	0.006	0.008	0.010
D	0.079 BSC		
D1	0.053	0.059	0.063
E	0.079 BSC		
E1	0.030	0.035	0.039
e	0.020 BSC		
L	0.008	0.012	0.016
R	0.008		
aaa	0.006		
bbb	0.004		
ccc	0.004		
ddd	0.003		

**Notes:**

1. Dimensions and tolerances conform to ASME Y14.5M-1994.
2. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.
3. Dimension b applied to metallized terminal and is measured between 0.10mm and 0.30mm from the terminal tip. If the terminal has the optional radius on the other end of the terminal, dimension b should not be measured in that radius area.
4. Coplanarity ddd applies to the terminals and all other bottom surface metallization.

**Part Marking**



**This datasheet contains preliminary data; supplementary data may be published at a later date. Alpha & Omega Semiconductor reserves the right to make changes at any time without notice.**

**LIFE SUPPORT POLICY**

ALPHA & OMEGA SEMICONDUCTOR PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS.

As used herein:

- |   |   |
|---|---|
| <p>1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.</p> | <p>2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.</p> |
|---|---|