

TECHNICAL DATASHEET Rev. D

Wall Industries, Inc.

LV12S12-150

Low Voltage DC-DC Converter 10-36 Vdc Input 12Vdc Output at 12.5A Half-Brick Package



Applications:

- For use in 12V and 24V battery applications.
- For use in Intermediate and Distributed Bus Architectures (IBA)
- Telecommunication equipment
- Network (LANs/WANs) Equipment
- Next generation low voltage, high current microprocessors and Ics

Features:

- Up to 90% Efficient
- Cost Efficient Solution
- Delivering 12.5A at Room Temperature with No Added Heat Sink at 400 LFM
- Fixed Switching Frequency
- High Reliability
- Consult Factory for Optional Heat Sink
- Output Short Circuit Protection
- Output Over Current Protection
- Optional Encapsulation for added Ruggedness
- Remote ON/OFF
- Remote Sense Compensation to 10% Vout
- Fast Transient Response
- 100% Burn In
- Soft Start

Description:

The LV12S12-150 is a high density, low input voltage, isolated converter with a wide input voltage range. Low input voltage converters are uncommon in the industry and the LV12S12-150 offers the flexibility of operation with both 12V and 24V busses. This state-of-the-art converter's features include fast transient response, short circuit protection, over current protection, soft start, and many other features that are required for today's demanding applications.



TECHNICAL DATASHEET

LV12S12-150

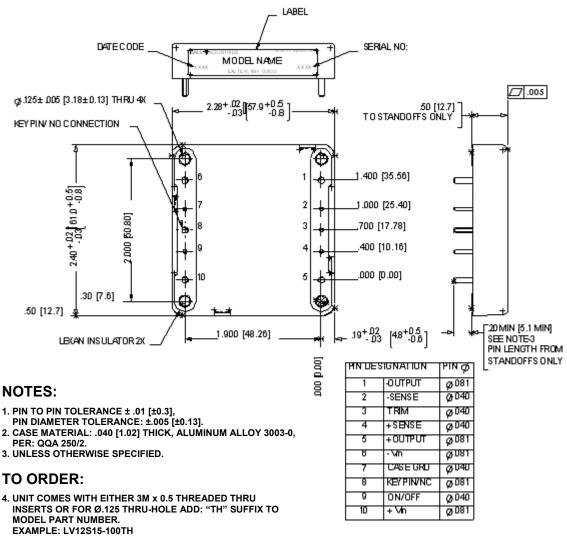
Load Regulation +RS shorted to ±Vo - 0.1 0.2 % Line Regulation +RS shorted to ±Vo - 0.1 0.2 % Temperature Drift - 0.1 0.2 % % % Remote Sense Compensation Max Output limited to 150W - 110% % % % Ripple 1uF Ceramic & 10uF Tantalum - 120 - mV _{sk-sk} Current Limit Ower Limited-Dependent upon SENSE - 15 - A Over Voltage Limit Output Clamped - - - Vde DNNAMIC RESPONSE 1uF Ceramic & 100% 1o, di/dt=1/AvS - 300 - mV Turn On Delay From Vin(min) to Vout (nom) - 4 - ms Turn On Overshoot Full Load Resistive - 0 - % Memote ON - Keovery to within 1% Norther Mark (NU NOFF Pin) 2.2 - - Vde Remote ON - Active High Min High (ON/OFF pin) 2.2	Technical Specifications	Model No. LV12	2S12-150			
SPECIFUATION Related condition Min Nom Max Unit SNICHAIR Frequency - 550 - Hill 2 INPUT (V_a) - 10 12/24 36 Vale Operating Voltage Range - 6.3 - A UVLO Turn On at - 0.15 - A Maximum Input Current Low Line - 0.0064 - A Input Current under "Remote Off" - 0.0064 - A Reflected Rippic Current - 0.0064 - A Voltage Set Point #RS shorted to 4Vo -18 8 - 8 Voltage Adjustment Max Output limited to 150W - 8 - 8 - 8 - 8 - 8 - 8 - 8 - 8 - 8 - 8 - 8 - 8 - 8 - 8 - 8 - 8	All specifications are based on 2:	5 °C, Nominal Input Voltage and Maximum Outp	ut Current unless of	herwise no	ted.	
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$\begin{split} & I_{ON/OFF} \text{ Sink to pull low - Active Low or High} & V_{ON/OFF} = 0V, Vin = 36V & - & - & 0.38 & mA \\ & I_{ON/OFF} \text{ Source to drive high - Active High} & V_{ON/OFF} = 5V, Vin = 36V & - & - & 0.03 & mA \\ & I_{ON/OFF} \text{ Source to drive high - Active Low} & V_{ON/OFF} = 5V, Vin = 36V & - & - & - & mA \\ & Turn On Delay - Active High & ON/OFF (max Low) to Vout (min) & - & 9 & - & ms \\ & Turn Off Delay - Active High & ON/OFF (0V) to Vout (min) & - & 160 & - & uS \\ & ISOLATION & & & & & & & & & & & & & & & & & & &$				-		
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			-		-	
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Table 1: Pin Assignments

Pin #	Pin Name	Function	Comments
1	-Vo	Negative Output	
2	-RS	Negative Remote Sense	If not used, leave open or short to -Vo
3	Trim	Output Voltage Trim	Refer to page 6
4	+RS	Positive Remote Sense	If not used, leave open or short to +Vo
5	+Vo	Positive Output	
6	-Vin	Negative Input	
7	CHGND	Chassis Ground (Case)	Short to –Vin if no chassis ground is available
8	Key Pin/NC	To Key Converter	Leave as a No Connect pin
9	ON/OFF	Remote On/Off	If not used, leave floating for Active High Unit If not used, short to –Vin on an Active Low Unit
10	+Vin	Positive Input	

Figure 1: Mechanical Dimensions



5. CONSULT FACTORY FOR OPTIONAL HEAT SINK.



DESIGN CONSIDERATIONS

Under Voltage Lock Out (UVLO)

The converter output is disabled until the input voltage exceeds the UVLO turn-on limit. The converter will remain ON until the input voltage falls below the UVLO turn-off limit.

Over Current Protection

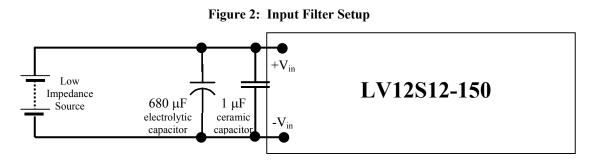
The converter is protected from short circuit and over current conditions. During these fault conditions, the converter output will 'hiccup'. The converter output will recover once the short or over current fault is removed.

Over Temperature Protection (OTP)

The converter has internal thermal protection that will shut the converter OFF once the case temperature exceeds the OTP turn-off limit. The converter will resume operation when the case temperature has dropped below the OTP turn-on limit.

Input Filter

It is recommended to bypass the +Vin and –Vin pins of the converter with a minimum of 680uF (50V minimum) capacitor (UCC - SXE50VB681M12X35LL). No other bypassing is needed. However, to reduce the input ripple beyond what is seen in Photo 1, larger values of capacitance may be used in conjunction with a ceramic capacitor. Additionally, an inductor may be placed between the source and the previously mentioned capacitor. No inductor should be placed between the capacitor and the input to the converter. It is important to note that placement of the input filter must be as close as possible to the input pins of the converter to assure a low impedance at the pins.



Output Filter

No additional output capacitor is needed for the power supply to operate. However, to reduce the ripple and noise on the output, additional capacitance may be added. A low ESR Ceramic capacitor may be added across the +Vo and -Vo pins to reduce the ripple and spike noise. Additional capacitance in the form of a tantalum or aluminum electrolytic may also be placed across these pins in order reduce ripple and improve the transient peak-to-peak voltage deviation.

Remote Sense

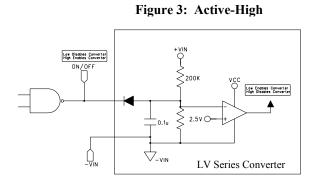
To improve the regulation at the load, route the connections from the -RS and the +RS pins to the –Vo and +Vo connections at the load. This will force the converter to regulate the voltage at the load and not at the pins of the converter (refer to Graph 8). If it is not desired to use the Remotes Sense feature, the –RS and +RS pins may be left open or they may be shorted to the -Vo and +Vo pins respectively. Shorting the RS pins to the Vo pins will reduce the voltage drops through the converter pins.



Remote ON/OFF

The converter has the ability to be remotely turned ON or OFF. The LV series is Active-High. Active-High means that a logic high at the ON/OFF pin will enable the supply (Figure 3). With Active-High, if the ON/OFF pin is left floating, the supply will be enabled.

Rev. D





Output Voltage Trim: (5V, 12V, 15V, and 20V Models)

The output is adjustable +/-10% of rated output voltage. To trim the output voltage down, place the trim resistor between the Trim and -Rs pins (Figure 5). To trim the output voltage up, place the trim resistor between the Trim and +Rs pins (Figure 4).

The value of the trim resistor with respect to the desired output voltage (Vo) can be derived from the following formulas, or looked up on the trim table (Table 2).

$$RTH = \frac{R_1 \cdot V_o \cdot \frac{V_{onom}}{U_1}}{V_o - V_{onom}} - \frac{R_1 \cdot V_o}{V_o - V_{onom}} - R_{\lim} \qquad \text{(in Kohms)}$$

$$RTL = \frac{R_1 \cdot V_o}{V_{onom} - V_o} - R_{\lim} \qquad (\text{in Kohms})$$

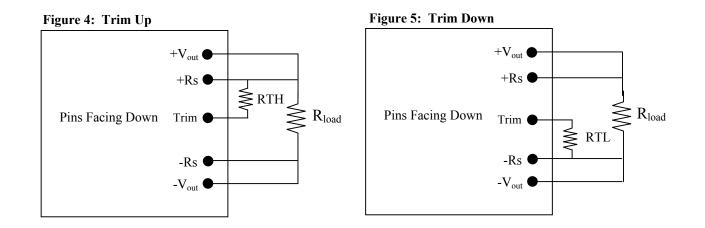


Table 2: Trim Equations for LV Series (5V, 12V, 15V, and 20V Models)

Vonom	U1	R1	Rlim	RTH to +Rs
12.000	2.500	5.11	5.11	RTL to -Rs

Percent	<u>Trim</u>	Low	<u>Trim</u>	High	
Trim	Vo	RTL	Vo	RTH	_
1%	11.880	500.78	12.120	1956.11	All in Kohms
2%	11.760	245.28	12.240	985.21	
3%	11.640	160.11	12.360	661.57	
4%	11.520	117.53	12.480	499.76	
5%	11.400	91.98	12.600	402.67	
6%	11.280	74.95	12.720	337.94	
7%	11.160	62.78	12.840	291.71	
8%	11.040	53.66	12.960	257.03	
9%	10.920	46.56	13.080	230.06	
10%	10.800	40.88	13.200	208.49	

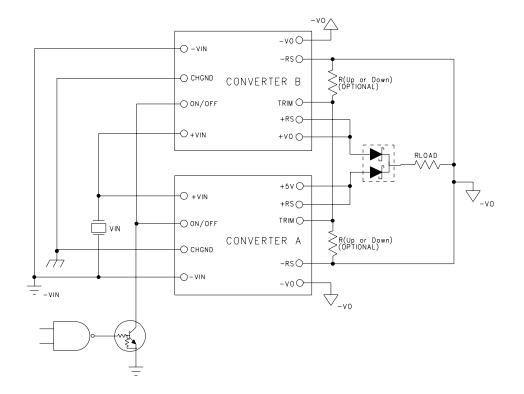
Note that while decreasing the output voltage, the maximum output current still remains at 12.5A, and while increasing the output voltage, the output current is reduced to maintain a total output power at 150 W.



Paralleling Converters

The LV series converters may be paralleled both for redundancy and for higher output current. However, in order to do this, a high-current, low V_f , schottky diode must be placed at the +Vo pin of each supply as shown in Figure 6. To improve sharing, tie the two TRIM pins together. The converters may be trimmed by adding a resistor value from Table 2 from each TRIM pin to ±RS pin, or alternatively, a single resistor of half the value of Table 2 from the common TRIM pins to the common ±RS pins.

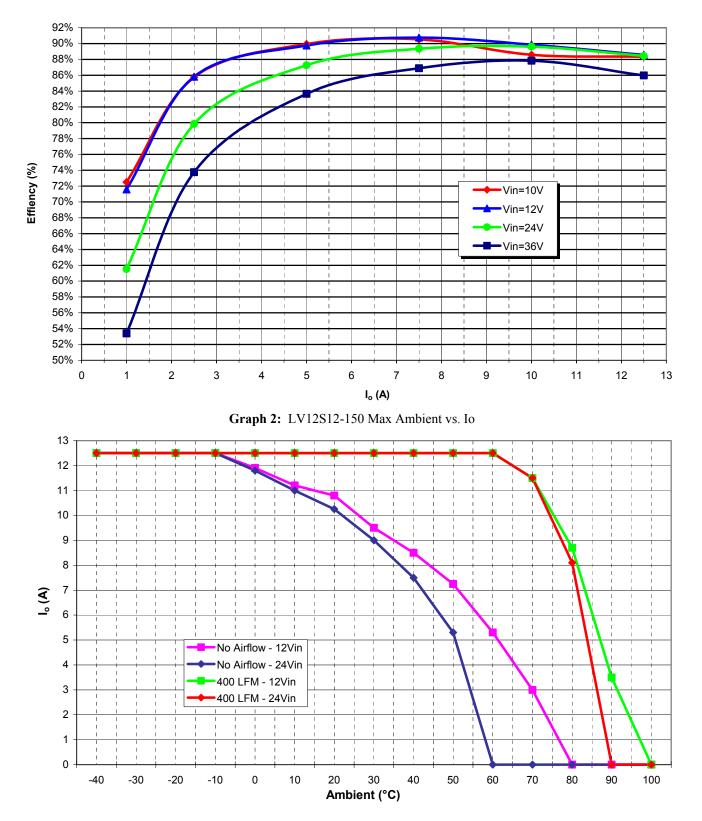
Figure 6: Paralleling Converters



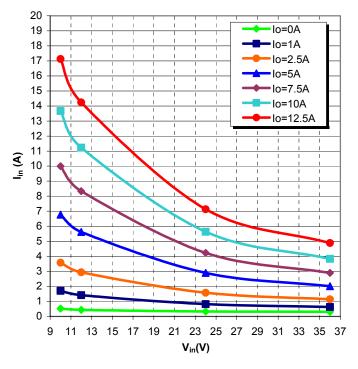


TECHNICAL DATASHEET LV12S12-150





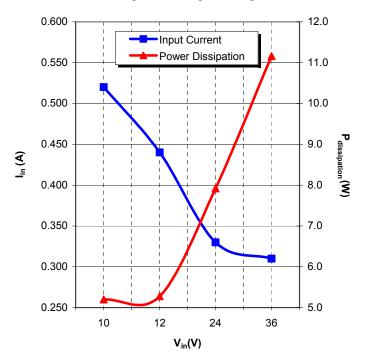




Graph 3: LV12S12-150 Input Current vs. Input Voltage

Rev. D

Graph 5: LV12S12-150 Min Load Input Current and Power Dissipation vs. Input Voltage

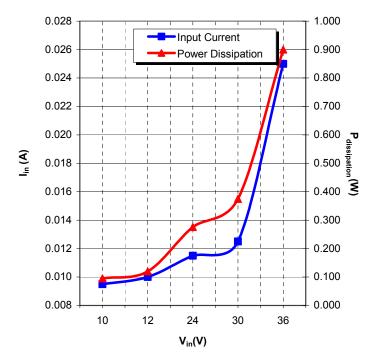


Note: Voltage measurements taken where the output pins are soldered into test board.

30 28 lo=2.5A lo=7.5A lo=1A lo=5A lo=10A 26 lo=12.5A lo=0A 24 22 20 ₹ 18 P_{dissipation} 16 14 12 10 8 6 4 2 11 13 15 17 19 21 23 25 27 29 31 33 35 37 9 $V_{in}(V)$

Graph 4: LV12S12-150 Power Dissipation vs. Input Voltage

Graph 6: LV12S12-150 "Remote Off" Input Current and Power Dissipation vs. Input Voltage



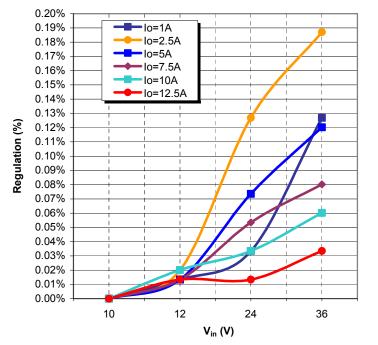




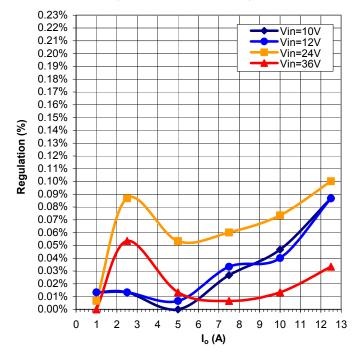
0.23% 0.22% 0.21% Vin=10V 0.20% Vin=12V Vin=24V 0.19% Vin=36V 0.18% 0.17% 0.16% 0.15% 8 0.14% Regulation 0.13% 0.12% 0.11% 0.10% 0.09% 0.08% 0.07% 0.06% 0.05% 0.04% 0.03% 0.02% 0.01% 0.00% 5 8 9 0 1 2 3 4 6 7 10 11 12 13 $I_{o}(A)$

Graph 7: LV12S12-150 Load Regulation (±RS Pins Open)

Graph 9: LV12S12-150 Line Regulation (+RS to +Vo, -RS to -Vo)

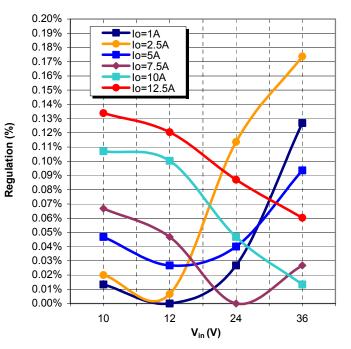


Note: Voltage measurements taken where the output pins are soldered into test board.



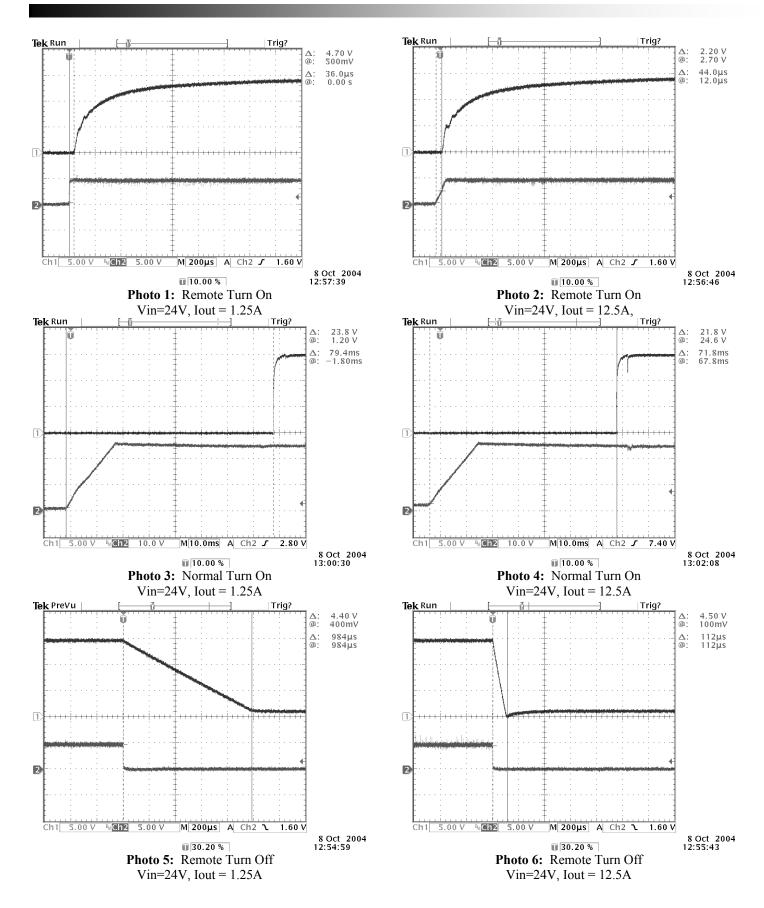
Graph 8: LV12S12-150 Load Regulation (+RS to +Vo, -RS to -Vo)

Graph 10: LV12S12-150 Line Regulation (±RS Pins Open)



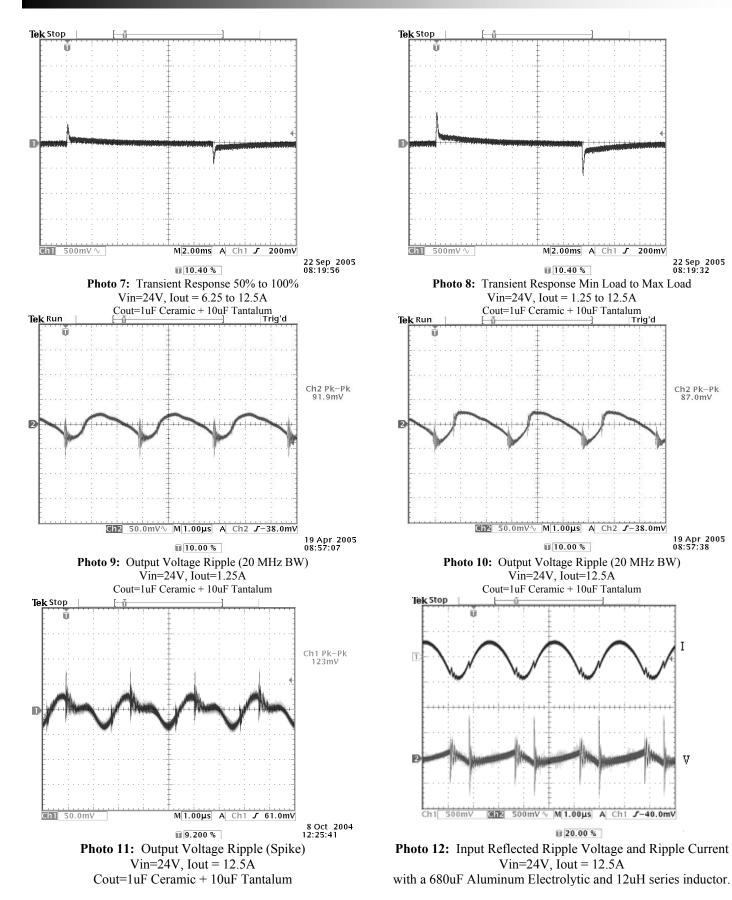


TECHNICAL DATASHEET LV12S12-150





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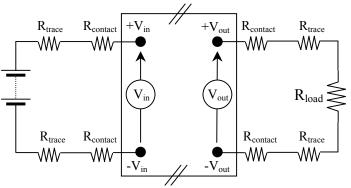
TEST SETUP:

The LV12S12-150 specifications are tested with the following configurations:

Regulation and Efficiency Setup

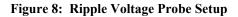
To ensure that accurate measurement are taken, the voltage measurements are taken directly at the terminal of the module. This minimizes errors due to contact and trace lengths between the load and the output of the supply. The following is a diagram of the test setup.

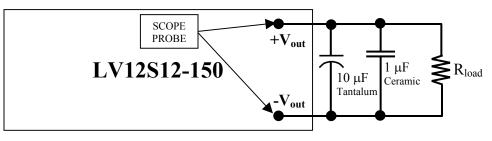
Figure 7: Regulation and Efficiency Probe Setup



Output Ripple Voltage Setup

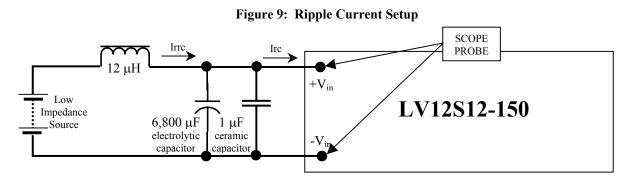
The module is tested with a 1uF ceramic capacitor in parallel with a 10uF tantalum capacitor across the output terminals.





Input Reflected Ripple Current and Input Ripple Current Setup

The module is tested for input reflected ripple current (Irrc) and input ripple current (Irc). The input ripple voltage is also measured at the pins with the following input filter. If there is a need to reduce input ripple current/voltage then additional ceramic capacitors can be added to the input of the converter.

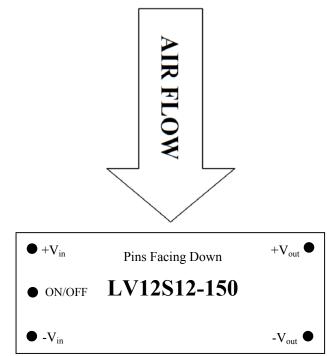




Converter Thermal Consideration

The converter is designed to operate without convective cooling if the derating curves are followed. The converter can operate at higher temperatures if airflow is applied. Airflow should be aligned lengthwise to the converter for optimum heat transfer. Contact Factory for derating curves.

Figure 10: Airflow Orientation







Company Information:

Wall Industries, Inc. has created custom and modified units for over 40 years. Our in-house research and development engineers will provide a solution that exceeds your performance requirements on-time and on budget. Our ISO9001-2000 certification is just one example of our commitment to producing a high quality, well documented product for our customers.

Our past projects demonstrate our commitment to you, our customer. Wall Industries, Inc. has a reputation for working closely with its customers to ensure each solution meets or exceeds form, fit and function requirements. We will continue to provide ongoing support for your project above and beyond the design and production phases. Give us a call today to discuss your future projects.

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