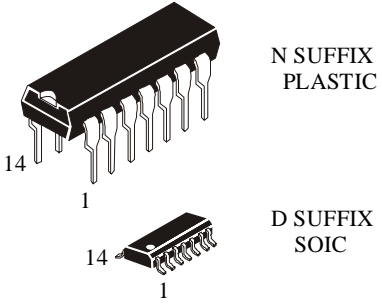


KK1488

Quadruple Line Drivers

The KK1488 is a monolithic quadruple line driver designed to interface data terminal equipment with data communication equipment in conformance with the specifications of EIA standard RS-232C.

- Meets specifications of EIA RS-232C
- Current limited output ± 10 mA Typical
- Power-off output impedance 300Ω Min
- Simple slew rate control by load capacitor
- Flexible operating supply range
- Input are TTL and DTL circuits compatible



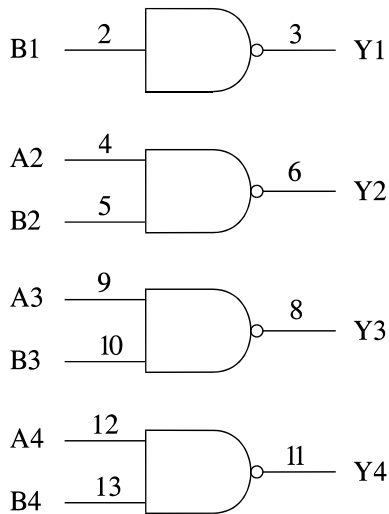
N SUFFIX
PLASTIC

D SUFFIX
SOIC

ORDERING INFORMATION
 KK1488N Plastic
 KK1488D SOIC

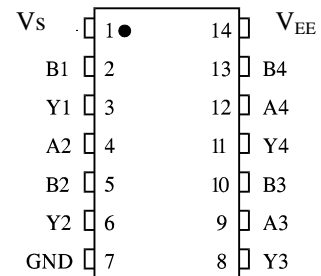
$T_A = 0^\circ$ to 70° C for all packages.

LOGIC DIAGRAM



PIN 1 = V_S
 PIN 14 = V_{EE}
 PIN 7 = GND

PIN ASSIGNMENT



FUNCTION TABLE

Inputs		Output
A	B	Y
H	H	L
L	X	H
X	L	H

X - don't care

MAXIMUM RATINGS*

Symbol	Parameter	Value	Unit
V _S	Supply Voltage	15	V
V _{EE}	Supply Voltage	- 15	V
V _I	Input Voltage Range	- 15 to 7	V
V _O	Output Voltage Range	-15 to 15	V
P _T	Continuous Total Dissipation at (or below) 25°C	1	W
T _{stg}	Storage Temperature Range	-65 to 150	°C

* Maximum Ratings are those values beyond damage to the device may occur. Functional operating should be restricted to the Recommended Operating Conditions.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V _S	Supply Voltage		15	V
V _{EE}	Supply Voltage	-15		V
V _{IL}	Low Level Input Voltage	1.9		V
V _{IH}	High Level Input Voltage		0.8	V
T _A	Operating Temperature, All Package Types	-10	70	°C

DC ELECTRICAL CHARACTERISTICS (T_A = -10°C to 70°C)

Symbol	Parameter	Test Conditions		Guaranteed Limits		Unit
				Min	Max	
V _{OH}	High-Level Output Voltage	V _{IL} =0.8V R _L =3kΩ	V _S =9V V _{EE} =-9V	6		V
			V _S =13.2V V _{EE} =13.2V	9		
V _{OL}	Low-Level Output Voltage	V _{IH} =1.9V R _L =3kΩ	V _S =9V V _{EE} =-9V	-6		V
			V _S =13.2V V _{EE} =-13.2V	-9		
I _{IH}	High-Level Input Current	V _I =5V V _S =9V, V _{EE} =-9V			10	μA
I _{IL}	Low-Level Input Current	V _I =0V V _S =9V, V _{EE} =-9V			-1.6	mA
I _{OS} *	Short-Circuit Output Current at High Level	V _I =0.8V V _O =0V V _S =9V, V _{EE} =-9V		-6	-12	mA
I _{OS} *	Short-Circuit Output Current at Low Level	V _I =1.9V V _O =0V V _S =9V, V _{EE} =-9V		6	12	mA
r _o	Output Resistance, power off	V _S =0V, V _{EE} =0V V _O =-3V or 3V		300		Ω
I _{OC+}	Supply Current from V _S	V _S =9V,	All inputs at 1.9V		20	mA
			All inputs at 0.8V		6	
		V _S =12V	All inputs at 1.9V		25	
			All inputs at 0.8V		7	
		V _S =15V T _A =25°C	All inputs at 1.9V		34	
			All inputs at 0.8V		12	
I _{OC-}	Supply Current from V _{EE}	V _{EE} =-9V,	All inputs at 1.9V		-17	mA
			All inputs at 0.8V		-0.015	
		V _{EE} =-12V	All inputs at 1.9V		-23	
			All inputs at 0.8V		-0.015	
		V _{EE} =-15V T _A =25°C	All inputs at 1.9V		-34	
			All inputs at 0.8V		-2.5	

* Not more than one output should be shorted at a time

AC ELECTRICAL CHARACTERISTICS($V_S = 9V, V_{EE} = -9V, T_A = 25^\circ C, t_r = t_f = 5ns$)

Symbol	Parameter	Test Condition	Guaranteed Limits		Unit
			Min	Max	
t_{PLH}	Propagation Delay Time, Low-to-High-Level Output	$R_L=3k\Omega, C_L=15pF$ See Figure 1		350	ns
t_{PHL}	Propagation Delay Time, High-to-Low-Level Output			175	ns
t_{TLH}	Transition Time, Low-to-High-Level Output *			100	ns
t_{THL}	Transition Time, High-to-Low-Level Output *			75	ns

* Measured between 10% and 90% points of output waveform.

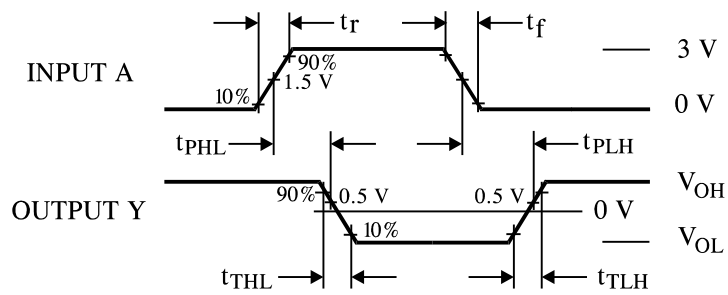
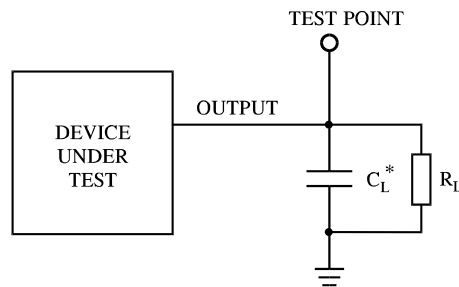


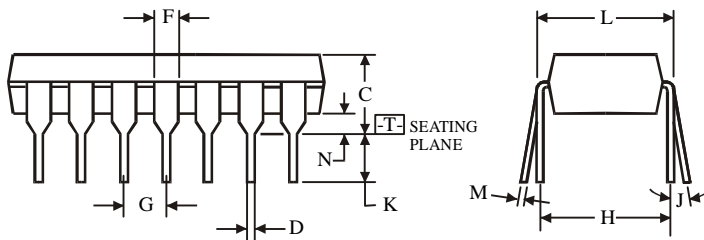
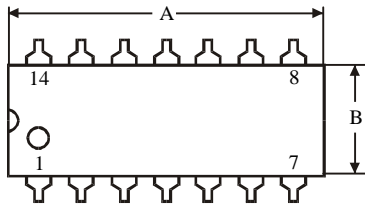
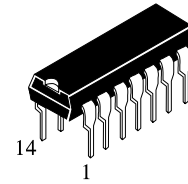
Figure 1. Switching Waveforms



* Includes all probe and jig capacitance

Figure 2. Test Circuit

**N SUFFIX PLASTIC DIP
(MS - 001AA)**



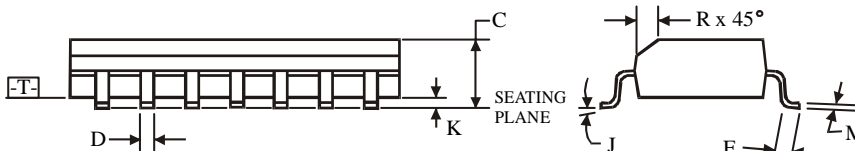
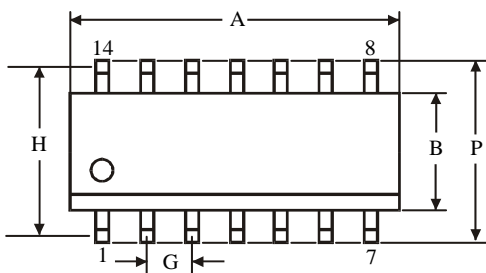
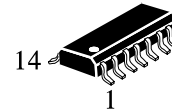
$\oplus 0.25 (0.010) \text{ (M) T}$

NOTES:

- Dimensions "A", "B" do not include mold flash or protrusions.
Maximum mold flash or protrusions 0.25 mm (0.010) per side.

Symbol	Dimension, mm	
	MIN	MAX
A	18.67	19.69
B	6.1	7.11
C		5.33
D	0.36	0.56
F	1.14	1.78
G	2.54	
H	7.62	
J	0°	10°
K	2.92	3.81
L	7.62	8.26
M	0.2	0.36
N	0.38	

**D SUFFIX SOIC
(MS - 012AB)**



$\oplus 0.25 (0.010) \text{ (M) T C (M)}$

NOTES:

- Dimensions A and B do not include mold flash or protrusion.
- Maximum mold flash or protrusion 0.15 mm (0.006) per side for A; for B - 0.25 mm (0.010) per side.

Symbol	Dimension, mm	
	MIN	MAX
A	8.55	8.75
B	3.8	4
C	1.35	1.75
D	0.33	0.51
F	0.4	1.27
G	1.27	
H	5.27	
J	0°	8°
K	0.1	0.25
M	0.19	0.25
P	5.8	6.2
R	0.25	0.5