Vishay High Power Products

Ultrafast Rectifier, 8 A FRED PtTM

MURB820-1

SЗ

Anode

25 ns

8 A

200 V

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TO-262

N/C



- · Ultrafast recovery time
- Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- · Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Peak repetitive reverse voltage	V _{RRM}		200	V	
Average rectified forward current	I _{F(AV)}	Total device, rated V_R , $T_C = 150 \ ^\circ C$	8		
Non-repetitive peak surge current	I _{FSM}		100	А	
Peak repetitive forward current	I _{FM}	Rated V _R , square wave, 20 kHz, T _C = 150 $^{\circ}$ C	16		
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	-		
Forward voltage	V _F	I _F = 8 A	-	-	0.975		
		I _F = 8 A, T _J = 150 °C	-	-	0.895		
Reverse leakage current	I _R	$V_{R} = V_{R}$ rated	-	-	5	μA	
		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	250		
Junction capacitance	CT	V _R = 200 V	-	25	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH	

MURB820

Base cathode

Q 2

D²PAK

PRODUCT SUMMARY

t_{rr}

I_{F(AV)}

 V_R

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N/C

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Anode

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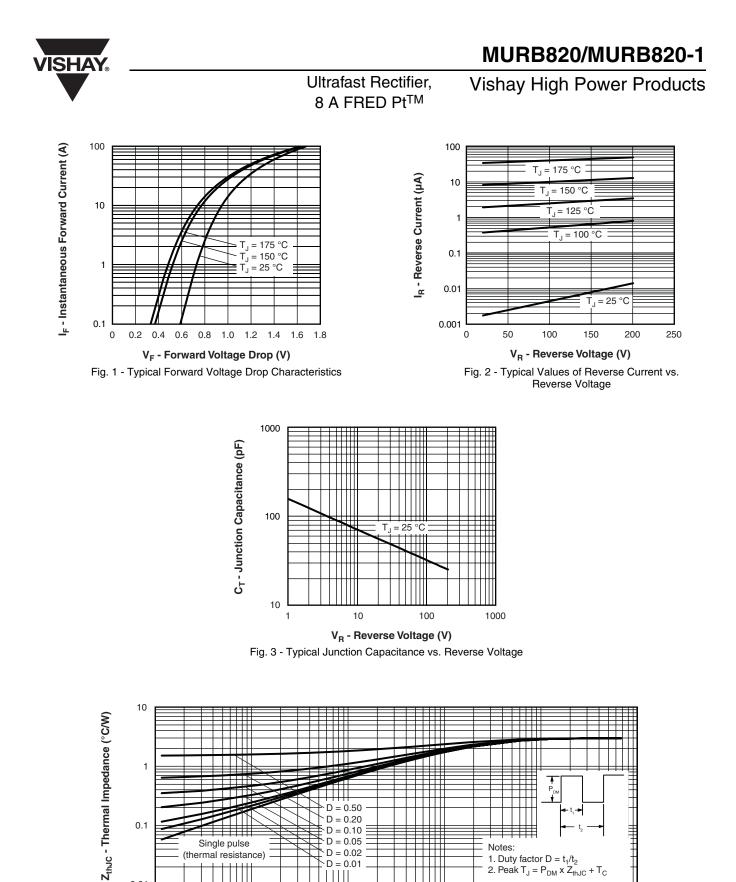
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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t _{rr}	$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, V_R = 30 \text{ V}$		-	-	35	
		$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{REC} = 0.25 \text{ A}$		-	-	25	
		T _J = 25 °C	I _F = 8 A dI _F /dt = 200 A/μs	-	20	-	ns
		T _J = 125 °C		-	34	-	
Peak recovery current		T _J = 25 °C		-	1.7	-	
	I _{RRM}	T _J = 125 °C	$V_{\rm B} = 160 \text{ V}$	-	4.2	-	
Reverse recovery charge	0	T _J = 25 °C	-n	-	23	-	nC
	Q _{rr}	T _J = 125 °C		-	75	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case	R _{thJC}		-	-	3.0	
Thermal resistance, junction to ambient	R _{thJA}		-	-	50	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2.0	-	g
			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)
Marking davias		Case style D ² PAK		MURB820		
Marking device		Case style TO-262		MURI	3820-1	



0.00001 0.0001 0.001 0.01 0.1 t₁ - Rectangular Pulse Duration (s)

Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

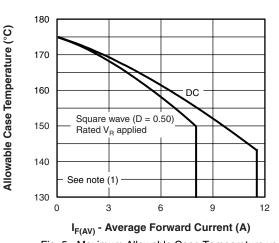
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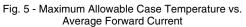
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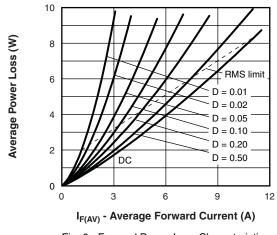


Fig. 6 - Forward Power Loss Characteristics

Note

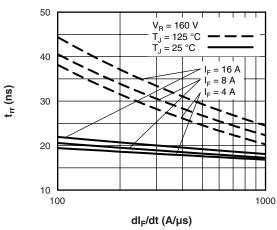
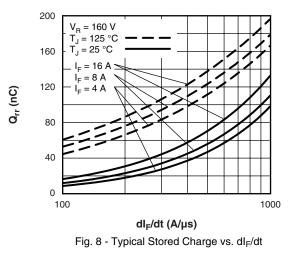


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt





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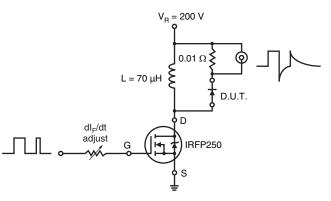


Fig. 9 - Reverse Recovery Parameter Test Circuit

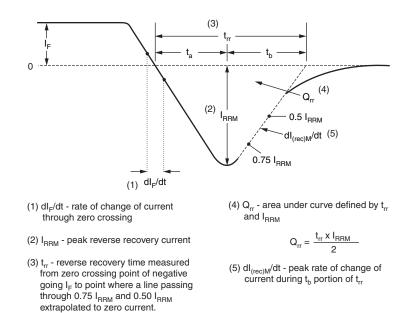


Fig. 10 - Reverse Recovery Waveform and Definitions

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Device code

MUR

1

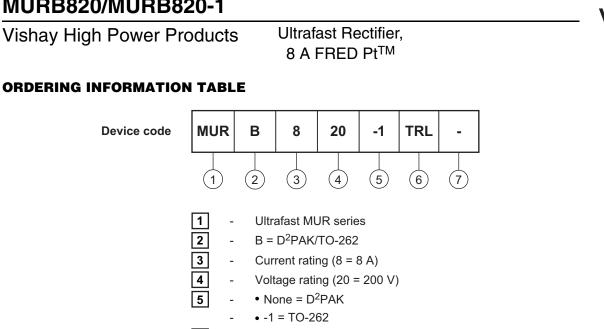
1

2

3 4 5

6

7



- -• None = Tube (50 pieces)
 - TRL = Tape and reel (left oriented, for D²PAK package)
 - TRR = Tape and reel (right oriented, for D²PAK package)

• None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95014			
Part marking information	http://www.vishay.com/doc?95008			
Packaging information	http://www.vishay.com/doc?95032			

ORDERING INFORMATION TABLE





Vishay

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