

## High voltage fast-switching NPN power transistor

#### **Features**

- High voltage capability
- Low spread of dynamic parameters
- Minimum lot-to-lot spread for reliable operation
- Very high switching speed
- Integrated antiparallel collector-emitter diode

### **Applications**

■ Electronic ballast for fluorescent lighting

#### **Description**

The device is manufactured using high voltage multi-epitaxial planar technology for high switching speeds and medium voltage capability. It uses a Cellular Emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA. The device is designed for use in lighting applications.

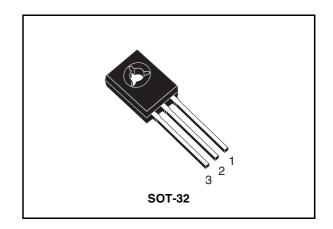


Figure 1. Internal schematic diagram

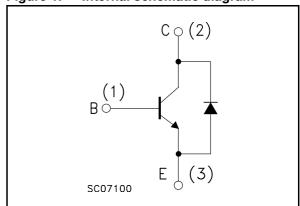


Table 1. Device summary

Order code	Marking	Package	Packaging
ST13003D-K	13003D	SOT-32	cardboard box

Electrical ratings ST13003D-K

# 1 Electrical ratings

Table 2. Absolute maximum rating

Symbol	Parameter	Value	Unit
V <sub>CES</sub>	Collector-emitter voltage (V <sub>BE</sub> = 0)	700	V
V <sub>CEO</sub>	Collector-emitter voltage (I <sub>B</sub> = 0)	400	V
V <sub>EBO</sub>	Emitter-base voltage $(I_C = 0, I_B = 0.75A, tp < 10\mu s)$	V <sub>(BR)EBO</sub>	V
I <sub>C</sub>	Collector current	1.5	Α
I <sub>CM</sub>	Collector peak current (t <sub>P</sub> < 5ms)	3	Α
I <sub>B</sub>	Base current	0.75	Α
I <sub>BM</sub>	Base peak current (t <sub>P</sub> < 5ms)	1.5	Α
P <sub>tot</sub>	Total dissipation at T <sub>c</sub> = 25°C	40	W
T <sub>stg</sub>	Storage temperature	-55 to 150	°C
TJ	Max. operating junction temperature	150	°C

## 2 Electrical characteristics

 $(T_{case} = 25^{\circ}C \text{ unless otherwise specified})$ 

Table 3. Electrical characteristics

Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
I <sub>CEV</sub>	Collector cut-off current (V <sub>BE</sub> =-1.5V)	V <sub>CE</sub> =700V V <sub>CE</sub> =700V	T <sub>c</sub> = 125°C			1 5	mA mA
V <sub>(BR)EBO</sub>	Emitter-Base breakdown voltage (I <sub>C</sub> = 0)	I <sub>E</sub> =10mA		9		18	V
V <sub>CEO(sus)</sub> (1)	Collector-emitter sustaining voltage (I <sub>B</sub> = 0)	I <sub>C</sub> =10mA		400			٧
V <sub>CE(sat)</sub> (1)	Collector-emitter saturation voltage	$I_{C} = 0.5A$ $I_{C} = 1A$ $I_{C} = 1.5A$	$I_B = 0.25A$			0.5 1 3	V V V
V <sub>BE(sat)</sub> (1)	Base-emitter saturation voltage	$I_{C} = 0.5A$ $I_{C} = 1A$	_			1 1.2	V V
h <sub>FE</sub>	DC current gain	I <sub>C</sub> =0.5A I <sub>C</sub> =1A	V <sub>CE</sub> =2V V <sub>CE</sub> =2V	8 5		20 25	
t <sub>r</sub> t <sub>s</sub>	Resistive load Rise time Storage time Fall time	$V_{CC} = 125V$ $I_{B1} = 0.2A$ $T_p = 25\mu s$	•			1 4 0.7	µs µs µs
t <sub>s</sub>	Inductive load Storage time	$I_C = 1A$ $V_{BE} = -5V$ $V_{Clamp} = 300V$	L=50mH		0.8		μs
V <sub>F</sub>	Diode forward voltage	I <sub>C</sub> = 0.5A				1.5	V

Note (1) Pulsed duration = 300µs, duty cycle ⊴.5%

### 2.1 Test circuits

Figure 2. Inductive load switching test circuit

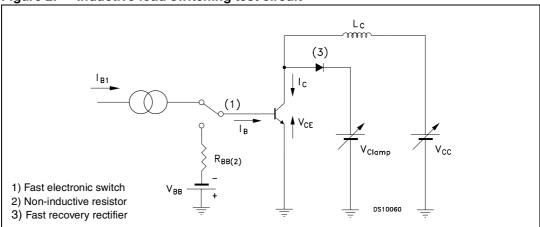
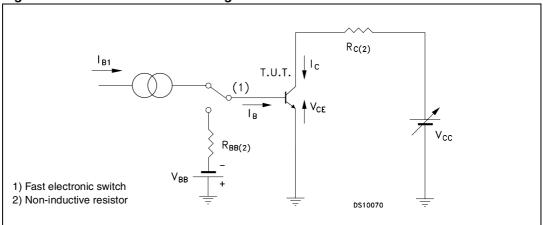


Figure 3. Resistive load switching test circuit

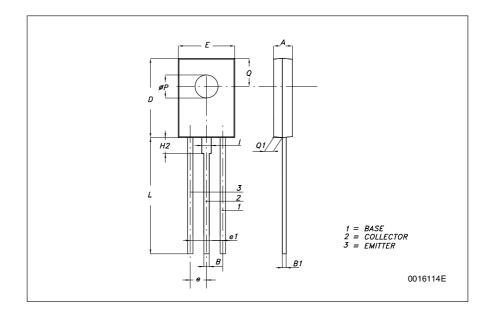


## 3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

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DIM.	mm.			
DIWI.	MIN.	TYP	MAX.	
Α	2.40		2.90	
В	0.64		0.88	
B1	0.39		0.63	
D	10.5		11.05	
E	7.40		7.80	
е	2.04	2.29	2.54	
e1	4.07	4.58	5.08	
L	15.3		16	
Р	2.90		3.20	
Q		3.80		
Q1	1		1.52	
H2		2.15		
I		1.27		



ST13003D-K Revision history

# 4 Revision history

Table 4. Revision history

Date	Revision	Changes
15-Nov-2007	1	Initial release.

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