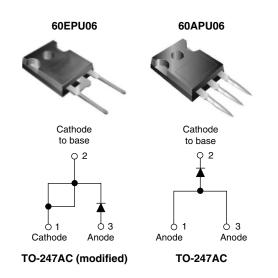


Vishay High Power Products

Ultrafast Soft Recovery Diode, 60 A FRED PtTM



PRODUCT SUMMARY				
t _{rr} (typical)	34 ns			
I _{F(AV)}	60 A			
V_{R}	600 V			

FEATURES

- · Ultrafast recovery
- 175 °C operating junction temperature
- · Designed and qualified for industrial level

BENEFITS

- Reduced RFI and EMI
- · Higher frequency operation
- · Reduced snubbing
- · Reduced parts count

DESCRIPTION/APPLICATIONS

These diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems.

The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for HF welding, power converters and other applications where switching losses are not significant portion of the total losses.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Cathode to anode voltage	V _R		600	V		
Continuous forward current	I _{F(AV)}	T _C = 116 °C	60			
Single pulse forward current	I _{FSM}	T _C = 25 °C	600	Α		
Maximum repetitive forward current	I _{FRM}	Square wave, 20 kHz	120			
Operating junction and storage temperatures	T _J , T _{Stg}		- 55 to 175	°C		

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _r	I _R = 100 μA	600	-	-	
Forward voltage V _F		I _F = 60 A	-	1.35	1.68	V
	V_{F}	I _F = 60 A, T _J = 125 °C	-	1.20	1.42	
	I _F = 60 A, T _J = 175 °C	-	1.11	1.30		
Reverse leakage current	I _R	$V_R = V_R$ rated	-	-	50	μΑ
		$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	-	500	
Junction capacitance	C _T	V _R = 600 V	-	39	-	pF

60EPU06/60APU06

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
	Reverse recovery time t _{rr}	$I_F = 1 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	34	45	
Reverse recovery time		T _J = 25 °C		-	81	-	ns
		T _J = 125 °C	$I_F = 60 \text{ A}$ $dI_F/dt = 200 \text{ A}/\mu\text{s}$ $V_R = 200 \text{ V}$	-	164	-	
Peak recovery current I _{RRM}	I _{RRM}	T _J = 25 °C		-	7.4	-	Α
		T _J = 125 °C		-	17.0	-	_ ^
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	300	-	nC
		T _J = 125 °C		-	1394	-	IIC

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Thermal resistance, junction to case	R _{thJC}		-	-	0.63	K/W		
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.2	-			
Weight			-	5.5	-	g		
		-	0.2	-	OZ.			
Mounting torque			1.2 (10)	-	2.4 (20)	N ⋅ m (lbf ⋅ in)		
		Case style TO-247AC modified		60EPU06				
Marking device		Case style TO-247AC		60APU06				



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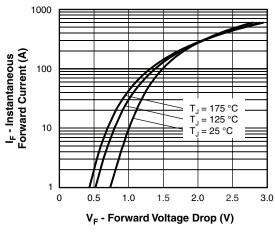


Fig. 1 - Typical Forward Voltage Drop Characteristics

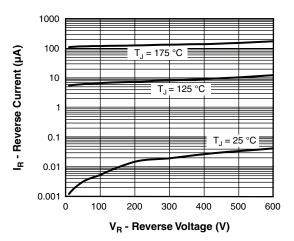


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

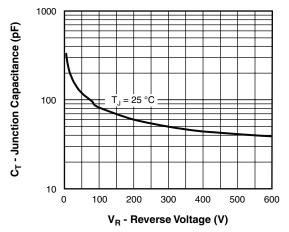


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

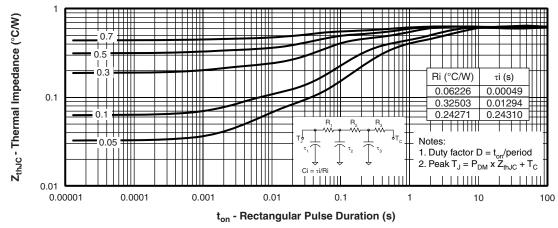
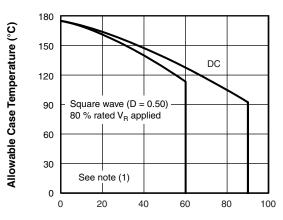


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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 $I_{F(AV)}$ - Average Forward Current (A)

Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

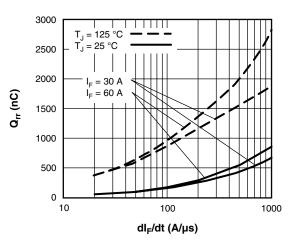


Fig. 7 - Typical Stored Charge vs. dl_F/dt

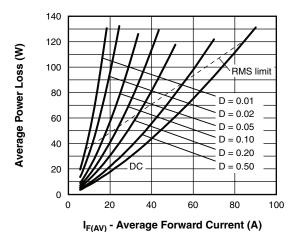


Fig. 6 - Forward Power Loss Characteristics

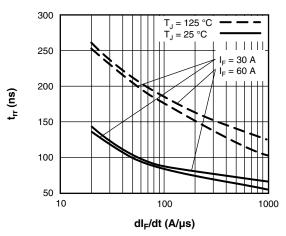


Fig. 8 - Typical Reverse Recovery Time vs. dl_F/dt

Note

 $^{(1)}$ Formula used: T $_{C}$ = T $_{J}$ - (Pd + Pd $_{REV}$) x R $_{th,JC}$; Pd = Forward power loss = I $_{F(AV)}$ x V $_{FM}$ at (I $_{F(AV)}$ /D) (see fig. 6); Pd $_{REV}$ = Inverse power loss = V $_{R1}$ x I $_{R}$ (1 - D); I $_{R}$ at V $_{R1}$ = 80 % rated V $_{R}$



Ultrafast Soft Recovery Diode, 60 A FRED PtTM

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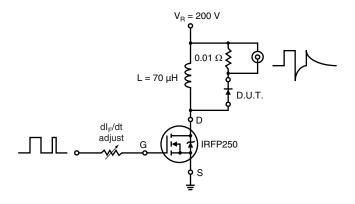
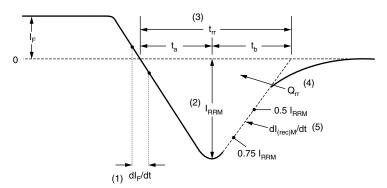


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

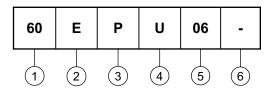
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ORDERING INFORMATION TABLE

Device code



- 1 Current rating (60 = 60 A)
- 2 Circuit configuration:

E = Single diode, 2 pins

A = Single diode, 3 pins

Package:

P = TO-247AC modified

4 - Type of silicon:

U = Ultrafast recovery

5 - Voltage rating (06 = 600 V)

6 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95001				
Part marking information	http://www.vishay.com/doc?95006			



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