



5 - TVS/ZENER ARRAY FOR ESD AND LATCH-UP PROTECTION

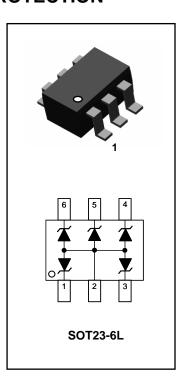
This Quad TVS/Zener Array family have been designed to Protect Sensitive Equipment against ESD and to prevent Latch-Up events in CMOS circuitry operating in the 5V. This TVS array offers an integrated solution to protect up to 5 data lines where the board space is a premium.

SPECIFICATION FEATURES

- 150W (8/20µs), 24W (10/1000µs) Power Dissipation
- Low Leakage Current, Maximum of 2µA at rated voltage
- Very Low Clamping Voltage
- IEC61000-4-2 ESD 20kV air, 15kV Contact Compliance
- Industry Standard Surface Mount Package SOT23-6L
- 100% Tin Matte Finish (RoHS Compliant)

APPLICATIONS

- Personal Digital Assistant (PDA)
- SIM Card Port Protection (Mobile Phone)
- Portable Instrumentation
- Mobile Phones and Accessories
- Memory Card Port Protection



Marking Code: "Q5A"

MAXIMUM RATINGS (Per Device)

Rating	Symbol	Value	Units
Peak Pulse Power (8/20µs Waveform)	P _{pp}	150	W
ESD Voltage (HBM)	V _{ESD}	25	kV
Operating Temperature Range	ТЈ	-55 to +150	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

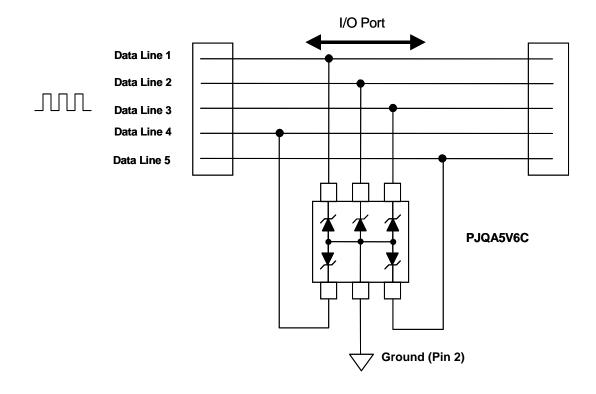
ELECTRICAL CHARACTERISTICS (Per Device) Tj = 25°C PJQA5V6C

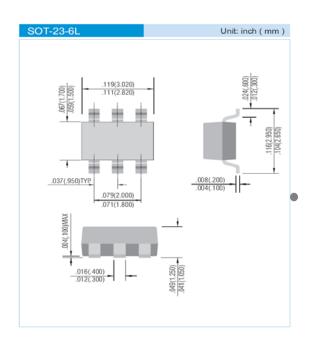
Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	V_{WRM}				3.0	V
Reverse Breakdown Voltage	V_{BR}	I _{BR} =1 mA	5.3	5.6	5.88	V
Reverse Leakage Current	I _R	V _R = 3.0V			2	μΑ
Clamping Voltage (8/20µs)	Vc	I _{pp} = 5 Amps			7.5	V
Clamping Voltage (8/20µs)	V _c	I _{pp} = 10 Amps			9.1	V
Off State Junction Capacitance	Cj	0 Vdc Bias f = 1MHz Between I/O pins and pin 2,5			265	pF
Off State Junction Capacitance	Cj	3 Vdc Bias f = 1MHz Between I/O pins and pin 2,5			170	pF

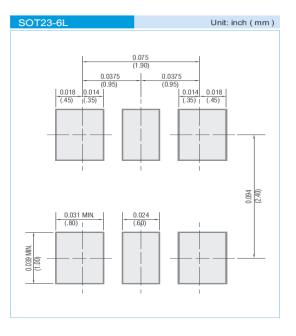




TYPICAL APPLICATION EXAMPLE AND PACKAGE DIMENSIONS







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