ECLIPTEK® CORPORATION

Marketing Bulletin

DATE: Saturday, April 01, 2000

TO: Affected Customers

FROM: Marketing

RE: ECH11 Series Termination

To all concerned parties,

This bulletin is to notify all customers of the discontinuation of the ECH11 series Ecliptek oscillator effective Saturday, April 01, 2000.

In compliance with our End of Life (EOL) policy, this notice will serve as advanced notice of product termination. New orders will not be accepted after Saturday, July 01, 2000, with delivery to be conclude by Saturday, September 30, 2000.

The EH11 series is a recommended alternate for the ECH11 series. This may not be an exact cross, so it is highly recommended that the data sheet(s) of the recommended alternate are reviewed and samples tested to ensure conformance.

If there are any questions pertaining to this bulletin, please contact your Ecliptek sales representative. Thank you again for your cooperation.

Ecliptek Marketing

3545 CADILLAC AVENUE · COSTA MESA, CA 92626-1401 · (714) 433-1200 · Fax (714) 433-1234

STANDARD SPECIFICATIONS

Frequency Range:	70.000MHz to 155.520MHz
Frequency Tolerance/Stability:	(All Values Inclusive of Operating Temp. Range, Supply Voltage, and Load)
00	±100ppm Max.
45	±50ppm Max.
25	±25ppm Max.
20	±20ppm Max. (0°C to +70°C only)
Operating Temperature Range	0°C to +70°C
ET	-40°C to +85°C
Storage Temperature Range	-55°C to +125°C
Supply Voltage	5.0Vdc ±10%
Input Current	50mA Maximum
Output Voltage Logic High	2.4Vdc Min. w/TTL Load, VDD-0.5Vdc Min. w/HCMOS Load
Output Voltage Logic Low	0.4Vdc Max. w/TTL Load, 0.5Vdc Max. w/HCMOS Load
Rise/Fall Time	3nSec (0.4Vdc to 2.4Vdc w/TTL Load, 20% to 80% of waveform w/HCMOS Load)
Duty Cycle	50% ±10% (@ 1.4Vdc w/TTL Load, @ 50% of waveform w/HCMOS Load)
T	50% ±5% (@ 1.4Vdc w/TTL Load or 15pF HCMOS Load)
Load Drive Capability	10TTL Load or 50pF HCMOS Load
Start Up Time	10 mSec Maximum
Aging @ 25°C	±5ppm/year
Pin 1 Connection	No Connect
TS	Tri-State (High Impedance)
Tri-State Input Voltage (VIH & VIL)	+2.4Vdc Min. to Enable Output, +0.8Vdc Max. to Disable Output (High Impedance), No Connect to Enable Output
Absolute Clock Jitter	±200pSec Maximum
One Sigma Clock Period Jitter	±50pSec Maximum

ORIGINAL IF IN RED

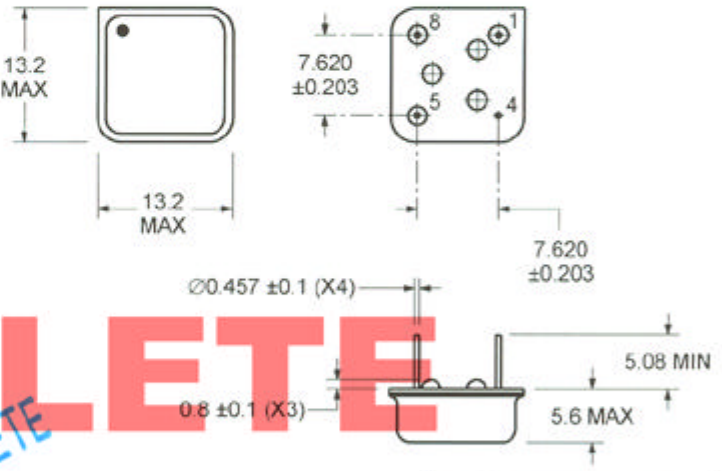
ENVIRONMENTAL & MECHANICAL

Shock:	Conditions and Criteria Listed in TQC41-883-007
Vibration:	Conditions and Criteria Listed in TQC41-883-008
Seal Integrity:	Conditions and Criteria Listed in TQC41-883-003
Solderability:	Conditions and Criteria Listed in TQC41-883-004 / 95% coverage
Marking Permenancy:	Conditions and Criteria Listed in TQC41-883-001

PART NUMBERING GUIDE

ECH11 **00** HS **ET** **T** **TS** - **70.000M** - **CL125**

- **CL125** — See Configuration Options in Table Below
- **70.000M** — Frequency
- **TS** — Pin 1 Connection: Blank = No Connect, TS = Tri-State Enable High
- **T** — Duty Cycle: Blank = 50 ±10%, T = 50 ±5%
- **ET** — Operating Temperature Range: Blank = 0°C to +70°C, ET = -40°C to +85°C
- **00** — Frequency Tolerance/Stability: 00 = ±100ppm Maximum, 45 = ±50ppm Maximum, 25 = ±25ppm Maximum, 20 = ±20ppm Maximum



PIN	CONNECTION
1	No Connect or Tri-state
4	Ground/Case Ground
5	Output
8	Supply Voltage

ALL DIMENSIONS IN MILLIMETERS

MARKING GUIDE

(Line #1) **ECLIPTEK**

(Line #2) **ECH11 TS**
- **TS** — Pin 1 Connection: Blank = No Connect, TS = Tri-State Enable High

(Line #3) **XX.XXXM**
- **XX.XXXM** — Frequency

(Line #4) **XX Y ZZ**
- **ZZ** — Week of Year
- **Y** — Last Digit of Year
- **XX** — Ecliptek Manufacturing Code Per TEN02-001-000

NOTE: Pin 1 shall be marked with a black dot. Marking shall conform to conditions listed in TQC41-001-000.

SPECIFICATION CONTROL DRAWING

ECLIPTEK® CORPORATION

Drawing Number: CSC02-200-000

Title: HALF SIZE HIGH FREQUENCY OSCILLATOR

CONFIGURATION OPTIONS

CLXXX = Cut Leads (MAL01-101-000)
G = Gull Wing (MAL02-001-000) G2 = Gull Wing (MAL02-011-000)

STANDARD SPECIFICATIONS

Frequency Range:	70.000MHz to 155.520MHz
Frequency Tolerance/Stability:	(All Values Inclusive of Operating Temp. Range, Supply Voltage, and Load)
00	±100ppm Max.
45	±50ppm Max.
25	±25ppm Max.
20	±20ppm Max. (0°C to +70°C only)
Operating Temperature Range	0°C to +70°C
ET	-40°C to +85°C
Storage Temperature Range	-55°C to +125°C
Supply Voltage	5.0Vdc ±10%
Input Current	50mA Maximum
Output Voltage Logic High	2.4Vdc Min. w/TTL Load, VDD-0.5Vdc Min. w/HCMOS Load
Output Voltage Logic Low	0.4Vdc Max. w/TTL Load, 0.5Vdc Max. w/HCMOS Load
Rise/Fall Time	3nSec (0.4Vdc to 2.4Vdc w/TTL Load, 20% to 80% of waveform w/HCMOS Load)
Duty Cycle	50% ±10% (@ 1.4Vdc w/TTL Load, @ 50% of waveform w/HCMOS Load)
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Start Up Time	10 mSec Maximum
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Pin 1 Connection	No Connect
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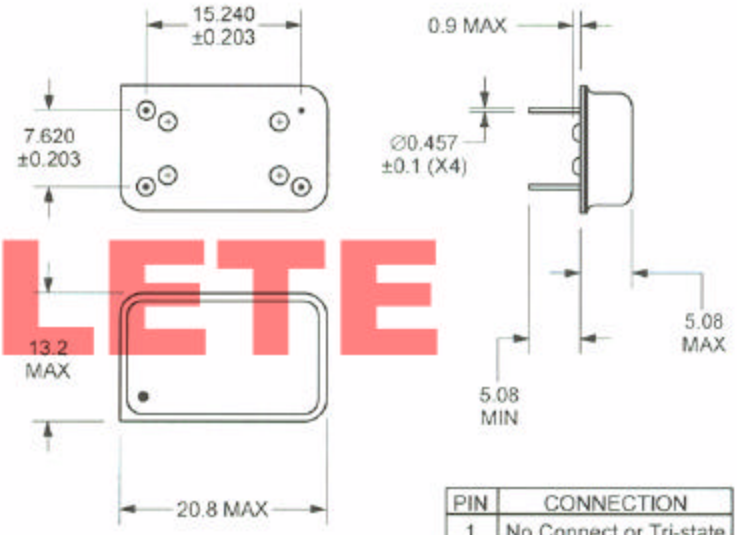
PART NUMBERING GUIDE

ECH11 00 ET T TS - 70.000M - CL125

- **See Configuration Options in Table Below**
- **Frequency**
- **Pin 1 Connection**
  Blank = No Connect
  TS = Tri-State Enable High
- **Duty Cycle**
  Blank = 50 ±10%, T = 50 ±5%
- **Operating Temperature Range**
  Blank = 0°C to +70°C, ET = -40°C to +85°C
- **Frequency Tolerance/Stability**
  00 = ±100ppm Maximum, 45 = ±50ppm Maximum,
  25 = ±25ppm Maximum, 20 = ±20ppm Maximum

OBSOLETE



ALL DIMENSIONS IN MILLIMETERS

PIN	CONNECTION
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8	Output
14	Supply Voltage

MARKING GUIDE

OBSOLETE

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SPECIFICATION CONTROL DRAWING

ECLIPTEK® CORPORATION

Drawing Number
CSC01-200-000

Title
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