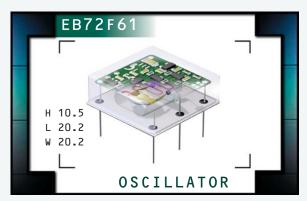
EB72F61 Series

- Oven Controlled Crystal Oscillator (OCXO)
- AT-Cut Crystal
- HCMOS output
- 3.3V supply voltage
- 5 pin DIP package
- External control voltage
- Stability to ±80ppb





ELECTRICAL SPECIFICATIONS

| Frequency Ra | • | 10.000MHz, 12.288MH | z, 12.800MHz, 16.000M | | | | | |
|---|---|---|--|--|--|----------|--|--|
| | perature Range (OTR) | | | 0° C to 50° C, 0° C to 70° C, or -20° C to 70° C | | | | |
| | erature Range | -55°C to 12 | -55°C to 125°C | | | | | |
| Supply Voltage (V _{DD}) | | | | 3.3V _{DC} ±5% | | | | |
| | erance / Stability | | | | | | | |
| vs. Initial Tole | | at Nominal V_{DD} and V_{C} , | at 25°C | | ±1.0ppm or ±500ppb Maximum | | | |
| vs. Temperature Stability | | at Nominal V_{DD} and V_{C} | | ±80ppb, ±3 | ±80ppb, ±100ppb, ±200ppb, ±280ppb, or | | | |
| | | | | ±500ppb M | laximum | | | |
| vs. Vdd | s. Vdd V _{DD} ±5% | | ±20ppb Ma | ±20ppb Maximum | | | | |
| vs. Load | | Vload ±5% | | ±20ppb Ma | ±20ppb Maximum | | | |
| vs. Aging (1 D | ay) | after 72 Hours of Oper | ation | ±3.0ppb M | ±3.0ppb Maximum | | | |
| vs. Aging (1 Ye | ear) | after 72 Hours of Oper | ation | ±500ppb M | ±500ppb Maximum | | | |
| vs. Aging (10) | Years) | after 72 Hours of Oper | ation | ±3.0ppm M | ±3.0ppm Maximum | | | |
| Crystal Cut | | | | AT-Cut | AT-Cut | | | |
| Warm Up Time | е | to ±500ppb of Final Free | quency at 1 Hour at 25°C | 3 Minutes I | 3 Minutes Maximum | | | |
| Power Consumption | | at Steady State, at 25° | ,C | 1.2Watts M | 1.2Watts Maximum | | | |
| | | During Warm Up, at 25 | J°C | 3.6Watts M | 3.6Watts Maximum | | | |
| Output Voltage Logic High (V _{OH}) I _{OH} = -4mA | | | | 2.6V _{DC} Mini | 2.6V _{DC} Minimum | | | |
| Output Voltag | Output Voltage Logic Low (V_{0L}) $I_{0L} = +4mA$ | | | | 0.4V _{DC} Maximum | | | |
| Rise Time / Fa | ise Time / Fall Time Measured at 20% to 80% of Waveform | | | | 6nSec Maximum | | | |
| Duty Cycle | | Measured at 50% of Wa | aveform | 50 ±5(%) | 50 ±5(%) | | | |
| Load Drive Ca | pability | | | 15pF HCMC | S Load Maximum | | | |
| Frequency De | viation | Referenced to F_0 at $V_c = 1$ | $1.65V_{DC}$; $V_{DD}=5.0V_{DC}$ over 0 | r OTR ±5ppm Minimum | | | | |
| Control Voltage Range | | | | $0.0V_{DC}$ to V_{DD} | | | | |
| Control Voltag | ge (V _c) | | | 1.65V _{DC} ±1. | .65V _{DC} | | | |
| Transfer Func | tion | | | | Positive Transfer Characteristic | | | |
| Reference Vol | tage Output | | | 2.8V _{DC} ±0.2 | 2.8V _{DC} ±0.2V _{DC} (Pin 5) | | | |
| Linearity | | | | ±10% Maxi | mum | | | |
| Input Impeda | ince | | | 10k0hms T | 10kOhms Typical | | | |
| Typical Phase Noise (at 12.800MHz) 1Hz Offset | | | | -70dBc/Hz | -70dBc/Hz | | | |
| 10Hz Offset 100Hz Offset | | | | -95dBc/Hz -120dBc/Hz | | | | |
| | | | | | | | | |
| | | , | -135dBc/Hz | | | | | |
| | | 10kHz Offset | | -140dBc/H | | | | |
| MANUFACTURER | CATEGORY | SERIES | PACKAGE | VOLTAGE | CLASS | REV = DA | | |
| ECLIPTEK CORP. | OSCILLATOR | EB72F61 | 5 pin DIP | 3.3V | OS2C | 05/07 | | |

PART NUMBERING GUIDE

EB72F61 D 10 B V 2 - 20.000M

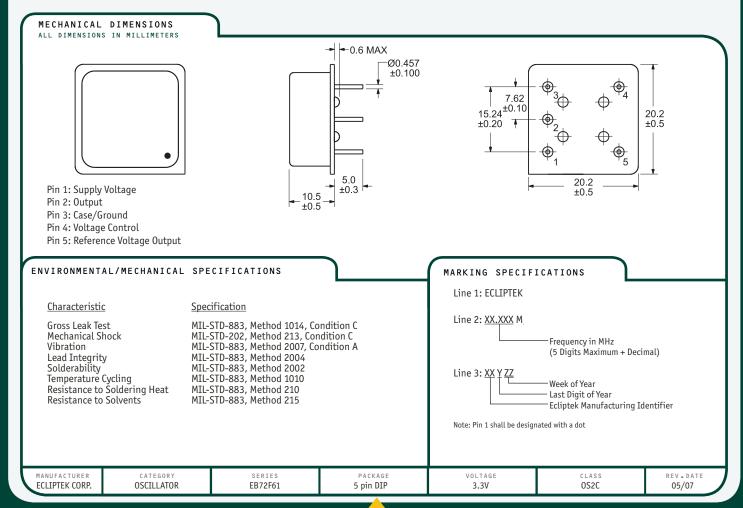
INITIAL TOLERANCE
C=±1.0ppm
D=±500ppb

FREQUENCY STABILITY
2 Digit Code Per Table 1

OPERATING TEMPERATURE RANGE

1 Letter Code Per Table 1

| | TABLE 1: PART NUMBERING CODES | | | | | | | | | | | | | |
|-----------------------|-------------------------------|--|--------|---------|---------|---------|---------|--|--|--|--|--|--|--|
| Range | | FREQUENCY STABILITY X Denotes availability | | | | | | | | | | | | |
| ature | | | ±80ppb | ±100ppb | ±200ppb | ±280ppb | ±500ppb | | | | | | | |
| nper | | Code | 08 | 10 | 20 | 28 | 50 | | | | | | | |
| g Tel | 0°C to +50°C | А | Х | Х | Х | Х | Х | | | | | | | |
| Operating Temperature | 0°C to +70°C | В | | Х | Х | Х | Х | | | | | | | |
| o | -20°C to +70°C | С | | | | Х | Х | | | | | | | |



FREQUENCY

DUTY CYCLE 2=50% ±5%

VOLTAGE CONTROL OPTION

Voltage Output on Pin 5

V=Voltage Control on Pin 4 and Reference