

5.0V/3.0V Plastic HCMOS/TTL Oscillator



Model: FSOLF Series

RoHS Compliant / Pb Free

Rev. 9/18/2006

http://www.foxonline.com/need_a_sample.htm



Discontinued

FEATURES

- 5.0V / 3.3V Operation
- HCMOS/TTL Output
- Tri-State Enable/Disable
- Extended Temperature Range
- Tape and Reel (1,000 pcs. STD)

Learn more about:
[Part Marking Identification](#)
[Tape and Reel Specification](#)
[Mechanical Specification](#)

Internet required

• PART NUMBER SELECTION [Learn More](#) - Internet Required

Part Number	Model Number	V _{DD} (V)	Frequency Stability ¹	Operating Temperature (°C)	Frequency Range (MHz)
259LF-Freq-xxxxx	FSO-2LF ³	5.0 ± 0.5	±100PPM	-20 ~ +70	1.000 ~ 66.6667
547LF-Freq-xxxxx	FSO-2RLF		+100PPM	-40 ~ +85	1.000 ~ 66.6667
432LF-Freq-xxxxx	FSO-25LF		±50PPM	-20 ~ +70	1.000 ~ 66.6667
433LF-Freq-xxxxx	FSO-3LF ³	3.3 ± 0.3	±100PPM	-20 ~ +70	1.000 ~ 66.6667
543LF-Freq-xxxxx	FSO-3RLF		±100PPM	-40 ~ +85	1.000 ~ 66.6667
434LF-Freq-xxxxx	FSO-35LF		±50PPM	-20 ~ +70	1.000 ~ 66.6667

• ELECTRICAL CHARACTERISTICS

PARAMETERS	FSO-2LF Series	FSO-3LF Series
	MAX (unless otherwise noted)	
Frequency Range (F _o)	1.000 ~ 66.6667 MHz	
Input Current (I _{DD})		
1.000 ~ 30.000 MHz	23mA	9mA
30.000+ ~ 66.6667 MHz	35mA	20mA
Output Symmetry (50% V _{DD})	40% ~ 60%	
Rise Time (20% ~ 80% V _{DD}) (T _R)		
1.000 ~ 30.000 MHz	8nS	6nS
30.000+ ~ 66.6667 MHz	7nS	6nS
Fall Time (80% ~ 20% V _{DD}) (T _F)		
1.000 ~ 30.000 MHz	8nS	6nS
30.000+ ~ 66.6667 MHz	7nS	6nS
Output Voltage (V _{OL})	0.4V	
(V _{OH})	4.6V Min	2.9V Min
Output Current (I _{OL})	16mA Min	
(I _{OH})	-16mA Min	
Output Load (TTL)	10TTL	
(HCMOS)	50pF	
Start-up Time (T _S)		
1.000 ~ 30.000 MHz	4mS	
30.000+ ~ 66.6667 MHz	10mS	
Enable/Disable Time ²	100nS	

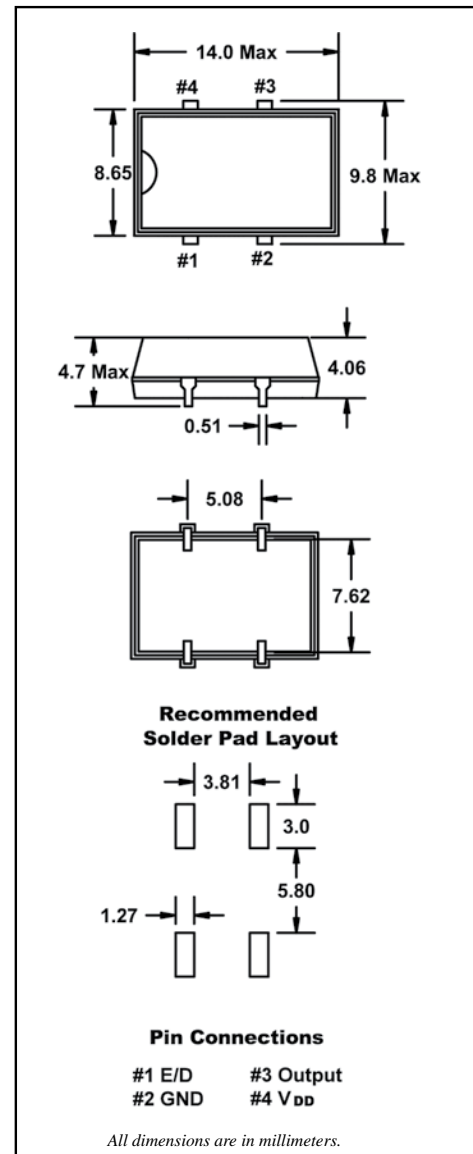
¹ Inclusive of 25°C tolerance, operating temperature range, input voltage change, aging, and load change.

² An internal pullup resistor from pin 1 to pin 4 allows active output if pin 1 is left open.

³ The FSO-2LF and FSO-3LF models will also operate over -40°C ~ +85°C, but with a stability of ±200 PPM.

Note: A 0.01µF bypass capacitor should be placed between V_{DD} (Pin 4) and GND (Pin 2) to minimize power supply line noise.

All specifications subject to change without notice.



• ENABLE / DISABLE FUNCTION

INH (Pin 1)	OUTPUT (Pin 3)
OPEN ²	ACTIVE
'1' Level V _{IH} ≥ 2.4 V	ACTIVE
'0' Level V _{IL} ≤ 0.6 V	High Z