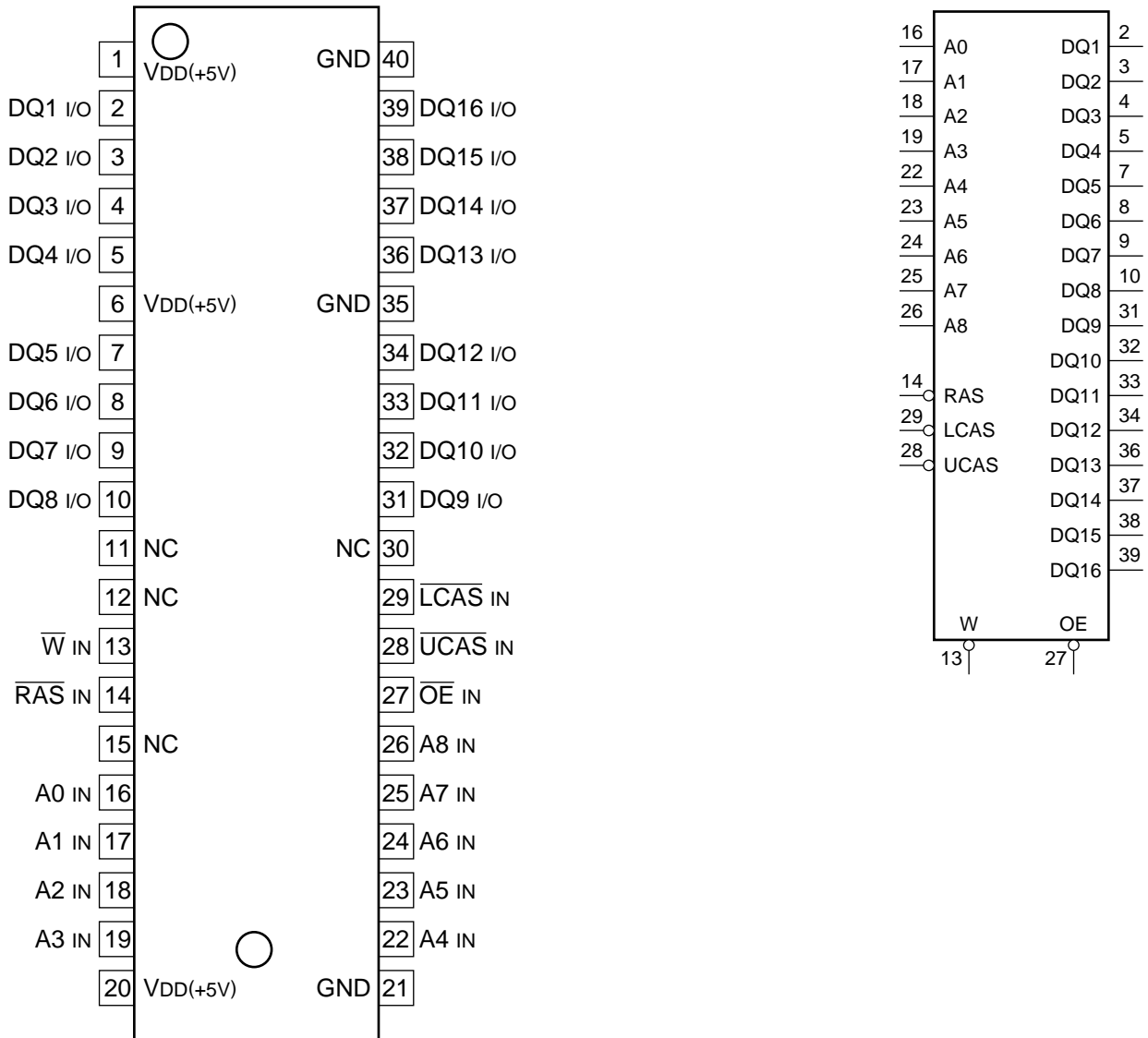


\*\*\*\*\*

C-MOS 4M (256K X 16)-BIT DYNAMIC RAM WITH FAST PAGE MODE  
-TOP VIEW-



- A0 - A8 ; ADDRESS INPUTS
- $\bar{RAS}$  ; ROW ADDRESS STROBE INPUT
- $\bar{UCAS}$  ; UPPER COLUMN ADDRESS STROBE INPUT
- $\bar{LCAS}$  ; LOWER COLUMN ADDRESS STROBE INPUT
- $\bar{W}$  ; READ / WRITE INPUT
- $\bar{OE}$  ; DATA OUTPUT ENABLE INPUT
- DQ1 - DQ16 ; DATA INPUTS / OUTPUTS

RAS	LCAS	UCAS	W	OE	DQ1 - DQ8	DQ9 - DQ16	STATE
1	1	1	1	1	HI-Z	HI-Z	STANDBY
0	1	1	1	1	HI-Z	HI-Z	REFRESH
0	0	1	1	0	DQ-OUT	HI-Z	LOWER BYTE READ
0	1	0	1	0	HI-Z	DQ-OUT	UPPER BYTE READ
0	0	0	1	0	DQ-OUT	DQ-OUT	WORD READ
0	0	1	0	1	DQ-IN	X	LOWER BYTE WRITE
0	1	0	0	1	X	DQ-IN	UPPER BYTE WRITE
0	0	0	0	1	DQ-IN	DQ-IN	WORD WRITE
0	0	0	1	1	HI-Z	HI-Z	—

0 ; LOW LEVEL  
 1 ; HIGH LEVEL  
 X ; DON'T CARE  
 HI-Z ; HIGH IMPEDANCE

