

High-performance $HDMI^{TM}$ 1.1 and 1.2 Compatible Signal Switch w/ Integrated Side-band Signal Support

Features

- 4-Differential Channel 2:1 Mux/DeMux + 2-Channel 2:1 Mux/DeMux
- DVI, HDMI[™] 1.1, 1.2 and 1.3 signal compatible
- Data Rate support beyond 3.3Gbps on high speed data channels
- Supports both AC coupled and DC coupled signals
- Switching speed: 4ns
- Isolation: -40dB@1GHz for high-speed channels
- · Integrated ESD protection
- High-speed data bits: 12kV HBM

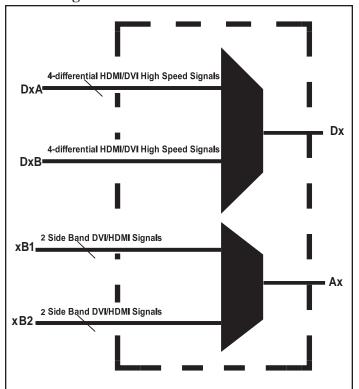
8kV contact

• Side band signal bits: 2kV HBM

Low bit-to-bit skewEnable/Disable time: 9ns

- Bidirectional
- Packaging (Pb-free & Green):
 - -48-pin BQSOP (B)

Block Diagram



Description

Pericom Semiconductor's PI3HDMI[™] series of switch circuits are targeted for high-resolution video networks that are based on DVI/HDMI[™] standards, and TMDS signal processing. The PI-3HDMI1210 is a 2-to-1 HDMI[™] Mux/DeMux Switch. The device multiplexes differential signals to one of two corresponding HDMI[™] inputs. The switch is bidirectional and offers little or no attenuation of the high-speed signals at the outputs. It is designed for low bitto-bit skew and high channel-to-channel noise isolation.

The maximum DVI/HDMITM data rate of 4.0Gbps provides the resolution required by the next generation HDTV and PC graphics. Three differential channels are used for data (video signals for DVI or audio/video signals for HDMITM), and one differential channel is used for Clock for decoding the TMDS signals at the outputs.

Because of its passive bidirectional feature, this switch can be used either at the video drivers side or at the receiver side. For PC graphics applications, the device sits at the drivers side to switch between multiple display units such as PC LCD monitor, projector, TV, etc. For consumer video applications, the device sits at the receiver end to switch between the source components such as DVD, D-VHS, STB, etc.

Pin Description

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V 00	
V _{DD} 331	48 🔲 D0+A
CP <u></u> 2	47 D0-A
D0+ <u>3</u>	46 D1+A
D0- <u>4</u>	45 D1-A
D1+ <u> </u>	44 D2+A
, D1- <u></u> 6	43 D2-A
$V_{DD}^{33} \longrightarrow 7$	42 D3+A
SEL 8	41 🔲 D3-A
D2+ 9	40 V _{DD} 33
D2- 10	39 D0+B
D3+ 11	38 D0-B
D3- 12	37 D1+B
CP 13	36 D1-B
V _{DD} 33 14	35 D2+B
GND 15	34 D2-B
CP 16	33 D3+B
GND 17	32 D3-B
A0 🔲 18	31 V _{DD} 33
GND 19	30 GND
A1 🔲 20	29 🔲 0B1
IN 21	28 🔲 0B2
V _{DD} 50 22	27 🔲 1B1
ĒN ☐ 23	26 1B2
CP24	25 GND



Truth Table

SEL(pin8)	Function ⁽¹⁾
L	$D_x \rightarrow D_x A$
Н	$D_x \rightarrow D_x B$

EN(pin23)	IN(pin21)	Function ⁽²⁾
L	L	$A_n \rightarrow {}_nB_1$
L	Н	$A_n \rightarrow {}_nB_2$
Н	X	$A_n = {}_n B_1 = {}_n B_2 = H_1 - Z$

Note:

1) x=0,1,2,3

2) n=0,1,2,3

Pin Description

Pin Name	Description
V_{DD} 33	3.3V Power supply
СР	Tie this pin to GND via 0.1μF capacitor for optimal operation
V_{DD} 50	5.0V Power supply
Dx	High Speed TMDS signal I/O
DxA, DxB	High Speed TMDS signal I/O
SEL	High Speed Signal Control
IN	Side Band Signal Control
Ax, xB1, xB2	Side Band Signal I/O
GND	GND pin
EN∖	Global Enable for Side Band Signals

Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	65°C to +150°C
DC Input Voltage for side band signals	0.5V to 5.5V
DC Input Voltage for side band signals DC Output Current	20mA
Power Dissipation	0.5W
Supply Voltage V _{DD} 33	
Supply Voltage V _{DD} 50	4.2V to 5.5V

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Power Supply Characteristics

Paramenter	Description	Min.	Max.	Units
V_{DD}^{33}	3.3V Power Supply	3.0	3.6	V
$V_{\mathrm{DD}^{50}}$	5.0 Power Supply	4.2	5.5	V



DC Electrical Characteristics for Switching over Operating Range

 $(T_A = -40^{\circ}C \text{ to } +85^{\circ}C)$

Paramenter	Description	Test Conditions	Min.	Typ ⁽²⁾	Max.	Units
V _{IHSEL} (3)	Input HIGH Voltage	Guaranteed HIGH level	2.8			
V _{IL} _{SEL} (3)	Input LOW Voltage	Guaranteed LOW level			2.0	V
V _{IKHS} ⁽⁴⁾	Clamp Diode Voltage	V _{DD} =Max., I _{IN} =–18mA			0.9	

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at $T_A = 25$ °C ambient and maximum loading.
- 3. $V_{IHSEL} + V_{ILSEL}$ are for SEL input only (pin 11)
- 4. For High-speed signals only

DC Electrical Characteristics for Switching over Operating Range

 $(T_A = -40^{\circ}\text{C to } +85^{\circ}\text{C})$ for high speed signals only

Paramenter	Description	Test Conditions	Min.	Typ ⁽²⁾	Max.	Units
V _{IHIN} (3)	Input HIGH Voltage	Guaranteed HIGH level	2.1			
V _{ILIN} ⁽³⁾	Input LOW Voltage	Guaranteed LOW level			0.8	V
V _{IKHS} (4)	Clamp Diode Voltage	V _{DD} =Max., I _{IN} =–18mA			-0.9	
IIH	Input HIGH Current	V _{DD} =Max., V _{IN} =V _{DD}			±5	
I _{IL}	Input LOW Current	V _{DD} =Max., V _{IN} =V _{SS}			±5	μΑ

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at $T_A = 25$ °C ambient and maximum loading.
- 3. $V_{IH_{IN}} + V_{IL_{IN}}$ are for IN input only.
- 4. For Side-speed signals only



Power Supply Characteristics

Parameters	Description	Test Conditions(1)	Min.	Typ. ⁽²⁾	Max.	Units
I_{CC}	Quiescent Power Supply Current	$V_{DD} = Max., V_{IN} = V_{DD} \text{ or } V_{SS}$		11		mA

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at $T_A = 25$ °C ambient and maximum loading.

Dynamic Electrical Characteristics Over the Operating Range (TA= -40° to +85°C)

Parameter	Description	Test Conditions	Min.	Typ. ⁽²⁾	Max.	Units
X _{TALK} _{HS} ⁽¹⁾	Crosstalk	See Fig. 1 for Measurement Setup, f = 1.65Gbps		-45		
X _{TALK} _{SB} ⁽²⁾		f = 250KHz		-80		dB
O _{IRR} HS ⁽¹⁾	OFF Isolation	See Fig. 2 for Measurement Setup, f = 1.65Gbps		-40		
O _{IRR} _{SB} ⁽²⁾		250KHz		-56		
BW _{HS} ⁽¹⁾	-3dB Bandwith			2.4		GHz
		@1.65 Gbps (8-bit deep color)		87		
	Incoming I are	@2.0625 Gbps (10-bit deep color)		-0.96		dВ
$\mathrm{IL}_{\mathrm{HS}^{(1)}}$	Insertion Loss	@2.475 Gbps (12-bit deep color)		-1.18		uB
		@3.3 Gbps (16-bit deep color)		-1.84		

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Notes:

- High-speed signal path only
- Side-band signal path only
- Typical values are @ $T_A = 25^{\circ}C$ ambient



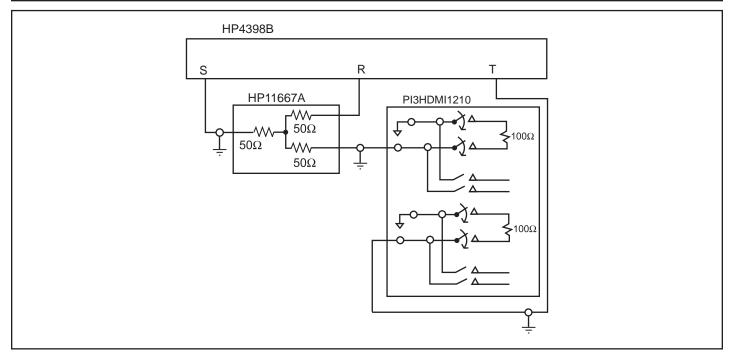


Fig 1. Crosstalk Setup

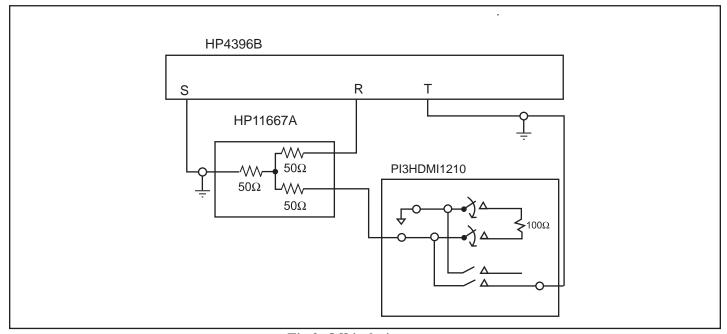


Fig 2. Off-isolation setup



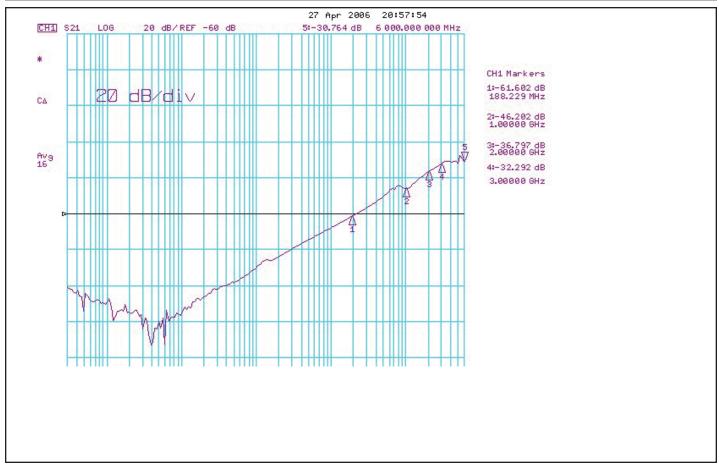


Fig 3. Crosstalk for High-speed signals only



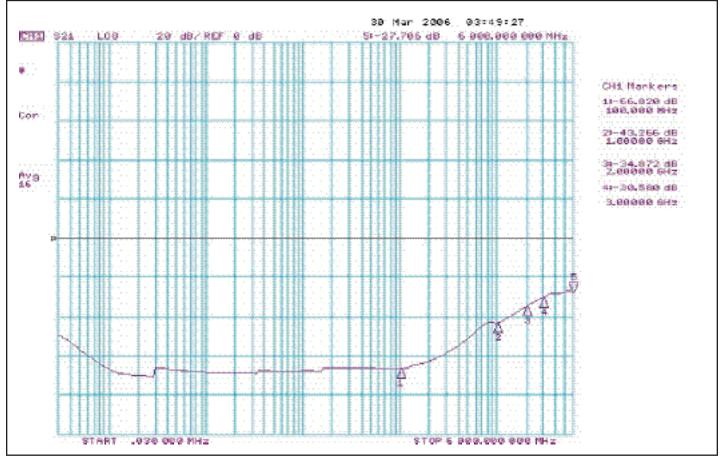


Fig 4. Off Isolation for high-speed signals only

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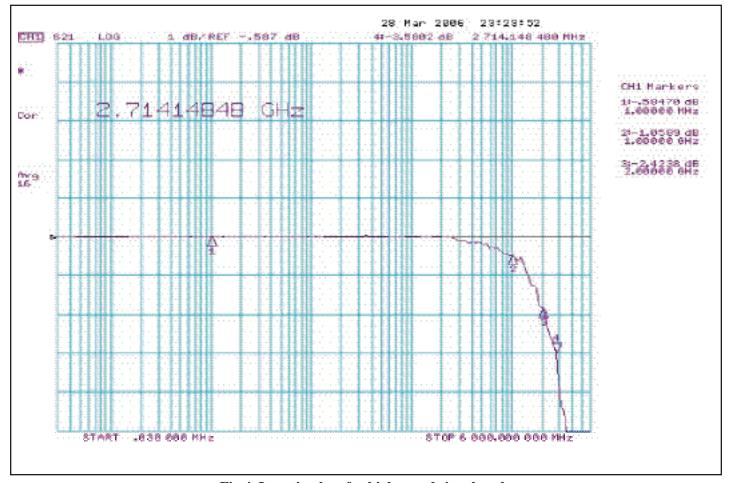


Fig 4. Insertion loss for high-speed signals only

Switching Characteristics (T_A = -40° to +85°C, V_{DD} 33= 3.3V±10%, V_{DD} 50= 5.0V±10%

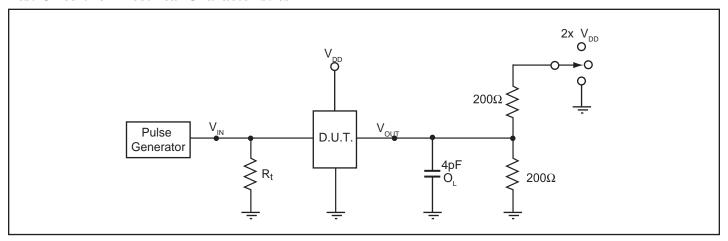
Paramenter	Description	Min.	Typ. ⁽²⁾	Max.	Units
tpZH, tpZL	Line Enable Time - SEL to AN, BN	0.5		8.0	77 G
tpHZ, tPLZ	Line Disable Time - SEL to A _N , B _N	0.5		4.0	ns

Notes:

1. For measurement setup, please see "Test Circuit For Electrical Characteristic" on page 6, and "Switching waveforms" on page 7



Test Circuit for Electrical Characteristics(1)



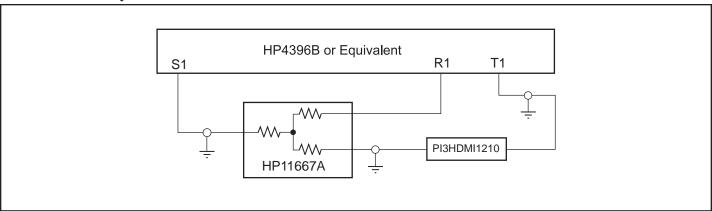
Notes:

- 1. $C_L = Load$ capacitance: includes jig and probe capacitance.
- R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator
- Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- All input impulses are supplied by generators having the following characteristics: $PRR \le MHz$, $Z_O = 50\Omega$, $t_R \le 2.5$ ns, $t_F \le 2.5$ ns. 4.
- The outputs are measured one at a time with one transition per measurement.

Switch Positions

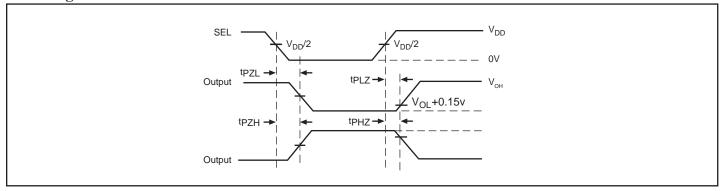
Test	Switch
t _{PLZ} , t _{PZL} (output on B-side)	2 x (V _{DD} 33)
t _{PHZ} , t _{PZH} (output on B-side)	GND
Prop Delay	Open

Test Circuit for Dynamic Electrical Characteristics

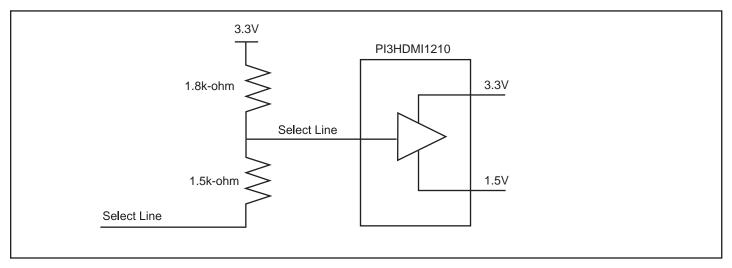




Switching Waveforms



Voltage Waveforms Enable and Disable Times



Example of a circuit that needs to be connected to the SEL pin (8) of the PI3HDMI1210 of our device



Application Test Results

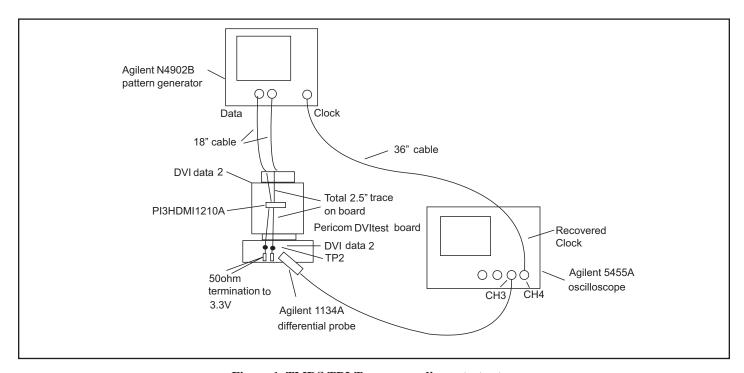


Figure 1. TMDS TP2 Tx eye compliance test setup

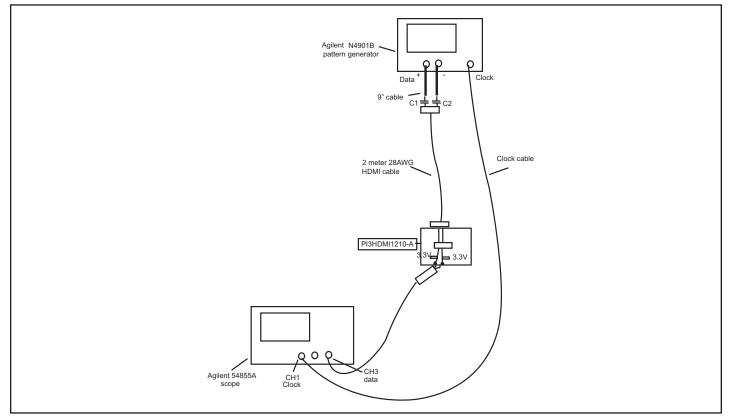


Figure 2. TMDS TP3 Rx "switch at receiver" eye compliance test setup



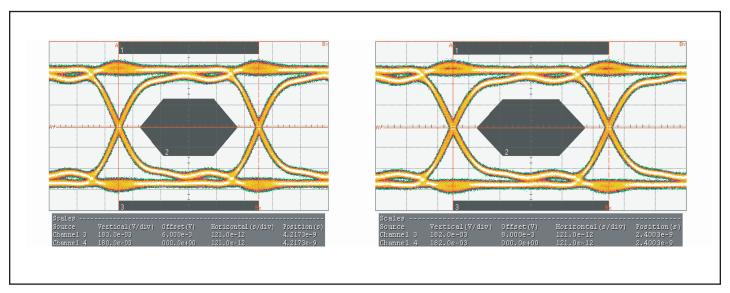


Figure 3. TP2 (Tx) eye-measurements (left is with switch, right is without switch), refer to figure 1 for the test set up

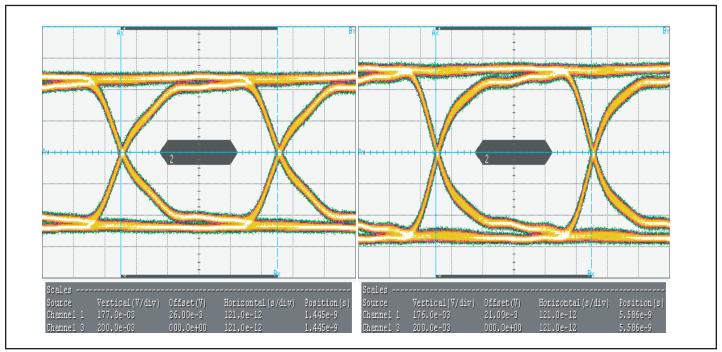
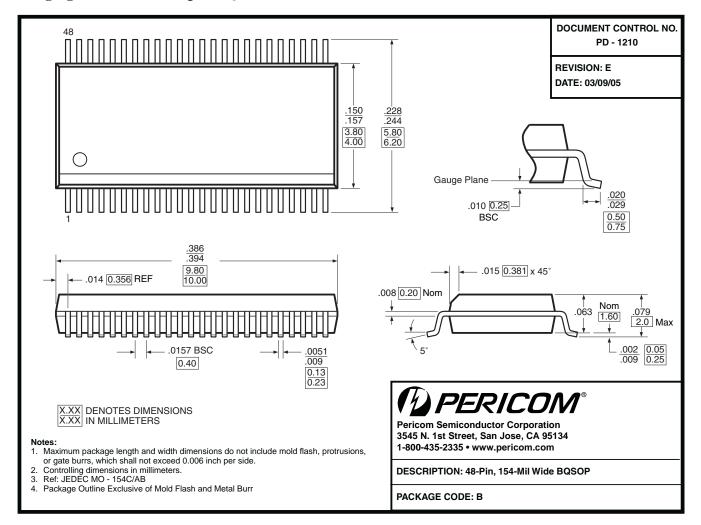


Figure 4. TP3 (Rx) - "Switch at receiver side" with 2-meter cable eye-measurements (left is with switch, right is without switch), refer to figure 2 for the test set up.



Packaging Mechanical: 48-pin BQSOP (B)



Ordering Information

Ordering Code	Package Code	Package Description
PI3HDMI1210BE	В	Pb-free & Green, 48-pin BQSOP

Notes:

- 1. Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- 2. E = Pb-free and Green
- 3. X Suffix = Tape/Reel
- HDMI & Deep Color are trademarks of Silicon Image

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High-performance HDMITM 1.1 and 1.2 Compatible Signal Switch w/ Integrated Side-band Signal Support

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