AEGIS
SEMICONDUTORES LTDA.

A5A:670.XX

VOLTAGE RATINGS

Part Number	V_{RRM} , VR (V) Max. rep. peak reverse voltage		V_{RSM} , V_R (V) Max. non-rep. peak reverse voltage
	T_J = 0 to 180°C	T_J = -40 to 0°C	T_J = 25 to 180°C
A5A:670.02	200	200	300
A5A:670.04	400	400	500
A5A:670.06	600	600	700
A5A:670.08	800	800	900
A5A:670.10	1000	1000	1100
A5A:670.12	1200	1200	1300

MAXIMUM ALLOWABLE RATINGS

PARAMETER	VALUE	UNITS	NOTES
T_J Junction Temperature	-40 to 180	°C	-
T_{stg} Storage Temperature	-40 to 180	°C	-
$I_{F(AV)}$ Max. Av. current	550	A	180° half sine wave
@ Max. T_C	125	°C	
$I_{F(RMS)}$ Nom. RMS current	1060	A	-
I_{FSM} Max. Peak non-rep. surge current	7.65	kA	50 Hz half cycle sine wave; Initial T_J = 180°C, rated V_{RRM} applied after surge.
	8.00		60 Hz half cycle sine wave; Initial T_J = 180°C, rated V_{RRM} applied after surge.
	9.10		50 Hz half cycle sine wave; Initial T_J = 180°C, no voltage applied after surge.
	9.50		60 Hz half cycle sine wave; Initial T_J = 180°C, no voltage applied after surge.
I^2t Max. I^2t capability	267	kA^2s	t = 10ms; Initial T_J = 180°C, rated V_{RRM} applied after surge.
	292		t = 8.3 ms; Initial T_J = 180°C, rated V_{RRM} applied after surge.
	378		t = 10ms; Initial T_J = 180°C, no voltage applied after surge.
	414		t = 8.3 ms; Initial T_J = 180°C, no voltage applied after surge.
$I^2t^{1/2}$ Max. $I^2t^{1/2}$ capability	4140	$kA^2s^{1/2}$	Initial T_J = 180°C, no voltage applied after surge. I^2t for time $t_x = I^2t^{1/2} * t_x^{1/2}$. (0.1 < tx < 10ms).
F Mounting Force	450	N.m	-

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CHARACTERISTICS

PARAMETER	MIN.	TYP.	MAX.	UNITS	TEST CONDITIONS
V_{FM} Peak forward voltage	---	1.50	1.67	V	Initial T_J = 25°C, 50-60Hz half sine, I_{peak} = 1728A.
$V_{F(TO)1}$ Low-level threshold	---	---	0.782	V	T_J = 180°C
$V_{F(TO)2}$ High-level threshold	---	---	0.859		Av. power = $V_{F(TO)} * I_{F(AV)} + r_F * [I_{F(RMS)}]^2$
r_{F1} Low-level resistance	---	---	0.541	m	Use low values for I_{FM} < $I_{F(AV)}$
r_{F2} High-level resistance	---	---	0.470		
I_{RM} Peak reverse current	---	13	30	mA	TJ = 180°C. Max. Rated VRRM
R_{thJC} Thermal resistance, junction-to-case	---	---	0.080	°C/W	DC operation, double side
	---	---	0.092	°C/W	180° sine wave, double side
	---	---	0.094	°C/W	120° rectangular wave, duble side
R_{thCS} Thermal resistance, case-to-sink	---	---	0.03	°C/W	Mtg. Surface smooth, flat and greased.
wt Weight	---	57(2.0)	---	g(oz.)	---
Case Style	DO-200AA			JEDEC	---

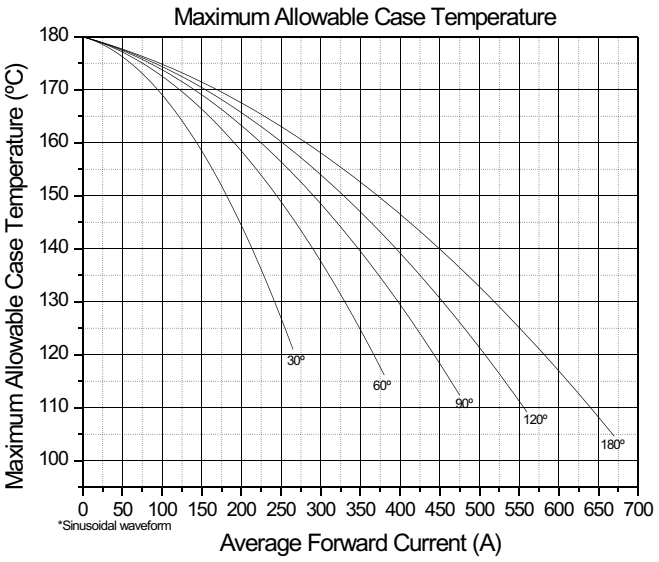

Fig. 1 - Current Ratings Characteristics

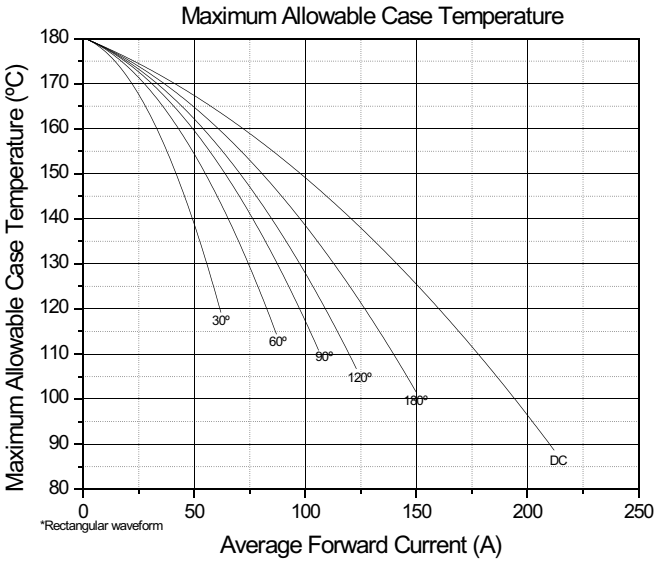

Fig. 2 - Current Ratings Characteristics

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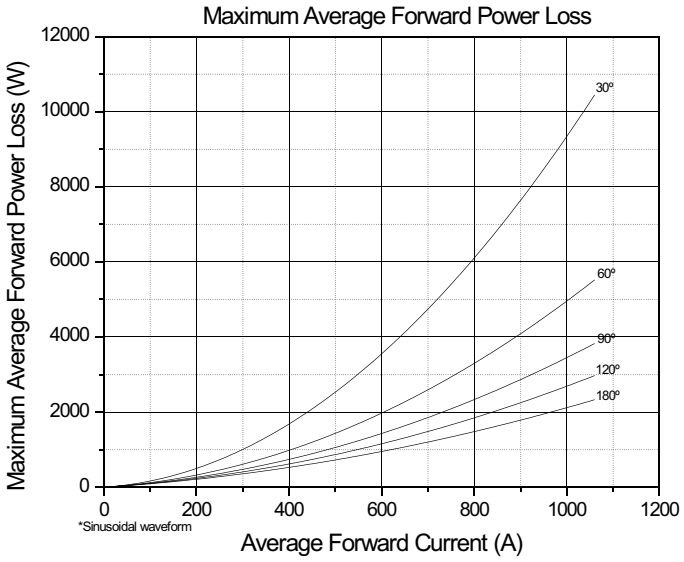

Fig. 3 - On-State Power Loss Characteristics

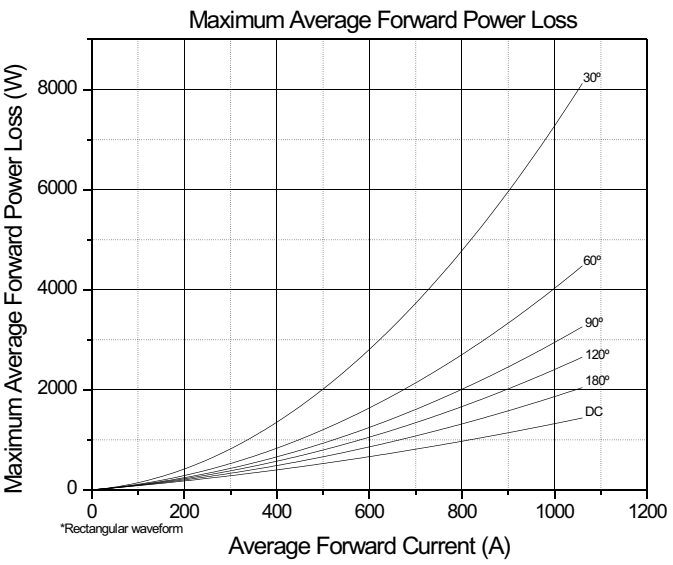

Fig. 4 - On-State Power Loss Characteristics

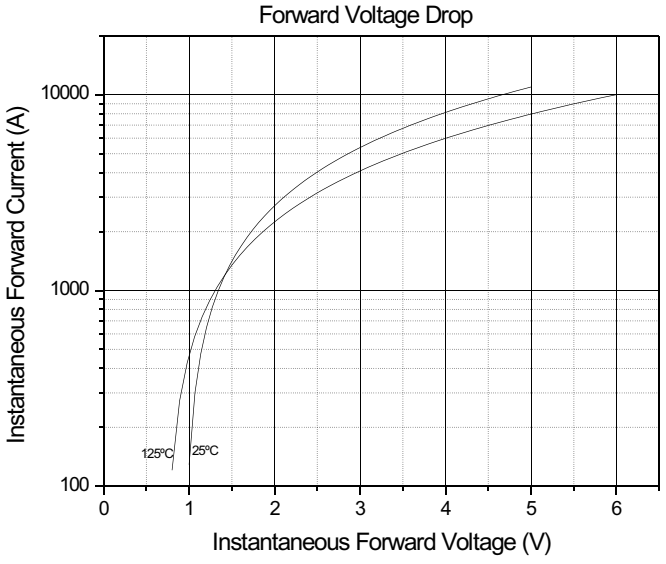

Fig. 5 - Forward Voltage Drop Characteristics

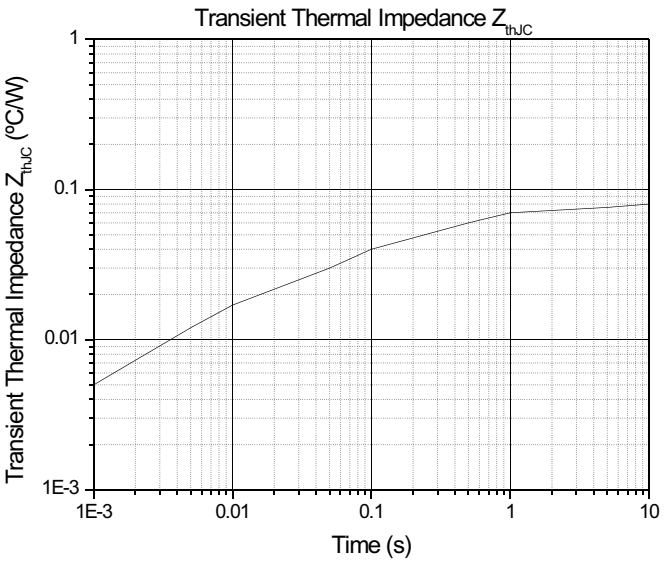

Fig. 6 - Transient Thermal Impedance Characteristics

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TO-200AA

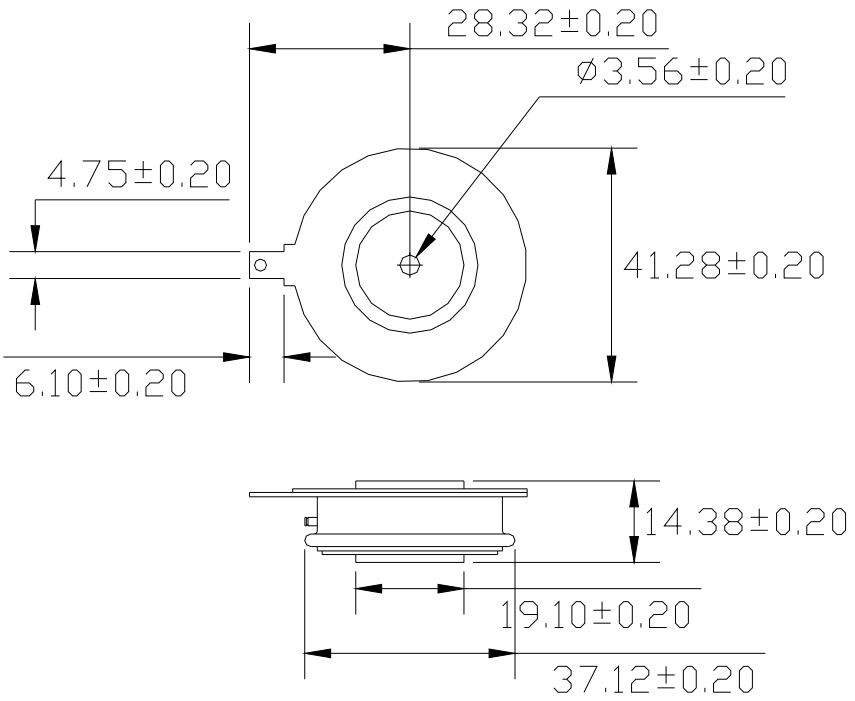

Fig. 7 - Outline Characteristics