TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7PA05FU

Dual Inverter (Open Drain)

Features

- Operating voltage range: V_{CC} = 1.8~3.6 V
- High-speed operation: t_{pZL} = 3.5 ns (max) at V_{CC} = 3.0~3.6 V

 t_{pZL} = 4.1 ns (max) at V_{CC} = 2.3~2.7 V

 t_{pZL} = 8.2 ns (max) at V_{CC} = 1.8 V

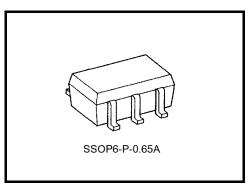
· High-level output current:

 I_{OL} = 24 mA (min) at V_{CC} = 3.0 V

 I_{OL} = 18 mA (min) at V_{CC} = 2.3 V

 I_{OL} = 6 mA (min) at V_{CC} = 1.8 V

- 3.6-V tolerant inputs
- 3.6-V power down protection outputs



Weight: 0.0068 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol Value		Unit
Power supply voltage	V _{CC}	-0.5~4.6	V
DC input voltage	V _{IN}	-0.5~4.6	٧
DC output voltage	V _{OUT}	-0.5~4.6 (Note 1)	٧
Input diode current	I _{IK}	-50	mA
Output diode current	lok	-50 (Note 2)	mA
DC output current	lout	+50	mA
Power dissipation	PD	200	mW
DC V _{CC} /ground current	Icc	±100	mA
Storage temperature	T _{stg}	-65~150	°C

Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating

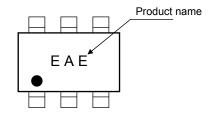
temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

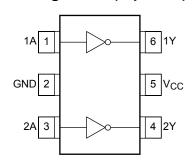
Note 1: The I_{OUT} absolute maximum rating must be adhered to.

Note 2: V_{OUT} < GND

Marking



Pin Assignment (top view)



Truth Table

А	Y
L	Z
Н	L

Z : High-impedance

IEC Logic Symbol



Operating Ranges

Characteristics	Symbol	Value	Unit
Power supply voltage	V _{CC}	1.8~3.6	V
rower supply voltage	VCC	1.2~3.6 (Note 3)	V
Input voltage	V _{IN}	-0.3~3.6	V
Output voltage	V _{OUT}	0~V _{CC}	٧
		24 (Note 4)	
Output Current	l _{OL}	18 (Note 5)	mA
		6 (Note 6)	
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~10 (Note 7)	ns/V

Note 3: Data retention only

Note 4: $V_{CC} = 3.0 \sim 3.6 \text{ V}$

Note 5: V_{CC} = 2.3~2.7 V

Note 6: $V_{CC} = 1.8 \text{ V}$

Note 7: $V_{IN} = 0.8 \sim 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$

DC Electrical Characteristics (Ta = -40~85°C, 2.7 V < V_{CC} \leq 3.6 V)

Characteristics	Symbol	ymbol Test Condition			Min	Max	Unit
	,			V _{CC} (V)			
High-Level Input Voltage	V _{IH}		_	2.7~3.6	2.0	_	V
Low-Level Input Voltage	V _{IL}		_	2.7~3.6	_	8.0	V
		V _{IN} = V _{IH}	I _{OL} = 100 μA	2.7~3.6	_	0.2	
Low Lovel Output Voltage	V-		I _{OL} = 12 mA	2.7	_	0.4	V
Low-Level Output Voltage	V _{OL}		I _{OL} = 18 mA	3.0		0.4	V
			I _{OL} = 24 mA	3.0		0.55	
Input Leakage Current	I _{IN}	V _{IN} = 0~3.6 V		2.7~3.6		±5.0	μΑ
Power-off Leakage Current	l _{OFF}	V _{IN} , V _{OUT} = 0~3.6 V		0		10.0	μΑ
Ouissant Supply Current	l	ICC $V_{IN} = V_{CC} \text{ or GND}$ $V_{CC} \le (V_{IN}, V_{OUT}) \le 3.6 \text{ V}$		2.7~3.6		20.0	
Quiescent Supply Current	icc			2.7~3.6		±20.0	μА
Increase in I _{CC} per Input	Δl _{CC}	$V_{IH} = V_{CC} - 0.6$	/	2.7~3.6		750	

DC Characteristics (Ta = -40~85°C, 2.3 V \leq V_{CC} \leq 2.7 V)

Characteristics	Symbol	Test Condition			Min	Max	Unit
Characteristics	Syllibol	rest c	rest Condition		IVIIII	IVIAX	Offic
High-Level Input Voltage	V_{IH}		_	2.3~2.7	1.6	_	V
Low-Level Input Voltage	V_{IL}		_	2.3~2.7	_	0.7	V
			$I_{OL} = 100 \mu A$	2.3~2.7	_	0.2	
Low-Level Output Voltage	V_{OL}	$V_{IN} = V_{IH}$	I _{OL} = 12 mA	2.3	_	0.4	V
			I _{OL} = 18 mA	2.3	_	0.6	
Input Leakage Current	I _{IN}	V _{IN} = 0~3.6 V		2.3~2.7	_	±5.0	μА
Power-off Leakage Current	l _{OFF}	V _{IN} , V _{OUT} = 0~3.6 V		0	_	10.0	μА
Outgozont Supply Current		V _{IN} = V _{CC} or GNI)	2.3~2.7	_	20.0	
Quiescent Supply Current	Icc	V _{CC} ≤ (V _{IN} , V _{OU})	r) ≦ 3.6 V	2.3~2.7	_	±20.0	μА

DC Characteristics (Ta = $-40 \sim 85$ °C, 1.8 V \leq V_{CC} < 2.3 V)

Characteristics	Symbol	Test Condition		\/ AA	Min	Max	Unit
				V _{CC} (V)			
High-Level Input Voltage	V _{IH}	_		1.8~2.3	0.7 × V _{CC}	l	V
Low-Level Input Voltage	V _{IL}	-	_		ı	0.2 × V _{CC}	٧
Low-Level Output Voltage	V	Maria Mari	$I_{OL} = 100 \mu A$	1.8		0.2	V
Low-Level Output Voltage	V _{OL}	$V_{IN} = V_{IH}$	I _{OL} = 6 mA	1.8	_	0.3	V
Input Leakage Current	I _{IN}	V _{IN} = 0~3.6 V	V _{IN} = 0~3.6 V		_	±5.0	μА
Power-off Leakage Current	l _{OFF}	V _{IN} , V _{OUT} = 0~3.6 V		0	_	10.0	μА
Quiescent Supply Current	loo	V _{IN} = V _{CC} or GND		1.8	_	20.0	μА
Quiescent Supply Current	Icc	V _{CC} ≦ (V _{IN} , V _{OUT}	-) ≦ 3.6 V	1.8	_	±20.0	μΑ

AC Electrical Characteristics (Ta = $-40 \sim 85^{\circ}$ C, input $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500$ Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit		
			1.8	1.0	8.2			
	t_{pZL}	t _{pZL} (Figure 1 and 2)		(Figure 1 and 2) 2.5 ± 0.2	2.5 ± 0.2	0.8	4.1	ns
Propagation delay time				0.6	3.5			
Propagation delay time	spagation delay time		1.8	1.0	6.8			
	t_{pLZ}	(Figure 1 and 2)	2.5 ± 0.2	0.8	3.8	ns		
			3.3 ± 0.3	0.6	3.5			

For C_L = 50 pF, add approximately 300 ps to the AC maximum specification.

Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f$ = 2.0 ns, C_L = 30 pF)

Characteristics	Symbol	Test Condition		Тур.	Unit		
			V _{CC} (V)				
		$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (Note 8)	1.8	0.25			
Quiet output maximum dynamic V _{OL}	V_{OLP}	$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (Note 8)	2.5	0.6	ns		
				$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (Note 8)	3.3	0.8	
		$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (Note 8)	1.8	-0.25			
Quiet output minimum dynamic V _{OL}	V_{OLV}	$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (Note 8)	2.5	-0.6	ns		
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (Note 8)	3.3	-0.8			
		$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (Note 8)	1.8	1.5			
Quiet output minimum dynamic V _{OH}	V_{OHV}	$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (Note 8)	2.5	1.9	ns		
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (Note 8)	3.3	2.2			

Note 8: Parameter guaranteed by design.

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	TYP.	Unit
Input Capacitance	C _{IN}	_	1.8, 2.5, 3.3	4	pF
Output Capacitance	C _{OUT}	_		3	pF
Power Dissipation Capacitance	C _{PD}	$f_{\text{IN}} = 10 \text{ MHz}$ (Not	9) 1.8, 2.5, 3.3	4	pF

Note 9: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

AC Test Circuit

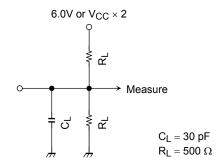
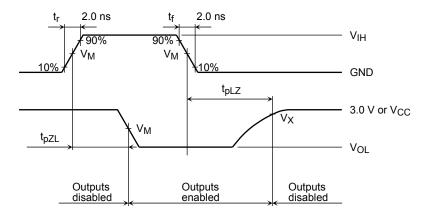


Figure 1

AC Waveforms



Symbol		V_{CC}	
Symbol	$3.3\pm0.3~\textrm{V}$	$2.5\pm0.2~\textrm{V}$	1.8 V
V_{IH}	2.7 V	V _{CC}	V _{CC}
V_{M}	1.5 V	V _{CC} /2	V _{CC} /2
VX	V _{OH} – 0.3 V	V _{OH} – 0.15 V	V _{OH} – 0.15 V

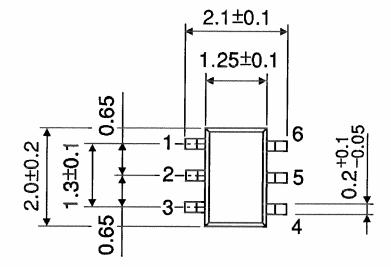
Figure 2 t_{pLH}, t_{pHL}

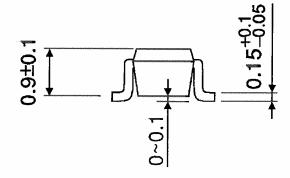
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Package Dimensions

SSOP6-P-0.65A

Unit: mm





Weight: 0.0068 g (typ.)

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20070701-EN GENERAL

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