DATA SHEET



MOS FIELD EFFECT TRANSISTOR NP84N04EHE, NP84N04KHE

NP84N04CHE, NP84N04DHE, NP84N04MHE, NP84N04NHE

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

These products are N-channel MOS Field Effect Transistors designed for high current switching applications.

ORDERING INFORMATION

PART NUMBER	LEAD PLATING	PACKING	PACKAGE		
NP84N04EHE-E1-AY Note1, 2			TO-263 (MP-25ZJ) typ. 1.4 g		
NP84N04EHE-E2-AY Note1, 2	Dura Ca (Tia)	Tana 200 n/raal			
NP84N04KHE-E1-AY Note1	Pure Sn (Tin)	Tape 800 p/reel	TO 202 (MD 257K) him 4.5 m		
NP84N04KHE-E2-AY Note1			TO-263 (MP-25ZK) typ. 1.5 g		
NP84N04CHE-S12-AZ Note1, 2	Sn-Ag-Cu		TO-220 (MP-25) typ. 1.9 g		
NP84N04DHE-S12-AY Note1, 2		T. b = 50 = 15 b =	TO-262 (MP-25 Fin Cut) typ. 1.8 g		
NP84N04MHE-S18-AY Note1	Pure Sn (Tin)	Tube 50 p/tube	TO-220 (MP-25K) typ. 1.9 g		
NP84N04NHE-S18-AY Note1			TO-262 (MP-25SK) typ. 1.8 g		

Notes 1. Pb-free (This product does not contain Pb in the external electrode.)

2. Not for new design

FEATURES

- Channel temperature 175 degree rated
- Super low on-state resistance

 $R_{DS(on)}$ = 5.2 m Ω MAX. (Vgs = 10 V, ID = 42 A)

• Low input capacitance

Ciss = 4410 pF TYP.

· Built-in gate protection diode

(TO-220)



(TO-262)



(TO-263)



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ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = 0 V)	VDSS	40	V
Gate to Source Voltage (VDS = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C) Note1	I _{D(DC)}	±84	Α
Drain Current (Pulse) Note2	ID(pulse)	±336	Α
Total Power Dissipation (Tc = 25°C)	Рт	200	W
Total Power Dissipation (T _A = 25°C)	Рт	1.8	W
Channel Temperature	Tch	175	°C
Storage Temperature	Tstg	-55 to +175	°C
Single Avalanche Current Note3	las	84/61/22	Α
Single Avalanche Energy Note3	Eas	70/372/484	mJ

Notes 1. Calculated constant current according to MAX. allowable channel temperature.

- **2.** PW \leq 10 μ s, Duty cycle \leq 1%
- 3. Starting T_{ch} = 25°C, V_{DD} = 20 V, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V (see Figure 4.)

THERMAL RESISTANCE

Channel to Case Thermal Resistance	$R_{\text{th(ch-C)}}$	0.75	°C/W
Channel to Ambient Thermal Resistance	Rth(ch-A)	83.3	°C/W

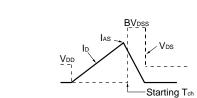


ELECTRICAL CHARACTERISTICS (TA = 25°C)

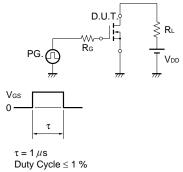
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 40 V, V _{GS} = 0 V			10	μΑ
Gate Leakage Current	Igss	V _{GS} = ±20 V, V _{DS} = 0 V			±10	μΑ
Gate to Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	2.0	3.0	4.0	V
Forward Transfer Admittance	y fs	V _{DS} = 10 V, I _D = 42 A	20	47		S
Drain to Source On-state Resistance	R _{DS(on)}	V _{GS} = 10 V, I _D = 42 A		4.6	5.2	mΩ
Input Capacitance	Ciss	V _{DS} = 25 V,		4410	6620	pF
Output Capacitance	Coss	V _{GS} = 0 V,		950	1430	pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		490	890	pF
Turn-on Delay Time	td(on)	V _{DD} = 20 V, I _D = 42 A,		36	79	ns
Rise Time	tr	V _{GS} = 10 V,		25	62	ns
Turn-off Delay Time	td(off)	$R_G = 1 \Omega$		77	150	ns
Fall Time	tf			28	69	ns
Total Gate Charge	Q _G	V _{DD} = 32 V,		87	130	nC
Gate to Source Charge	Qgs	V _{GS} = 10 V,		20		nC
Gate to Drain Charge	Q _{GD}	I _D = 84 A		32		nC
Body Diode Forward Voltage	V _F (S-D)	I _F = 84 A, V _{GS} = 0 V		1.0		V
Reverse Recovery Time	trr	I _F = 84 A, V _{GS} = 0 V,		49		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/μs		60		nC

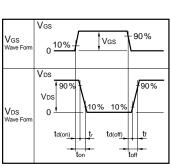
TEST CIRCUIT 1 AVALANCHE CAPABILITY

$\begin{array}{c} \text{D.U.T.} \\ \text{RG} = 25 \ \Omega \\ \text{VGS} = 20 \rightarrow 0 \ V \end{array} \begin{array}{c} \text{D.U.T.} \\ \text{S} 50 \ \Omega \\ \text{W} \end{array} \begin{array}{c} \text{VDD} \\ \text{W} \end{array}$



TEST CIRCUIT 2 SWITCHING TIME





TEST CIRCUIT 3 GATE CHARGE

$$\begin{array}{c|c} D.U.T. \\ \hline \\ IG = 2 \text{ mA} \\ \hline \\ VOD \\ \end{array}$$

TYPICAL CHARACTERISTICS (TA = 25°C)

Figure1. DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA

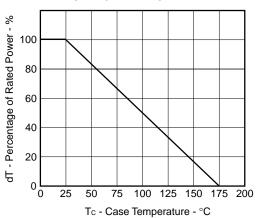


Figure 3. FORWARD BIAS SAFE OPERATING AREA

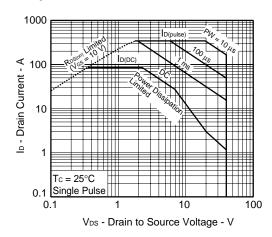


Figure 2. TOTAL POWER DISSIPATION vs. CASE TEMPERATURE

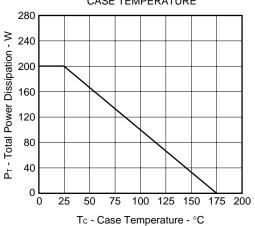


Figure4. SINGLE AVALANCHE ENERGY DERATING FACTOR

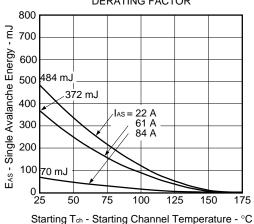
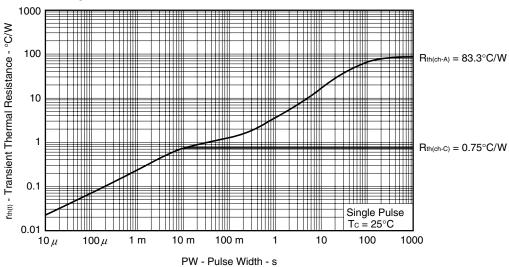
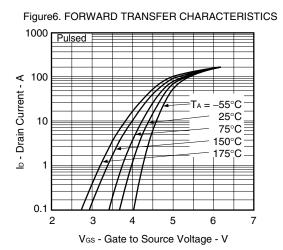
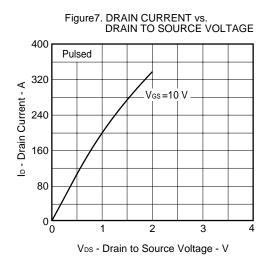
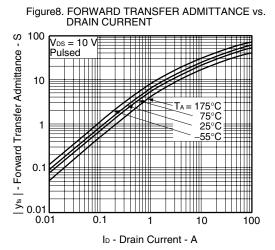


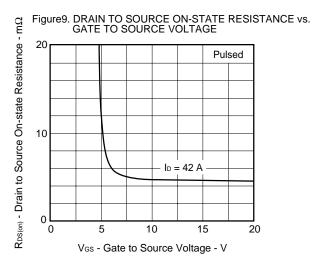
Figure 5. TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

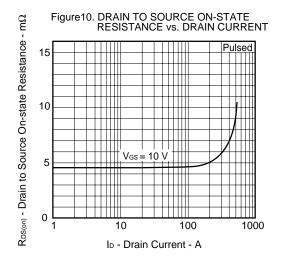


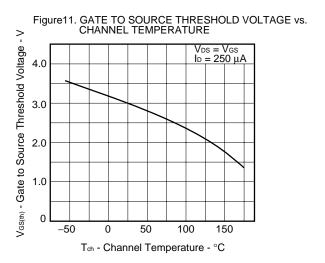


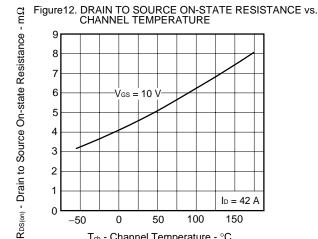












100

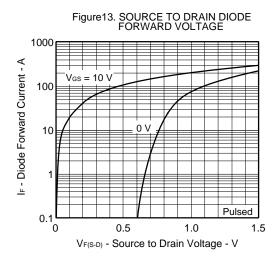
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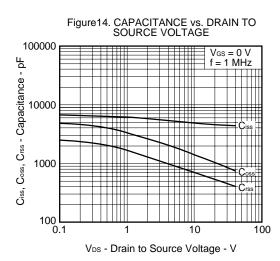
Tch - Channel Temperature - °C

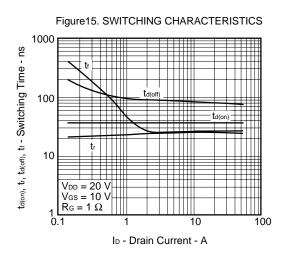
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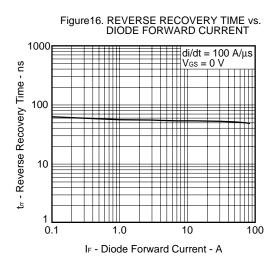
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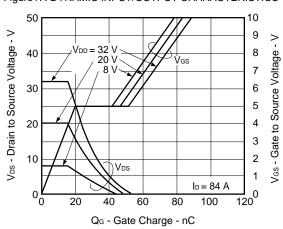
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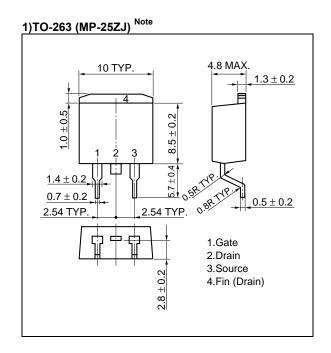


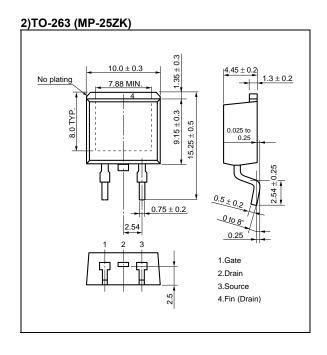


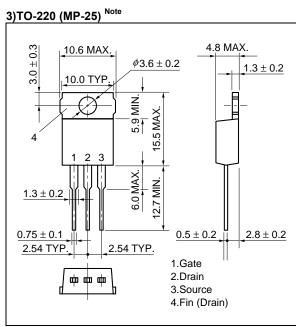


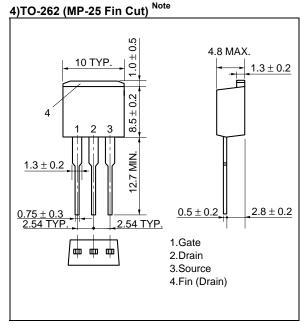


<R> PACKAGE DRAWINGS (Unit: mm)

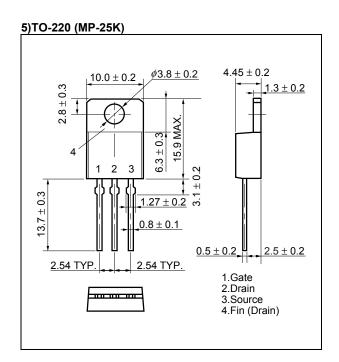


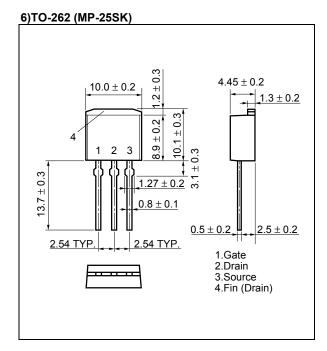




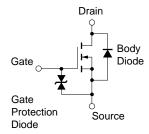


Note Not for new design





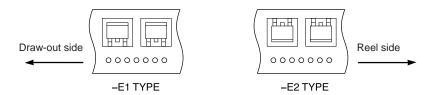
EQUIVALENT CIRCUIT



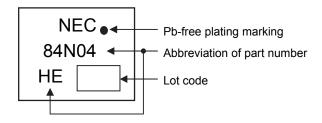
Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

<R> TAPE INFORMATION

There are two types (-E1, -E2) of taping depending on the direction of the device.



<R> MARKING INFORMATION



<R> RECOMMENDED SOLDERING CONDITIONS

These products should be soldered and mounted under the following recommended conditions.

For soldering methods and conditions other than those recommended below, please contact an NEC Electronics sales representative.

For technical information, see the following website.

Semiconductor Device Mount Manual (http://www.necel.com/pkg/en/mount/index.html)

Soldering Method	Soldering Conditions	Recommended Condition Symbol	
Infrared reflow	Maximum temperature (Package's surface temperature): 260°C or below		
MP-25ZJ, MP-25ZK	Time at maximum temperature: 10 seconds or less		
	Time of temperature higher than 220°C: 60 seconds or less	IR60-00-3	
	Preheating time at 160 to 180°C: 60 to 120 seconds		
	Maximum number of reflow processes: 3 times		
	Maximum chlorine content of rosin flux (percentage mass): 0.2% or less		
Wave soldering	Maximum temperature (Solder temperature): 260°C or below		
MP-25, MP-25K, MP-25SK,	Time: 10 seconds or less	THDWS	
MP-25 Fin Cut	Maximum chlorine content of rosin flux: 0.2% (wt.) or less		
Partial heating	Maximum temperature (Pin temperature): 350°C or below		
MP-25ZJ, MP-25ZK,	Time (per side of the device): 3 seconds or less	P350	
MP-25K, MP-25SK	Maximum chlorine content of rosin flux: 0.2% (wt.) or less		
Partial heating	Maximum temperature (Pin temperature): 300°C or below		
MP-25, MP-25 Fin Cut	Time (per side of the device): 3 seconds or less	P300	
	Maximum chlorine content of rosin flux: 0.2% (wt.) or less		

Caution Do not use different soldering methods together (except for partial heating).

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