TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7WG32FC

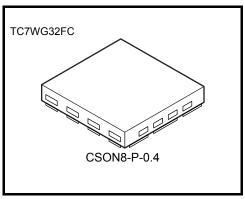
## Dual 2-Input OR Gate

#### **Features**

- High-level output current:  $I_{OH}/I_{OL} = \pm 8 \text{ mA (min)}$ at  $V_{CC} = 3 \text{ V}$
- High-speed operation: t<sub>pd</sub> = 2.8 ns (typ.)

at  $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$ 

- Operating voltage range: V<sub>CC</sub> = 0.9~3.6 V
- 5.5-V tolerant inputs
- 3.6-V power down protection outputs



Weight: 0.002 g (typ.)

## **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol	Value	Unit	
Power supply voltage	V <sub>CC</sub>	-0.5~4.6	V	
DC input voltage	V <sub>IN</sub>	-0.5~7.0	V	
DC output voltage	\/a	-0.5~4.6 (Note 1)	V	
	V <sub>OUT</sub>	-0.5~V <sub>CC</sub> + 0.5 (Note 2)		
Input diode current	I <sub>IK</sub>	-20	mA	
Output diode current	lok	-20 (Note 3)	mA	
DC output current	lout	±25	mA	
DC V <sub>CC</sub> /GND current	Icc	±50	mA	
Power dissipation	P <sub>D</sub>	150 (Note 4)	mW	
Storage temperature	T <sub>stg</sub>	-65~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V<sub>CC</sub> = 0V

Note 2: High or Low State.

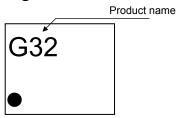
IOUT absolute maximum rating must be observed.

Note 3: V<sub>OUT</sub> < GND

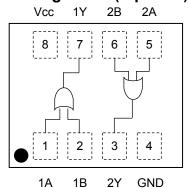
Note 4: Mounted on an FR4 board.

 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ t}, \text{ Cu Pad: } 11.56 \text{ mm}^2)$ 

## Marking



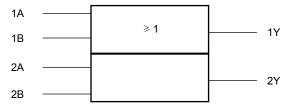
#### Pin Assignment (top view)



## **Truth Table**

Inp	Outputs		
Α	В	Y	
L	L	L	
L	Н	Н	
Н	L	Н	
Н	Н	Н	

# **IEC Logic Symbol**



## **Operating Ranges**

Characteristics	Symbol	Value	Unit	
Power supply voltage	V <sub>CC</sub>	0.9~3.6	V	
Input voltage	V <sub>IN</sub>	0~5.5	V	
Output valtage	\/a=	0~3.6 (Note 5)	V	
Output voltage	V <sub>OUT</sub>	0~V <sub>CC</sub> (Note 6)	V	
		±8.0 (Note 7)		
		±4.0 (Note 8)	mA	
Output Current		±3.0 (Note 9)		
	I <sub>OH</sub> /I <sub>OL</sub>	±1.7 (Note 10)		
		±0.3 (Note 11)		
		±0.02 (Note 12)		
Operating temperature	T <sub>opr</sub>	-40~85	°C	
Input rise and fall time	dt/dV	0~10 (Note 13)	ns/V	

Note 5:  $V_{CC} = 0V$ 

Note 6: High or Low state.

Note 7:  $V_{CC} = 3.0 \sim 3.6 \text{ V}$ 

Note 8:  $V_{CC} = 2.3 \sim 2.7 \text{ V}$ 

Note 9:  $V_{CC} = 1.65 \sim 1.95 \text{ V}$ 

Note 10: V<sub>CC</sub> = 1.4~1.6 V

Note 11: V<sub>CC</sub> = 1.1~1.3 V

Note 12:  $V_{CC} = 0.9 \text{ V}$ 

Note 13:  $V_{IN} = 0.8 \sim 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$ 

## **Electrical Characteristics**

## **DC Electrical Characteristics**

Characteristics Symbol Test Condition		Ta = 25°C			0	Ta = -4	Unit			
		lest Condition		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
				0.9	$V_{CC}$	_	_	V <sub>CC</sub>	_	
High-level VIH input voltage	_		1.1~1.3	V <sub>CC</sub> × 0.7	١	_	V <sub>CC</sub> × 0.7		V	
			1.4~1.6	V <sub>CC</sub> × 0.65	_	_	V <sub>CC</sub> × 0.65	_		
			1.65~1.95	V <sub>CC</sub> × 0.65	_	_	V <sub>CC</sub> × 0.65	_		
				2.3~2.7	1.7	_	_	1.7	_	
				3.0~3.6	2.0	_	_	2.0	_	
				0.9	_	_	GND	_	GND	
							V <sub>CC</sub> × 0.3	_	V <sub>CC</sub> × 0.3	V
Low-level	$V_{IL}$						V <sub>CC</sub> × 0.35	_	V <sub>CC</sub> × 0.35	
input voltage				1.65~1.95			V <sub>CC</sub> × 0.35	_	V <sub>CC</sub> × 0.35	
				2.3~2.7			0.7		0.7	
			3.0~3.6			0.8		0.8		
High-level VOH output voltage	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> =-0.02 mA	0.9	0.75	_	_	0.75	_	V	
		I <sub>OH</sub> = -0.3 mA	1.1~1.3	V <sub>CC</sub> × 0.75		_	V <sub>CC</sub> × 0.75	_		
		$I_{OH} = -1.7 \text{ mA}$	1.4~1.6	V <sub>CC</sub> × 0.75	_	_	V <sub>CC</sub> × 0.75	_		
		$I_{OH} = -3.0 \text{ mA}$	1.65~ 1.95	V <sub>CC</sub> -0.45		_	V <sub>CC</sub> -0.45	_		
			$I_{OH} = -4.0 \text{ mA}$	2.3~2.7	2.0	_	_	2.0	_	
			$I_{OH} = -8.0 \text{ mA}$	3.0~3.6	2.48	_	_	2.48	_	
			$I_{OL} = 0.02 \text{ mA}$	0.9	_	_	0.1		0.1	
Low-level V <sub>OL</sub> output voltage	V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OL</sub> = 0.3 mA	1.1~1.3	_	_	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	V	
		I <sub>OL</sub> = 1.7 mA	1.4~1.6		-	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25		
			I <sub>OL</sub> = 3.0 mA	1.65~ 1.95	_	_	0.45	_	0.45	
		I <sub>OL</sub> = 4.0 mA	2.3~2.7	_	_	0.4		0.4		
	I <sub>OL</sub> = 8.0 mA		3.0~3.6	_	_	0.4	_	0.4		
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 0~5.5V		0~3.6			±0.1	_	±1.0	μΑ
Power off leakage current	loff	V <sub>IN</sub> = 0~5.5V V <sub>OUT</sub> = 0~3.6V		0	_	_	1.0	_	10.0	μΑ
Quiescent supply current	I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		3.6	_	_	1.0	_	10.0	μΑ

# AC Electrical Characteristics (input $t_r = t_f = 3 \text{ ns}$ )

Characteristics	Symbol	Toot Condition		Ta = 25°C			Ta = -40~85°C		Unit
Gridiacteristics Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Uniit	
		C <sub>L</sub> = 10 pF,	0.9	_	19.8	_	_	_	-
			1.1~1.3	_	10.1	18.7	1.0	34.5	
			1.4~1.6	_	5.9	8.9	1.0	10.8	
		$R_L = 1 M\Omega$	1.65~ 1.95	_	4.5	6.4	1.0	6.9	
			2.3~2.7	_	3.1	4.2	1.0	4.7	ns
			3.0~3.6	_	2.3	3.4	1.0	4.0	
			0.9		22.5		_	_	
	<sup>t</sup> pLH <sup>t</sup> pHL	$C_L$ = 15 pF, $R_L$ = 1 $M\Omega$	1.1~1.3	_	11.6	21.5	1.0	37.2	
Propagation delay time			1.4~1.6	_	6.6	9.8	1.0	12.0	
			1.65~ 1.95	_	5.0	7.1	1.0	7.3	
			2.3~2.7		3.5	4.5	1.0	5.1	
			3.0~3.6		2.8	3.8	1.0	4.4	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		30.0		_	_	
			1.1~1.3		15.0	29.6	1.0	56.0	
			1.4~1.6		8.5	13.1	1.0	15.9	
			1.65~ 1.95		6.3	9.2	1.0	9.6	
			2.3~2.7		4.3	5.7	1.0	6.1	
			3.0~3.6		3.5	4.4	1.0	4.8	
Input capacitance	C <sub>IN</sub>	_	3.6	_	3	_	_	_	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 14)	0.9 ~ 3.6		11	_	_		pF

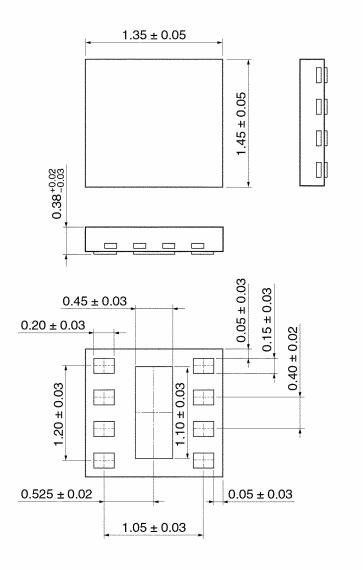
Note 14: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$ 

# **Package Dimensions**

CSON8-P-0.4 Unit: mm



Weight: 0.002 g (typ.)

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20070701-EN GENERAL

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