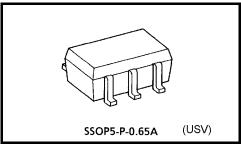
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SG86FU

### EXCLUSIVE OR Gate

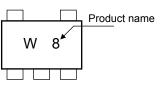
#### Features

- High-level output current:  $I_{OH}/I_{OL} = \pm 8 \text{ mA (min)}$ at V<sub>CC</sub> = 3 V
- High-speed operation: t<sub>pd</sub> = 2.7 ns (typ.)
  - at V<sub>CC</sub> = 3.3 V,15pF
- Operating voltage range: V<sub>CC</sub> = 0.9~3.6 V
- 5.5-V tolerant inputs.
- 3.6-V power down protection output.

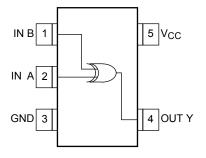


Weight: 0.006 g (typ.)

## Marking



## Pin Assignment (top view)



## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Value	Unit			
Power supply voltage	V <sub>CC</sub>	-0.5~4.6	V			
DC input voltage	VIN	-0.5~7.0	V			
	Vaur	-0.5~ 4.6 (Note 1)	V			
DC output voltage	VOUT	-0.5~ V <sub>CC</sub> + 0.5 (Note 2)	V			
Input diode current	I <sub>IK</sub>	-20	mA			
Output diode current	I <sub>OK</sub>	-20 (Note 3)	mA			
DC output current	IOUT	±25	mA			
DC V <sub>CC</sub> /ground current	I <sub>CC</sub>	±50	mA			
Power dissipation	PD	200	mW			
Storage temperature	T <sub>stg</sub>	-65~150	°C			

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

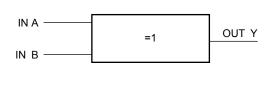
Note 1:  $V_{CC} = 0V$ 

Note 2: High or Low State. I<sub>OUT</sub> abusolute maximum rating must be observed.

Note 3: V<sub>OUT</sub> < GND

## <u>TOSHIBA</u>

## IEC Logic Symbol



А	В	Y
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L

**Truth Table** 

## **Operating Ranges**

Characteristics	Symbol	Value	Unit	
Power supply voltage	V <sub>CC</sub>	0.9~3.6	V	
Input voltage	V <sub>IN</sub>	0~5.5	V	
Output voltage		0~3.6 (Note 4)	V	
	Vout	0~V <sub>CC</sub> (Note 5)	v	
Output Current		±8.0 (Note 6)		
	I <sub>OH</sub> /I <sub>OL</sub>	±4.0 (Note 7)		
		±3.0 (Note 8)	m (	
		±1.7 (Note 9)	mA	
		±0.3 (Note 10)		
		±0.02 (Note 11)		
Operating temperature	T <sub>opr</sub>	-40~85	°C	
Input rise and fall time	dt/dV	0~10 (Note 12)	ns/V	

Note 4:  $V_{CC} = 0V$ 

Note 5: High or Low state.

Note 6:  $V_{CC} = 3.0 \sim 3.6 \text{ V}$ 

Note 7:  $V_{CC} = 2.3 \sim 2.7 \text{ V}$ 

Note 8:  $V_{CC} = 1.65 \sim 1.95 \text{ V}$ 

Note 9:  $V_{CC} = 1.4 \sim 1.6 V$ 

Note 10:  $V_{CC} = 1.1 \sim 1.3 \text{ V}$ 

Note 11:  $V_{CC}=0.9\ V$ 

Note 12:  $V_{IN} = 0.8 \sim 2.0 \text{ V}, \text{ V}_{CC} = 3.0 \text{ V}$ 

## **DC Electrical Characteristics**

Characteristics Symbol Test Condition				Ta = 25°C			Ta = -40~85°C		Unit	
Characteristics	Symbol			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
				0.9	V <sub>CC</sub>	_	—	V <sub>CC</sub>	_	-
				1.1~1.3	$V_{CC} \times 0.7$		_	$V_{CC} \times 0.7$		
High-level V <sub>IH</sub> input voltage	1.4~1.6			V <sub>CC</sub> × 0.65	_	_	V <sub>CC</sub> × 0.65	_	V	
	1.65~1.95			V <sub>CC</sub> × 0.65	_	_	V <sub>CC</sub> × 0.65	_		
	2.3~2.7			1.7	_	_	1.7	_		
				3.0~3.6	2.0	_	_	2.0	_	
				0.9	_	_	GND	_	GND	
			1.1~1.3			$V_{CC} \times 0.3$	_	$V_{CC} \times 0.3$	v	
Low-level	VIL		_		_	_	V <sub>CC</sub> × 0.35	—		$\begin{array}{c} V_{CC} \\ \times \ 0.35 \end{array}$
input voltage				1.65~1.95			V <sub>CC</sub> × 0.35	_		$\begin{array}{c} V_{CC} \\ \times \ 0.35 \end{array}$
				2.3~2.7			0.7			0.7
				3.0~3.6			0.8			0.8
			I <sub>OH</sub> =-0.02 mA	0.9	0.75	_		0.75	_	V
		OH VIN = VIH or VIL	I <sub>OH</sub> = -0.3 mA	1.1~1.3	$\begin{array}{c} V_{CC} \\ \times \ 0.75 \end{array}$		—	V <sub>CC</sub> × 0.75		
High-level Vон	V <sub>ОН</sub>		I <sub>OH</sub> = -1.7 mA	1.4~1.6	$\begin{array}{c} V_{CC} \\ \times \ 0.75 \end{array}$		—	V <sub>CC</sub> × 0.75		
output voltage			I <sub>OH</sub> = -3.0 mA	1.65~ 1.95	V <sub>CC</sub> -0.45		_	V <sub>CC</sub> -0.45		
			I <sub>OH</sub> = -4.0 mA	2.3~2.7	2.0	_	—	2.0	_	
			I <sub>OH</sub> = -8.0 mA	3.0~3.6	2.48	_		2.48	_	
			I <sub>OL</sub> = 0.02 mA	0.9	—	_	0.1		0.1	V
Low-level V <sub>OL</sub>			I <sub>OL</sub> = 0.3 mA	1.1~1.3	_	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	—	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	
	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> = 1.7 mA	1.4~1.6	_		V <sub>CC</sub> × 0.25		V <sub>CC</sub> × 0.25	
		OI VIL	I <sub>OL</sub> = 3.0 mA	1.65~ 1.95	_	_	0.45	_	0.45	
			I <sub>OL</sub> = 4.0 mA	2.3~2.7	—	_	0.4		0.4	
			I <sub>OL</sub> = 8.0 mA	3.0~3.6	—	_	0.4	—	0.4	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 0~5.5V		0~3.6	_		±0.1	_	±1.0	μΑ
Power off leakage current	IOFF	V <sub>IN</sub> = 0~5.5V V <sub>OUT</sub> = 0~3.6V		0	—		1.0		10.0	μΑ
Quiescent supply current	ICC	$V_{IN} = V_{CC}$	or GND	3.6	_	_	1.0		10.0	μΑ

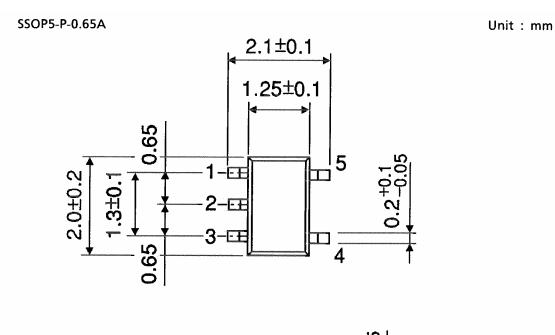
## AC Electrical Characteristics (input $t_r = t_f = 3 \text{ ns}$ )

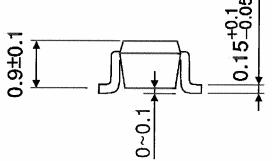
Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40~85°C		Unit	
Symbo	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
		$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	23.0		_		
			1.1~1.3	_	11.7	20.9	1.0	39.1	
			1.4~1.6	_	6.7	10.0	1.0	11.8	ns
			1.65~ 1.95		5.1	6.6	1.0	7.6	
			2.3~2.7		3.4	4.1	1.0	4.7	
Propagation delay time			3.0~3.6	_	2.7	3.3	1.0	3.9	
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		23.7		_	_	
	tplh tphl		1.1~1.3		11.9	22.8	1.0	39.4	
			1.4~1.6		6.7	9.9	1.0	11.9	
			1.65~ 1.95	_	5.1	7.3	1.0	7.5	
			2.3~2.7	_	3.4	4.7	1.0	5.3	
			3.0~3.6		2.7	3.6	1.0	4.1	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	32.1	_	_	_	
			1.1~1.3		15.7	31.4	1.0	59.4	
			1.4~1.6		8.7	13.9	1.0	16.9	
			1.65~ 1.95	_	6.5	9.8	1.0	10.2	
			2.3~2.7		4.2	6.0	1.0	6.5	
			3.0~3.6	_	3.4	4.7	1.0	5.1	
Input capacitance	C <sub>IN</sub>		3.6		3		—		pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 13)	0.9~3.6	_	9		—		pF

Note 13: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:  $I_{CC \text{ (opr)}} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

## Package Dimensions





Weight: 0.006 g (typ.)

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20070701-EN GENERAL

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