

4.2V, Wide Bandwidth, 2-Channel, 2:1 Mux/DeMux USB 2.0 Switch w/ Single Select

Features

USB 2.0 compliant

• R_{ON} is 4Ω typical

· Low bit-to-bit skew

• Low Crosstalk: -31dB @ 250MHz

Near-Zero propagation delay: 250ps

• Switching speed: 9ns

• Channel On Capacitance: 6pF (typical)

• V_{CC} Operating Range: 2.7V to 4.2V $\pm 10\%$

• ESD>2000V . . . Human Body Model

• >700 MHz bandwidth (or data frequency)

• Packaging (Pb-free & Green):

—12-contact, 1.6mm x 2.2mm, TQFN (ZK)

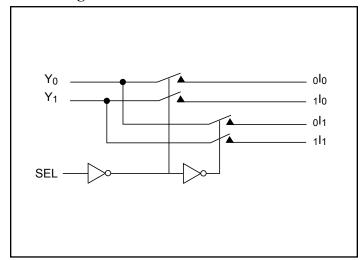
—12-contact, 3mm x 3.5mm, TDFN (ZE)

Application

• Routes signals for USB 2.0

· Hand held devices

Block Diagram



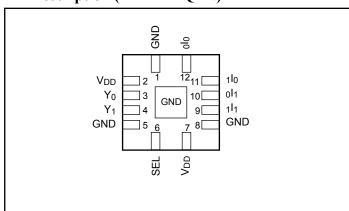
Truth Table

Function	SEL
Y_n to $_nI_o$	L
Y_n to ${}_nI_1$	Н

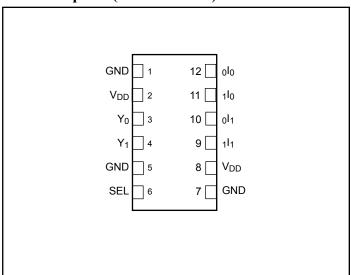
Description

The PI3USB10M is a single differential channel 2:1 multiplexer/demultiplexer USB 2.0 Switch. Industry leading advantages include a propagation delay of less than 250ps, resulting from its low channel resistance and I/O capacitance. The device multiplexes differential outputs from a USB Host device to one of two corresponding outputs. The switch is bidirectional and offers little or no attenuation of the high-speed signals at the outputs. It is designed for low bit-to-bit skew, high channel-to-channel noise isolation and is compatible with various standards, such as High Speed USB 2.0 (480 Mb/s).

Pin Description (ZK - 12TQFN)



Pin Description (ZE - 12TDFN)





Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	65°C to +150°C
Supply Voltage to Ground Potential	0.5V to +4.0V
DC Input Voltage	0.5V to +5.5V
DC Output Current	120mA
Power Dissipation	0.5W

Note: Stresses greater than those listed under MAXIMUM RAT-INGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics for USB 2.0 Switching over Operating Range

 $(T_A = -40^{\circ}C \text{ to } +85^{\circ}C, V_{CC} = 3.0 - 4.4V)$

Paramenter	Description	Test Conditions ⁽¹⁾		Min.	Typ. ⁽²⁾	Max.	Units
V	Input HIGH Voltage	Guaranteed	$V_{CC} = 4.2V$	2.0			
$V_{ m IH}$	input fildri voltage	HIGH level	$V_{CC} = 3.3V$	1.6			V
$ m V_{IL}$	Input LOW Voltage	Guaranteed HIG	H level			0.8	V
V_{IK}	Clamp Diode Voltage	$V_{CC} = Max., I_{IN}$	I = -18mA		-0.7	-1.2	
${ m I}_{ m IH}$	Input HIGH Current	$V_{CC} = Max., V_{IN} = VCC$				±5	^
${ m I}_{ m IL}$	Input LOW Current	$V_{CC} = Max., V_{IN} = GND$				±5	μΑ
R _{ON}	Switch On-Resistance ⁽³⁾	$\begin{aligned} V_{CC} &= Min., \ 1.25V \leq V_{IN} \leq V_{CC}, \\ I_{IN} &= -40mA \end{aligned}$			4.0	6.5	
R _{FLAT(ON)}	On-Resistance Flatness ⁽³⁾	V_{CC} = Min., V_{IN} @ 1.5V and V_{CC} I_{IN} = -40mA			1.0		Ω
$\Delta R_{ m ON}$	On-Resistance match from center ports to any other port ⁽³⁾	$V_{CC} = Min., 1.5$ $I_{IN} = -40mA$	$V \le V_{IN} \le V_{CC}$		0.9	2.0	

Notes:

- 1. For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. $V_{CC} = 3.0 4.4V$, $T_A = 25$ °C ambient and maximum loading.
- 3. Measured by the voltage drop between Y and In pin at indicated current through the Switch-On-Resistance is determined by the lower of the voltages on the two (Y, In) pins.

Power Supply Characteristics ($V_{CC} = 3.0 - 4.4V$)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Тур.	Max.	Units
I_{CC}	Quiescent Power Supply Current	$V_{CC} = Max., V_{IN} = GND \text{ or } V_{CC}$			800	μA

Notes:

1. For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.



Capacitance ($T_A = 25$ °C, f = 1MHz)

Parameters ⁽³⁾	Description	Test Conditions ⁽¹⁾	Typ. ⁽²⁾	Max.	Units
C_{IN}	Input Capacitance		2	3	
C _{OFF}	Port I Capacitance, Switch OFF	$V_{IN} = 0V$	4	6	pF
C _{ON}	Switch Capacitance, Switch ON		7.5	9.5	

Dynamic Electrical Characteristics Over the Operating Range

Parameters	Description	Test Conditions	Min.	Typ. ⁽²⁾	Max.	Units
X _{TALK}	Crosstalk	D 500 f - 250 MHz		-31		dD.
O_{IRR}	OFF Isolation	$R_L = 50\Omega$, $f = 250$ MHz		-39		dB
BW	Bandwidth -3dB	$R_{\rm L} = 50\Omega$		700		MHz

Notes:

- 1. For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at $V_{CC} = 3.3V$, $T_A = 25^{\circ}C$ ambient and maximum loading.
- 3. This parameter is determined by device characterization but is not production tested.

Switching Characteristics

Parameters	Description	Test (1) Conditions	Min.	Тур.	Max.	Units
t_{PD}	Propagation Delay ^(2,3)	See Test	-	0.25		
t _{PZH} , t _{PZL}	Line Enable Time - SEL to Y _N , _N I ₀ , I _N	Circuit for Electrical	0.5	1	15.0	
t _{PHZ} , t _{PLZ}	Line Disable Time - SEL to Y _N , NI ₀ , I _N	Character-	0.5	1	9.0	ns
t _{SK(o)}	Output Skew between center port to any other port ⁽²⁾	istics	-	0.1	0.2	115
$t_{SK(p)}$	Skew between opposite transitions of the same output (t_{PHL} - t_{PLH}) (2)		1	0.1	0.2	

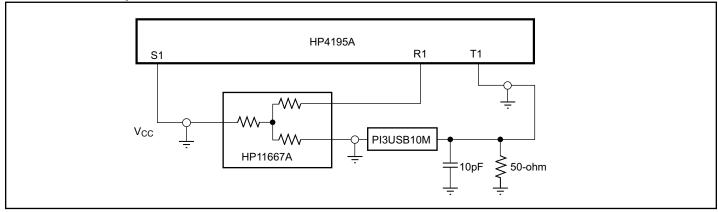
Notes:

- 1. For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Guaranteed by design.
- 3. The switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 10pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interactions with the load on the driven side.

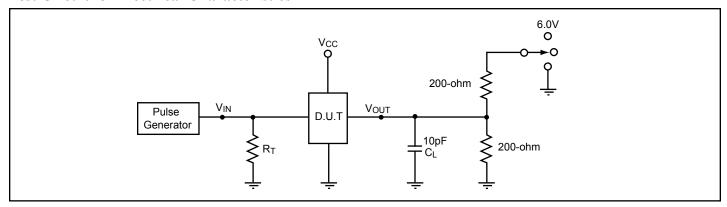
06-0014 3 PS8799B 03/16/06



Test Circuit for Dynamic Electrical Characteristics



Test Circuit for Electrical Characteristics



Notes:

 C_L = Load capacitance: includes jig and probe capacitance.

 R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator

Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control.

Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.

All input impulses are supplied by generators having the following characteristics: PRR \leq MHz, $Z_O = 50\Omega$, $t_R \leq 2.5$ ns, $t_F \leq 2.5$ ns.

The outputs are measured one at a time with on transition per measurement.

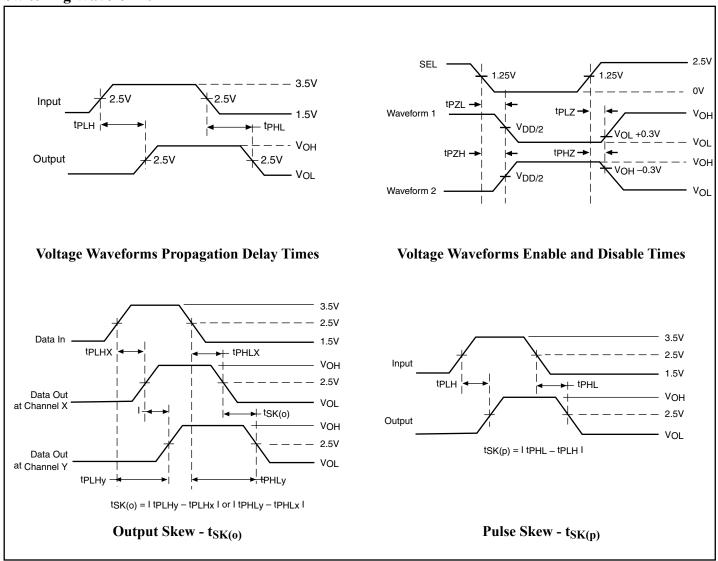
Switch Positions

Test	Switch
t _{PLZ} , t _{PZL} (output on I-side)	6.0V
t _{PHZ} , t _{PZH} (output on I-side)	GND
Prop Delay	Open

06-0014 4 PS8799B 03/16/06



Switching Waveforms



Applications Information

Logic Inputs

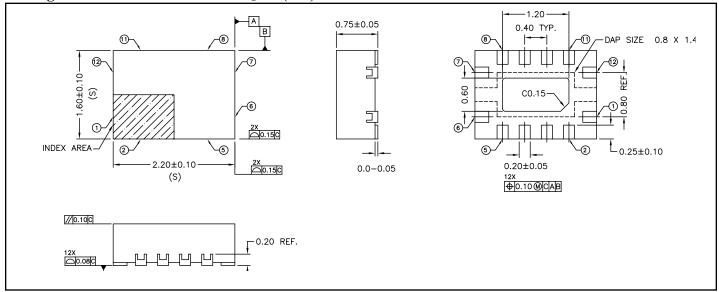
The logic control inputs can be driven up to +3.6V regardless of the supply voltage. For example, given a +3.3V supply, the output enables or select pins may be driven low to 0V and high to 3.6V. Driving IN Rail-to-Rail® minimizes power consumption.

Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd

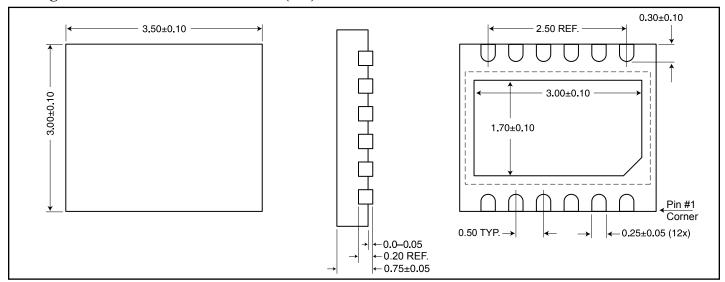
06-0014 5 PS8799B 03/16/06



Package Mechanical: 12-Contact TQFN (ZK)



Package Mechanical: 12-Contact TDFN (ZE)



Ordering Information

Ordering Code	Package Code	Package Description	Top Marking
PI3USB10MZKE	ZK	Pb-free & Green, 12-contact TQFN	UM
PI3USB10MZEE	ZE	Pb-free & Green, 12-contact TDFN	YK

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free and Green
- X suffix = Tape/Reel

Pericom Semiconductor Corporation • 1-800-435-2336 • www.pericom.com