DATA SHEET

NEC

MOS INTEGRATED CIRCUIT

μPD121WxxA Series

HIGH CURRENT 1.5 A GENERAL-PURPOSE CMOS REGULATOR

DESCRIPTION

The μPD121WxxA series is general-purpose CMOS regulators which have 1.5 A output current capacity. These products are suitable for power supply of large-scale ASICs etc. By ON/OFF function, the power consumption can be kept low level at the time of off-state. This series of regulator has 3 fixed output voltage type 1.8 V, 2.5 V, 3.3 V, and adjustable output voltage type (1.8 to 3.3 V).

FEATURES

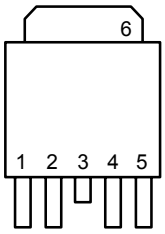
- Output Current: 1.5 A
- Output Voltage: 1.8 V, 2.5 V, 3.3 V (Fixed type) / 1.8 to 3.3 V (Adjustable type)
- Output Voltage Tolerance: $V_O \pm 2.0\%$ (T_J = 25°C)
- Dropout Voltage: V_{DIF} = 1.0 V MAX. (I_O = 1.5 A)
- Quiescent Current: 150 μA TYP. (I_O = 0 A)
- Standby Current: 1 μA
- Available for laminated ceramic capacitor: (Electric capacity 10 μF or higher)
- On-chip over-current protection circuit
- On-chip thermal shut down circuit

APPLICATIONS

These regulators are suitable for large-scale ASICs which are used in digital appliances etc.

PIN CONFIGURATION (Marking Side)

5-PIN TO-252 (5-PIN MP-3ZK)



1. INPUT
2. ON/OFF
3. GND **Note**
4. NC / ADJ
5. OUTPUT
6. GND (Fin)

Note No.3 pin is cut and can not be connected to substrate. No.6 is Fin and common to GND pin.

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.
Not all products and/or types are available in every country. Please check with an NEC Electronics sales representative for availability and additional information.

Document No. G18852EJ1V0DS00 (1st edition)
Date Published July 2007 NS
Printed in Japan

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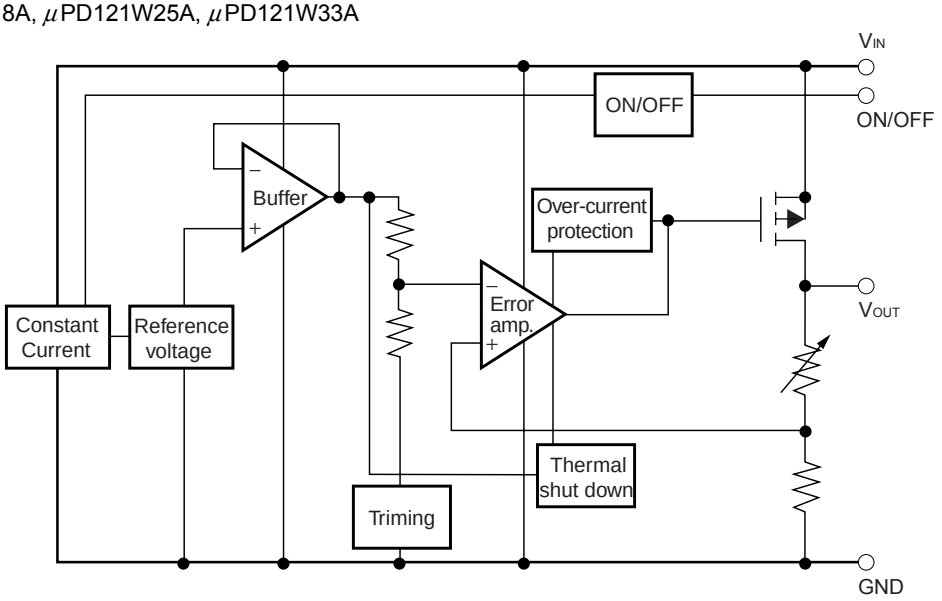
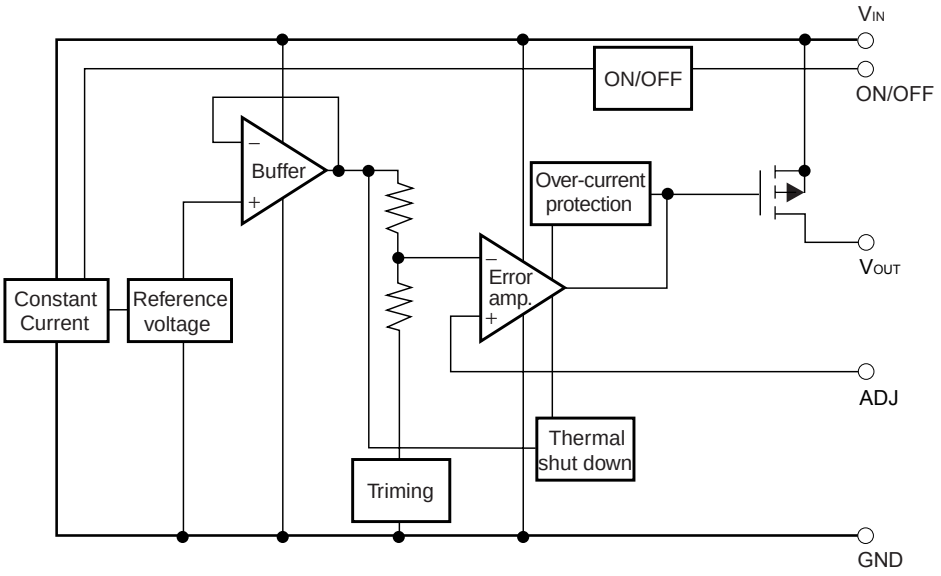
BLOCK DIAGRAM

μPD121W18A, μPD121W25A, μPD121W33A



μPD121W00A



ORDERING INFORMATION

Part Number	Package	Output Voltage	Type	Marking
μPD121W18AT1F	5-PIN TO-252 (5-PIN MP-3ZK)	1.8 V	Fixed	121W18
μPD121W25AT1F	5-PIN TO-252 (5-PIN MP-3ZK)	2.5 V	Fixed	121W25
μPD121W33AT1F	5-PIN TO-252 (5-PIN MP-3ZK)	3.3 V	Fixed	121W33
μPD121W00AT1F	5-PIN TO-252 (5-PIN MP-3ZK)	1.8 to 3.3 V	Adjustable	121W00

Remark Since it is the tape-packaged product, “-E1” or “-E2” is added to the end of its product name.

Part Number	Package	Package Type
μPD121W18AT1F-E1-AT [Note]	5-PIN TO-252 (5-PIN MP-3ZK)	• 16 mm wide embossed taping • Pin 1 on draw-out side • 2,500 pcs/reel
μPD121W18AT1F-E2-AT [Note]	5-PIN TO-252 (5-PIN MP-3ZK)	• 16 mm wide embossed taping • Pin 1 at take-up side • 2,500 pcs/reel
μPD121W25AT1F-E1-AT [Note]	5-PIN TO-252 (5-PIN MP-3ZK)	• 16 mm wide embossed taping • Pin 1 on draw-out side • 2,500 pcs/reel
μPD121W25AT1F-E2-AT [Note]	5-PIN TO-252 (5-PIN MP-3ZK)	• 16 mm wide embossed taping • Pin 1 at take-up side • 2,500 pcs/reel
μPD121W33AT1F-E1-AT [Note]	5-PIN TO-252 (5-PIN MP-3ZK)	• 16 mm wide embossed taping • Pin 1 on draw-out side • 2,500 pcs/reel
μPD121W33AT1F-E2-AT [Note]	5-PIN TO-252 (5-PIN MP-3ZK)	• 16 mm wide embossed taping • Pin 1 at take-up side • 2,500 pcs/reel
μPD121W00AT1F-E1-AT [Note]	5-PIN TO-252 (5-PIN MP-3ZK)	• 16 mm wide embossed taping • Pin 1 on draw-out side • 2,500 pcs/reel
μPD121W00AT1F-E2-AT [Note]	5-PIN TO-252 (5-PIN MP-3ZK)	• 16 mm wide embossed taping • Pin 1 at take-up side • 2,500 pcs/reel

Note Pb-free (This product does not contain Pb in the external electrode and other parts.)

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C, unless otherwise specified)

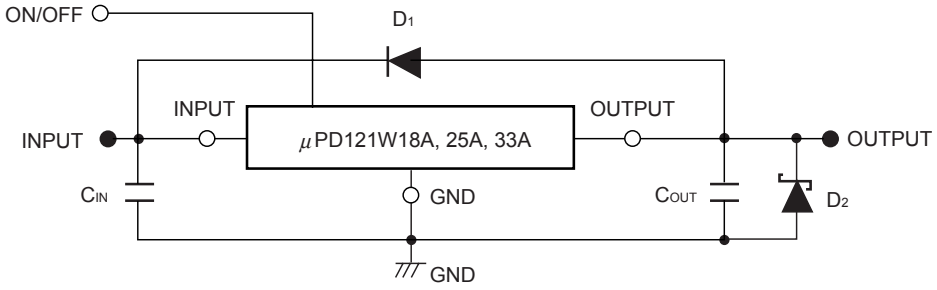
Parameter	Symbol	Rating	Unit
Input Voltage	V_{IN}	−0.3 to +6.0	V
ON/OFF Pin Voltage	$V_{ON/OFF}$	−0.3 to V_{IN}	V
Internal Power Dissipation (T_C = 25°C) **Note**	P_T	10	W
Operating Ambient Temperature	T_A	−40 to +85	°C
Operating Junction Temperature	T_J	−40 to +150	°C
Storage Temperature	T_{stg}	−55 to +150	°C
Thermal Resistance (junction to ambient)	$R_{th(J-A)}$	125	°C/W
Thermal Resistance (junction to case)	$R_{th(J-C)}$	12.5	°C/W

Note Internally limited. When the operating junction temperature rises above 150°C, the internal circuit shuts down the output voltage.

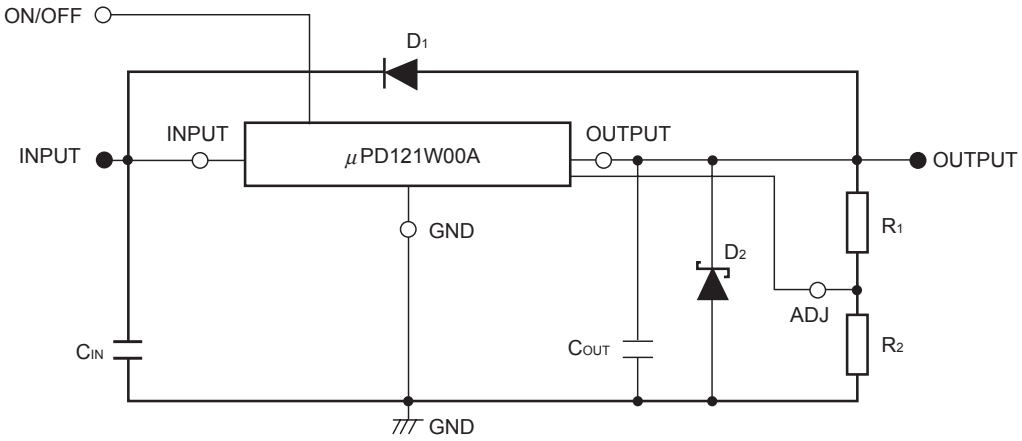
Caution Product quality may suffer if the absolute maximum rating is exceeded even momentarily for any parameter. That is, the absolute maximum ratings are rated values at which the product is on the verge of suffering physical damage, and therefore the product must be used under conditions that ensure that the absolute maximum ratings are not exceeded.

TYPICAL CONNECTION

μPD121W18A, μPD121W25A, μPD121W33A



μPD121W00A



C_{IN} : 0.1 μF or higher. Be sure to connect C_{IN} to prevent parasitic oscillation. Set this value according to the length of the line between the regulator and the INPUT pin. Use of a film capacitor or other capacitor with first-rate voltage and temperature characteristics is recommended. If using a laminated ceramic capacitor, it is necessary to ensure that C_{IN} is 0.1 μF or higher for the voltage and temperature range to be used.

C_{OUT} : 10 μF or higher. Be sure to connect C_{OUT} to prevent oscillation and improve excessive load regulation. Place C_{IN} and C_{OUT} as close as possible to the IC pins (within 1 to 2 cm). Also, in case of using a laminated ceramic capacitor, please note following items.

- It is necessary to ensure that C_{OUT} is 10 μF or higher for the voltage and temperature range to be used.
- In case of using laminated ceramic capacitor, it is easy to become state of parasitic oscillation. Because ESR of laminated ceramic capacitor is very low. Therefore, the capacitor and load condition (output current) which fulfill the condition of the stable operation area of ESR shown below are recommended.
- Stable Operation Area as below is regulated under condition of which this product is not on a substrate. Therefore impedance on substrate is not considered.

D_1 : If the OUTPUT pin has a higher voltage than the INPUT pin, connect a diode.

D_2 : If the OUTPUT pin has a lower voltage than the GND pin, connect a Schottky barrier diode.

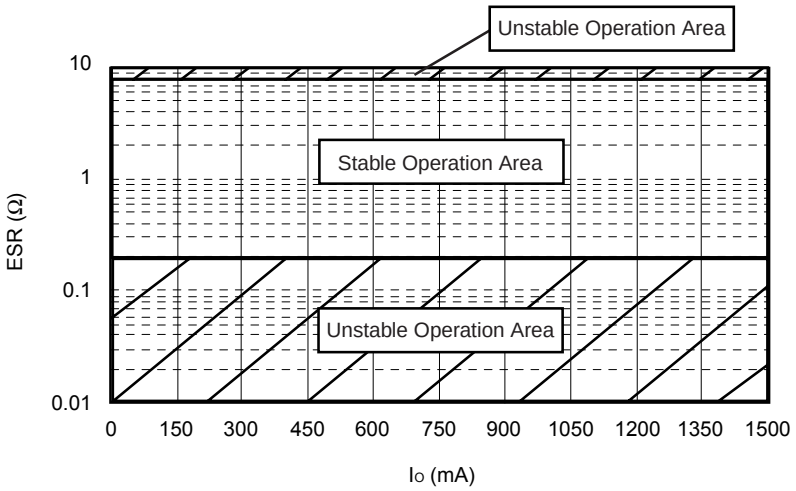
R_1, R_2: The total amount of R_1 and R_2 is sure to below 500 kΩ (375 kΩ TYP.). R_2 = 100 kΩ is recommended.

$V_{OUT} = (1 + R_1/R_2) V_{ADJ}$ **Note**

Note When V_{OUT} = 3.0 V: R_1 = 275 kΩ, R_2 = 100 kΩ

Caution Make sure that no external voltage is applied to the OUTPUT pin.

μPD121WxxA Series C_{OUT} ESR Stable Operation Area



RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Type Number	MIN.	TYP.	MAX.	Unit
Input Voltage	V_{IN}	μPD121W18A	2.8		5.5	V
		μPD121W25A	3.5		5.5	V
		μPD121W33A	4.3		5.5	V
		μPD121W00A	V_O + 1		5.5	V
Output Voltage	V_O	μPD121W00A	1.8		3.3	V
ON/OFF Pin Voltage	$V_{ON/OFF}$	All	0		V_{IN}	V
Output Current	I_O	All	0		1.5	A
Operating Ambient Temperature	T_A	All	−40		+85	°C
Operating Junction Temperature	T_J	All	−40		+125	°C

Caution1. Turn on V_{IN} and $V_{ON/OFF}$ at the same time, or turn on V_{IN} first and then $V_{ON/OFF}$.
Turn off V_{IN} and $V_{ON/OFF}$ at the same time, or turn off $V_{ON/OFF}$ first and then V_{IN}.

2. If absolute maximum rating is not exceeded, you can used this product above the recommended operating range. However, since a margin with absolute maximum rating decreases, please use this product after sufficient evaluation.

ELECTRICAL CHARACTERISTICS

μPD121W18A

(T_J = 25°C, V_{IN} = $V_{ON/OFF}$ = 2.8 V, I_O = 1.0 A, C_{IN} = 0.1 μF, C_{OUT} = 10 μF, unless otherwise specified)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	V_{O1}	–	1.764	1.8	1.836	V
	V_{O2}	2.8 V ≤ V_{IN} ≤ 5.5 V, 0 A ≤ I_O ≤ 1.5 A	(1.746)	–	(1.854)	V
Line Regulation	REG_{IN}	2.8 V ≤ V_{IN} ≤ 5.5 V	–	1	18	mV
Load Regulation	REG_L	0 A ≤ I_O ≤ 1.5 A	–	1	18	mV
Quiescent Current	I_{BIAS1}	I_O = 0 A	–	150	300	μA
	I_{BIAS2}	I_O = 1.5 A	–	(2500)	(5000)	μA
Quiescent Current Change	ΔI_{BIAS1}	2.8 V ≤ V_{IN} ≤ 5.5 V	–	(100)	(300)	μA
	ΔI_{BIAS2}	0 A ≤ I_O ≤ 1.5 A	–	(2350)	(5000)	μA
Output Noise Voltage	V_n	10 Hz ≤ f ≤ 100 kHz	–	160	–	$\mu V_{r.m.s.}$
Ripple Rejection	R • R	f = 1 kHz, 2.8 V ≤ V_{IN} ≤ 3.8 V	–	65	–	dB
Dropout Voltage	V_{DIF}	I_O = 1.5 A	–	0.6	1.0	V
Short Circuit Current	I_{Oshort}	–	–	1.0	–	A
Peak Output Current	I_{Opeak}	–	1.5	–	–	A
Temperature Coefficient of Output Voltage	$\Delta V_O/\Delta T$	I_O = 5 mA, 0°C ≤ T_J ≤ 125°C	–	0.01	–	mV/°C
ON-state Voltage	V_{ON}	I_O = 0 A	1.5	–	V_{IN}	V
OFF-state Voltage	V_{OFF}	I_O = 0 A	–	–	0.5	V
ON-state ON/OFF Pin Current	I_{ON}	I_O = 0 A	–	–	2	μA
Standby Current	$I_{BIAS(OFF)}$	$V_{ON/OFF}$ = 0 V	–	–	1	μA

Remark Values in parentheses are product design values, and are thus provided as reference values.

μPD121W25A

(T_J = 25°C, V_{IN} = $V_{ON/OFF}$ = 3.5 V, I_O = 1.0 A, C_{IN} = 0.1 μF, C_{OUT} = 10 μF, unless otherwise specified)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	V_{O1}	–	2.45	2.5	2.55	V
	V_{O2}	3.5 V ≤ V_{IN} ≤ 5.5 V, 0 A ≤ I_O ≤ 1.5 A	(2.425)	–	(2.575)	V
Line Regulation	REG_{IN}	3.5 V ≤ V_{IN} ≤ 5.5 V	–	1	25	mV
Load Regulation	REG_L	0 A ≤ I_O ≤ 1.5 A	–	1	25	mV
Quiescent Current	I_{BIAS1}	I_O = 0 A	–	150	300	μA
	I_{BIAS2}	I_O = 1.5 A	–	(2500)	(5000)	μA
Quiescent Current Change	ΔI_{BIAS1}	3.5 V ≤ V_{IN} ≤ 5.5 V	–	(100)	(300)	μA
	ΔI_{BIAS2}	0 A ≤ I_O ≤ 1.5 A	–	(2350)	(5000)	μA
Output Noise Voltage	V_n	10 Hz ≤ f ≤ 100 kHz	–	230	–	$\mu V_{r.m.s.}$
Ripple Rejection	R • R	f = 1 kHz, 3.5 V ≤ V_{IN} ≤ 4.5 V	–	60	–	dB
Dropout Voltage	V_{DIF}	I_O = 1.5 A	–	0.7	1.0	V
Short Circuit Current	I_{Oshort}	–	–	1.0	–	A
Peak Output Current	I_{Opeak}	–	1.5	–	–	A
Temperature Coefficient of Output Voltage	$\Delta V_O/\Delta T$	I_O = 5 mA, 0°C ≤ T_J ≤ 125°C	–	–0.07	–	mV/°C
ON-state Voltage	V_{ON}	I_O = 0 A	1.5	–	V_{IN}	V
OFF-state Voltage	V_{OFF}	I_O = 0 A	–	–	0.5	V
ON-state ON/OFF Pin Current	I_{ON}	I_O = 0 A	–	–	2	μA
Standby Current	$I_{BIAS(OFF)}$	$V_{ON/OFF}$ = 0 V	–	–	1	μA

Remark Values in parentheses are product design values, and are thus provided as reference values.

μPD121W33A

(T_J = 25°C, V_{IN} = $V_{ON/OFF}$ = 5.0 V, I_O = 1.0 A, C_{IN} = 0.1 μF, C_{OUT} = 10 μF, unless otherwise specified)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	V_{O1}	–	3.234	3.3	3.366	V
	V_{O2}	4.3 V ≤ V_{IN} ≤ 5.5 V, 0 A ≤ I_O ≤ 1.5 A	(3.201)	–	(3.399)	V
Line Regulation	REG_{IN}	4.3 V ≤ V_{IN} ≤ 5.5 V	–	1	33	mV
Load Regulation	REG_L	0 A ≤ I_O ≤ 1.5 A	–	1	33	mV
Quiescent Current	I_{BIAS1}	I_O = 0 A	–	150	300	μA
	I_{BIAS2}	I_O = 1.5 A	–	(2500)	(5000)	μA
Quiescent Current Change	ΔI_{BIAS1}	4.3 V ≤ V_{IN} ≤ 5.5 V	–	(100)	(300)	μA
	ΔI_{BIAS2}	0 A ≤ I_O ≤ 1.5 A	–	(2350)	(5000)	μA
Output Noise Voltage	V_n	10 Hz ≤ f ≤ 100 kHz	–	340	–	$\mu V_{r.m.s.}$
Ripple Rejection	R • R	f = 1 kHz, 4.3 V ≤ V_{IN} ≤ 5.3 V	–	60	–	dB
Dropout Voltage	V_{DIF}	I_O = 1.5 A	–	0.6	1.0	V
Short Circuit Current	I_{Oshort}	–	–	1.0	–	A
Peak Output Current	I_{Opeak}	–	1.5	–	–	A
Temperature Coefficient of Output Voltage	$\Delta V_O/\Delta T$	I_O = 5 mA, 0°C ≤ T_J ≤ 125°C	–	–0.1	–	mV/°C
ON-state Voltage	V_{ON}	I_O = 0 A	1.5	–	V_{IN}	V
OFF-state Voltage	V_{OFF}	I_O = 0 A	–	–	0.5	V
ON-state ON/OFF Pin Current	I_{ON}	I_O = 0 A	–	–	2	μA
Standby Current	$I_{BIAS(OFF)}$	$V_{ON/OFF}$ = 0 V	–	–	1	μA

Remark Values in parentheses are product design values, and are thus provided as reference values.

μPD121W00A

(T_J = 25°C, V_{IN} = $V_{ON/OFF}$ = 5.0 V, I_O = 1.0 A, V_O = 3.0 V, C_{IN} = 0.1 μF, C_{OUT} = 10 μF, unless otherwise specified)

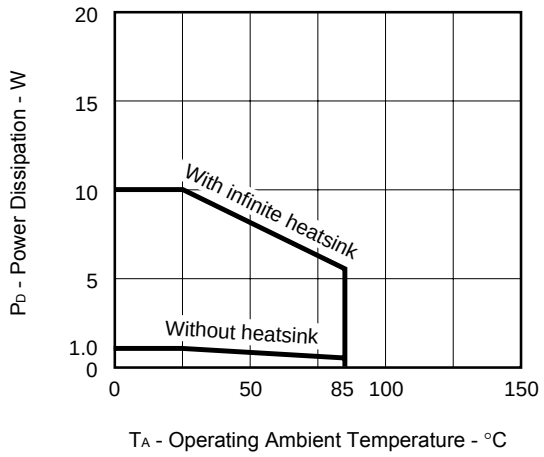
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Reference Voltage	V_{ADJ1}	–	–2% Note	0.8	+2% Note	V
	V_{ADJ2}	2.8 V ≤ V_{IN} ≤ 5.5 V, 0 A ≤ I_O ≤ 1.5 A	(–3%) Note	–	(+3%) Note	V
Line Regulation	REG_{IN}	V_O + 1 V ≤ V_{IN} ≤ 5.5 V	–	1	1% Note	mV
Load Regulation	REG_L	0 A ≤ I_O ≤ 1.5 A	–	1	1% Note	mV
Quiescent Current	I_{BIAS1}	I_O = 0 A	–	150	300	μA
	I_{BIAS2}	I_O = 1.5 A	–	(2500)	(5000)	μA
Quiescent Current Change	ΔI_{BIAS1}	V_O + 1 V ≤ V_{IN} ≤ 5.5 V	–	(100)	(300)	μA
	ΔI_{BIAS2}	0 A ≤ I_O ≤ 1.5 A	–	(2350)	(5000)	μA
Output Noise Voltage	V_n	10 Hz ≤ f ≤ 100 kHz	–	220	–	$\mu V_{r.m.s.}$
Ripple Rejection	R • R	f = 1 kHz, V_O + 1 V ≤ V_{IN} ≤ V_O + 2 V	–	70	–	dB
Dropout Voltage	V_{DIF}	I_O = 1.5 A	–	0.6	1.0	V
Short Circuit Current	I_{Oshort}	–	–	1.0	–	A
Peak Output Current	I_{Opeak}	–	1.5	–	–	A
Temperature Coefficient of Output Voltage	$\Delta V_O/\Delta T$	I_O = 5 mA, 0°C ≤ T_J ≤ 125°C	–	–0.12	–	mV/°C
ON-state Voltage	V_{ON}	I_O = 0 A	1.5	–	V_{IN}	V
OFF-state Voltage	V_{OFF}	I_O = 0 A	–	–	0.5	V
ON-state ON/OFF Pin Current	I_{ON}	I_O = 0 A	–	–	2	μA
Standby Current	$I_{BIAS(OFF)}$	$V_{ON/OFF}$ = 0 V	–	–	1	μA

Note This is the percentage to the output voltage (V_O: the unit is V).

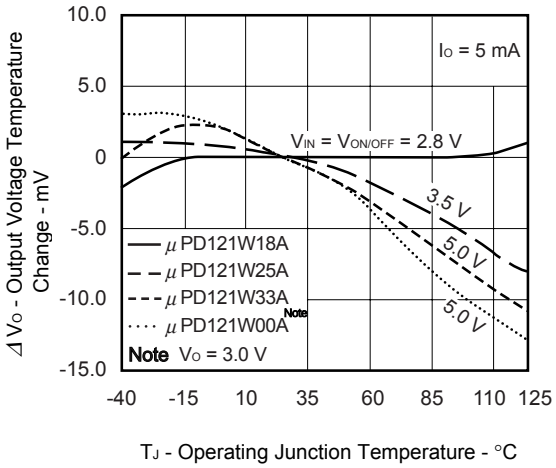
Remark Values in parentheses are product design values, and are thus provided as reference values.

TYPICAL CHARACTERISTICS

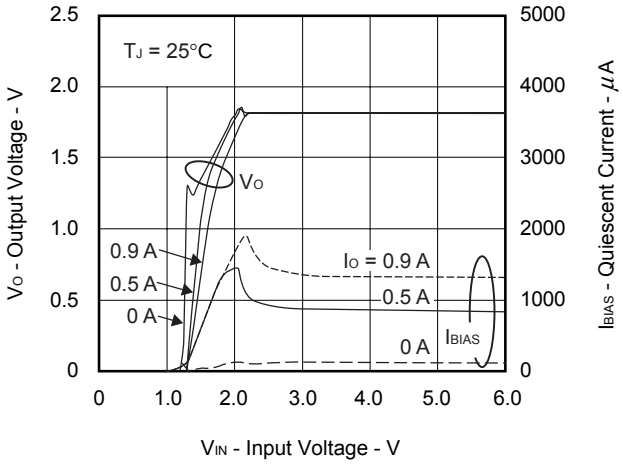
P_D vs. T_A



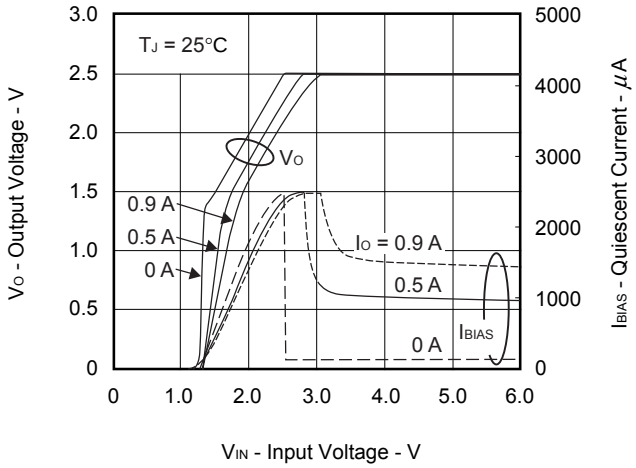
ΔV_O vs.T_J



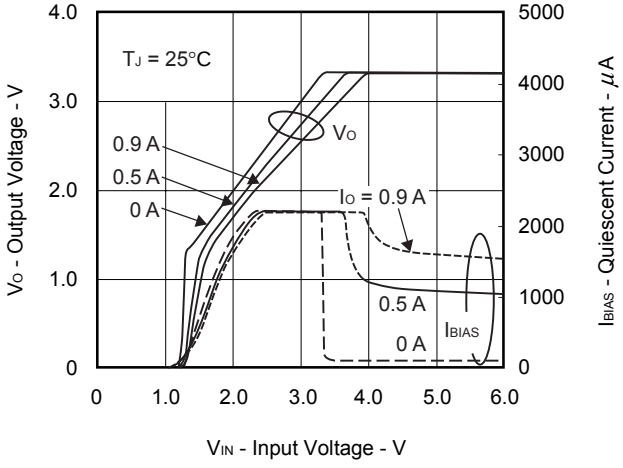
V_O vs. V_{IN}, I_{BIAS} vs. V_{IN} (μPD121W18A)



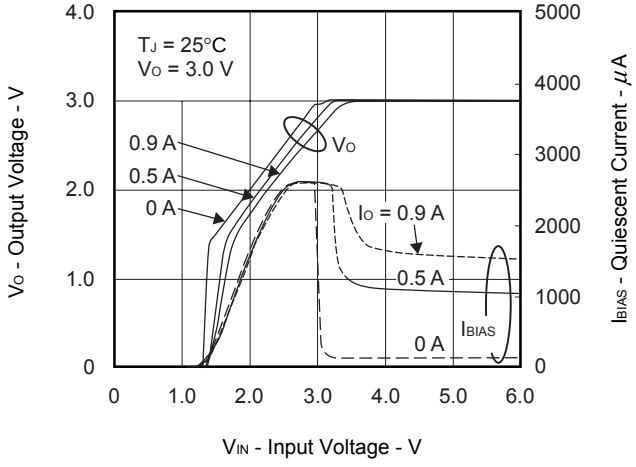
V_O vs. V_{IN}, I_{BIAS} vs. V_{IN} (μPD121W25A)



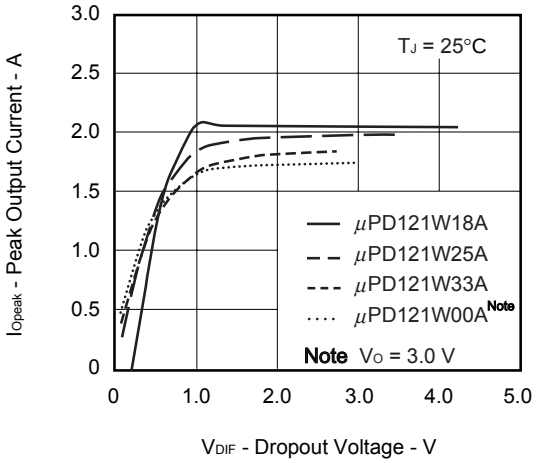
V_O vs. V_{IN}, I_{BIAS} vs. V_{IN} (μPD121W33A)



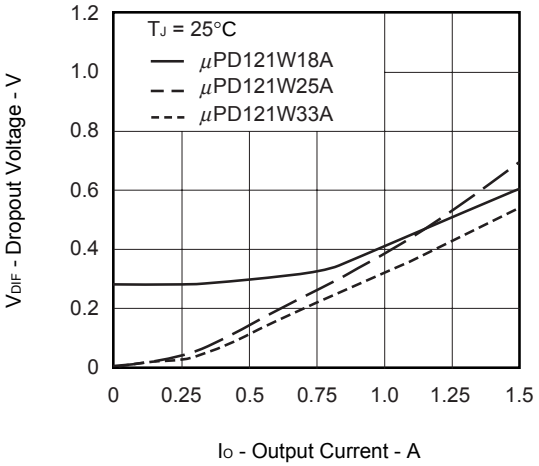
V_O vs. V_{IN}, I_{BIAS} vs. V_{IN} (μPD121W00A)



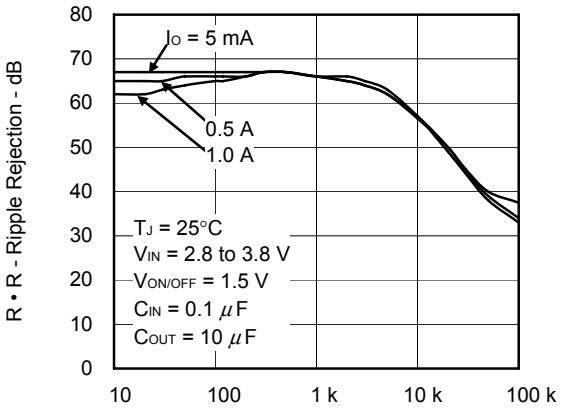
I_{Opeak} vs. V_{DIF}



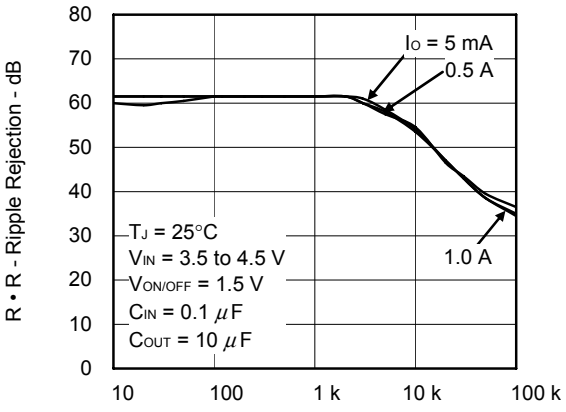
V_{DIF} vs. I_O



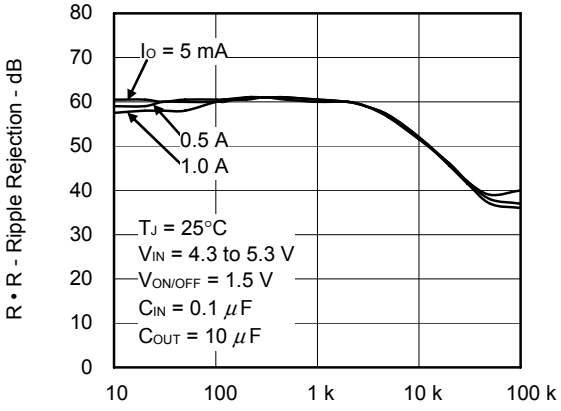
R • R vs. f (μPD121W18A)



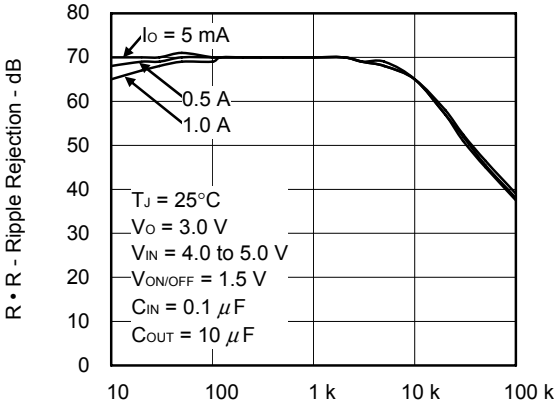
R • R vs. f (μPD121W25A)



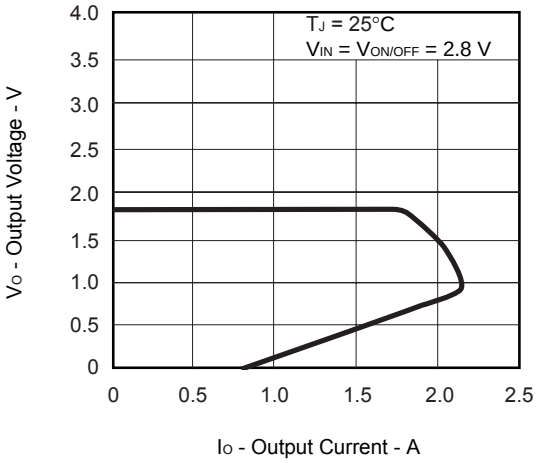
R • R vs. f (μPD121W33A)



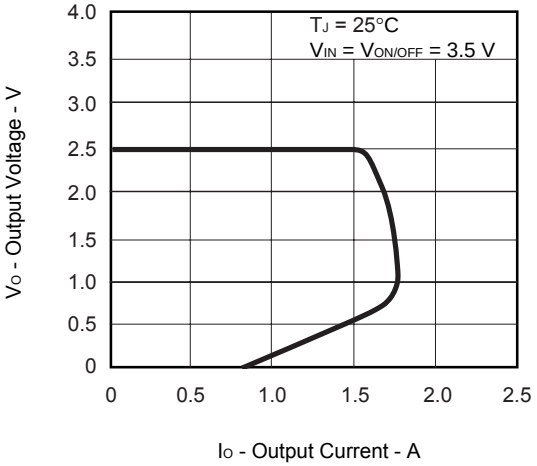
R • R vs. f (μPD121W00A)



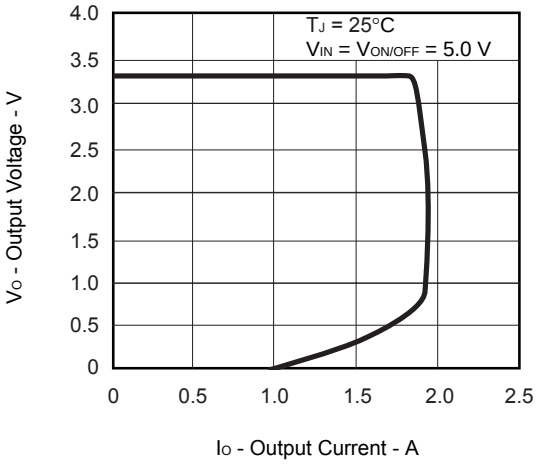
V_O vs. I_O (μPD121W18A)



V_O vs. I_O (μPD121W25A)

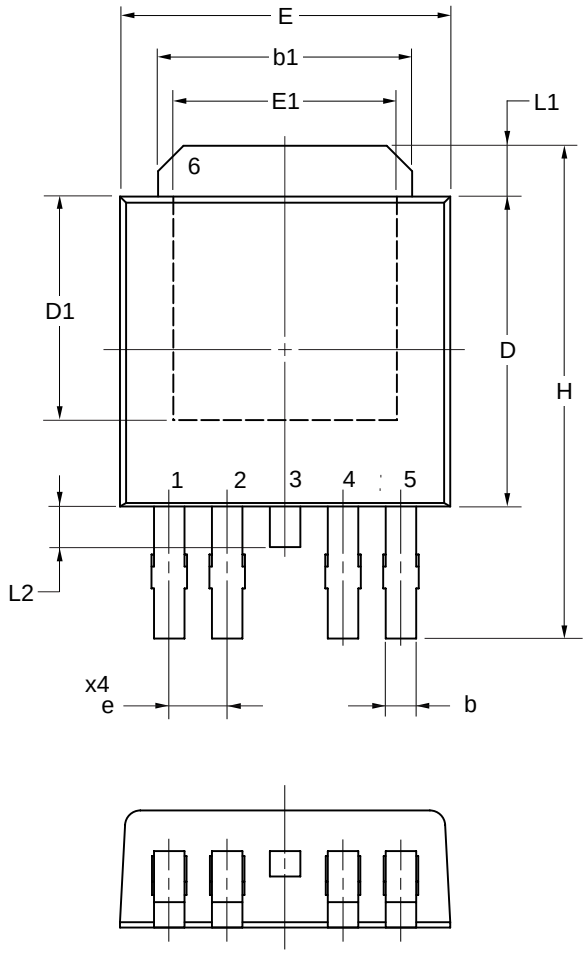


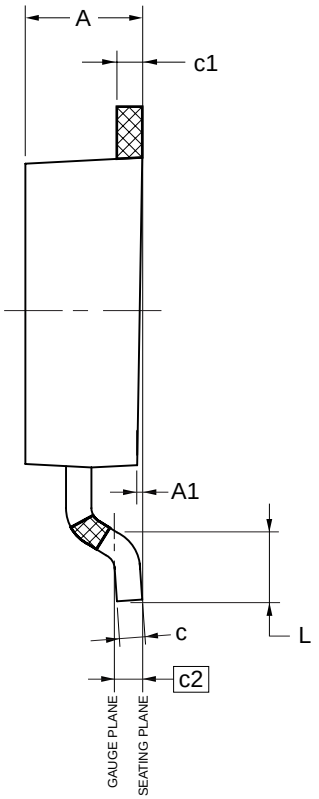
V_O vs. I_O (μPD121W33A)



PACKAGE DRAWING (Unit: mm)

5-PIN TO-252 (MP-3ZK)





NOTE
1. ⊠ No Plating area

(UNIT:mm)

ITEM	DIMENSIONS
D	6.10±0.20
D1	4.4TYP(4.0MIN)
E	6.50±0.20
E1	4.4TYP(4.3MIN)
H	9.8TYP(10.3MAX)
A	2.30±0.10
A1	0 to 0.25
b	0.60±0.10
b1	5.0
c	0.50±0.10
c1	0.50±0.10
c2	0.508
e	1.14
L	1.52±0.12
L1	1.0
L2	0.80

P5T1F-114-1

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RECOMMENDED MOUNTING CONDITIONS

The μPD121WxxA Series should be soldered and mounted under the following recommended conditions.

For soldering methods and conditions other than those recommended below, contact an NEC Electronics sales representative.

For technical information, see the following website.

Semiconductor Device Mount Manual (http://www.necel.com/pkg/en/mount/index.html)

μPD121W18AT1F-AT Note, μPD121W25AT1F-AT Note,
μPD121W33AT1F-AT Note, μPD121W00AT1F-AT Note: 5-PIN TO-252 (5-PIN MP-3ZK)

Process	Conditions	Symbol
Infrared reflow	Package peak temperature: 260°C, Time: 60 seconds MAX. (at 220°C or higher), Count: Three times, Flux: Rosin flux with low chlorine (0.2 Wt% or below) recommended.	IR60-00-3
Partial Heating Method	Pin temperature: 350°C or below, Heat time: 3 seconds or less (per each side of the device).	P350

Note Pb-free (This product does not contain Pb in the external electrode and other parts.)

Caution Apply only one kind of soldering condition to a device, except for "partial heating method", or the device will be damaged by heat stress.

REFERENCE DOCUMENTS

USER'S MANUAL USAGE OF THREE TERMINAL REGULATORS	Document No.G12702E
INFORMATION VOLTAGE REGULATOR OF SMD	Document No.G11872E
SEMICONDUCTOR DEVICE MOUNT MANUAL	http://www.necel.com/pkg/en/mount/index.html

NOTES FOR CMOS DEVICES

① VOLTAGE APPLICATION WAVEFORM AT INPUT PIN

Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between V_{IL} (MAX) and V_{IH} (MIN) due to noise, etc., the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between V_{IL} (MAX) and V_{IH} (MIN).

② HANDLING OF UNUSED INPUT PINS

Unconnected CMOS device inputs can be cause of malfunction. If an input pin is unconnected, it is possible that an internal input level may be generated due to noise, etc., causing malfunction. CMOS devices behave differently than Bipolar or NMOS devices. Input levels of CMOS devices must be fixed high or low by using pull-up or pull-down circuitry. Each unused pin should be connected to V_{DD} or GND via a resistor if there is a possibility that it will be an output pin. All handling related to unused pins must be judged separately for each device and according to related specifications governing the device.

③ PRECAUTION AGAINST ESD

A strong electric field, when exposed to a MOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it when it has occurred. Environmental control must be adequate. When it is dry, a humidifier should be used. It is recommended to avoid using insulators that easily build up static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors should be grounded. The operator should be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions need to be taken for PW boards with mounted semiconductor devices.

④ STATUS BEFORE INITIALIZATION

Power-on does not necessarily define the initial status of a MOS device. Immediately after the power source is turned ON, devices with reset functions have not yet been initialized. Hence, power-on does not guarantee output pin levels, I/O settings or contents of registers. A device is not initialized until the reset signal is received. A reset operation must be executed immediately after power-on for devices with reset functions.

⑤ POWER ON/OFF SEQUENCE

In the case of a device that uses different power supplies for the internal operation and external interface, as a rule, switch on the external power supply after switching on the internal power supply. When switching the power supply off, as a rule, switch off the external power supply and then the internal power supply. Use of the reverse power on/off sequences may result in the application of an overvoltage to the internal elements of the device, causing malfunction and degradation of internal elements due to the passage of an abnormal current.

The correct power on/off sequence must be judged separately for each device and according to related specifications governing the device.

⑥ INPUT OF SIGNAL DURING POWER OFF STATE

Do not input signals or an I/O pull-up power supply while the device is not powered. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements.

Input of signals during the power off state must be judged separately for each device and according to related specifications governing the device.

- **The information in this document is current as of July, 2007. The information is subject to change without notice. For actual design-in, refer to the latest publications of NEC Electronics data sheets or data books, etc., for the most up-to-date specifications of NEC Electronics products. Not all products and/or types are available in every country. Please check with an NEC Electronics sales representative for availability and additional information.**
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  "Specific": Aircraft, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems and medical equipment for life support, etc.

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