



DISCRETE POWER DIODES and THYRISTORS

DATA BOOK



ST183C..C SERIES

INVERTER GRADE THYRISTORS

Hockey Puk Version

Features

- Metal case with ceramic insulator
- International standard case TO-200AB (A-PUK)
- All diffused design
- Center amplifying gate
- Guaranteed high dV/dt
- Guaranteed high dl/dt
- High surge current capability
- Low thermal impedance
- High speed performance

Typical Applications

- Inverters
- Choppers
- Induction heating
- All types of force-commutated converters

370A



case style TO-200AB (A-PUK)

Major Ratings and Characteristics

Parameters	ST183C..C	Units
$I_{T(AV)}$	370	A
@ T_{hs}	55	°C
$I_{T(RMS)}$	690	A
@ T_{hs}	25	°C
I_{TSM}	4900	A
@ 60Hz	5130	A
I^2t	120	KA ² s
@ 60Hz	110	KA ² s
V_{DRM}/V_{RRM}	400 to 800	V
t_q range	10 to 20	μs
T_J	- 40 to 125	°C

ST183C..C Series

ELECTRICAL SPECIFICATIONS

Voltage Ratings

Type number	Voltage Code	V_{DRM}/V_{RRM} , maximum repetitive peak voltage V	V_{RSM} , maximum non-repetitive peak voltage V	I_{DRM}/I_{RRM} max. @ $T_J = T_{J\max}$ mA
ST183C..C	04	400	500	40
	08	800	900	

Current Carrying Capability

Frequency				Units
50Hz	770	660	1220	A
400Hz	730	600	1270	
1000Hz	600	490	1210	
2500Hz	350	270	860	
Recovery voltage V_r	50	50	50	
Voltage before turn-on V_d	V_{DRM}	V_{DRM}	V_{DRM}	V
Rise of on-state current di/dt	50	50	-	A/ μ s
Heatsink temperature	40	55	40	°C
Equivalent values for RC circuit	47Ω / 0.22μF	47Ω / 0.22μF	47Ω / 0.22μF	

On-state Conduction

Parameter	ST183C..C	Units	Conditions
$I_{T(AV)}$ Max. average on-state current @ Heatsink temperature	370 (130)	A	180° conduction, half sine wave double side (single side) cooled
	55 (85)	°C	
$I_{T(RMS)}$ Max. RMS on-state current	690	A	DC@ 25°C heatsink temperature double side cooled
I_{TSM} Max. peak, one half cycle, non-repetitive surge current	4900		t = 10ms No voltage reapplied t = 8.3ms 100% V_{RRM} reapplied t = 10ms No voltage reapplied t = 8.3ms 100% V_{RRM} reapplied
	5130		
	4120		
	4310		
I^2t Maximum I^2t for fusing	120	KA ² s	Sinusoidal half wave, Initial $T_J = T_{J\max}$
	110		
	85		
	78		
$I^2\sqrt{t}$ Maximum $I^2\sqrt{t}$ for fusing	1200	KA ² /s	t = 0.1 to 10ms, no voltage reapplied

ST183C..C Series

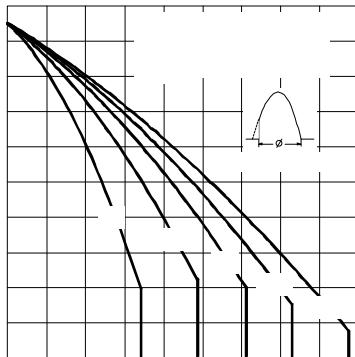


Fig. 3 - Current Ratings Characteristics

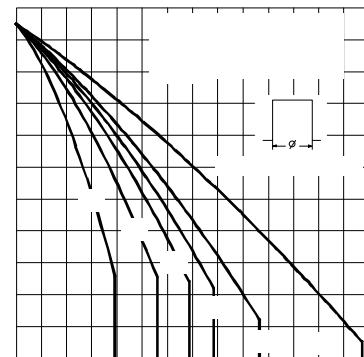


Fig. 4 - Current Ratings Characteristics

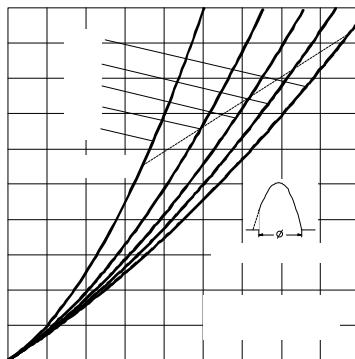


Fig. 5 - On-state Power Loss Characteristics

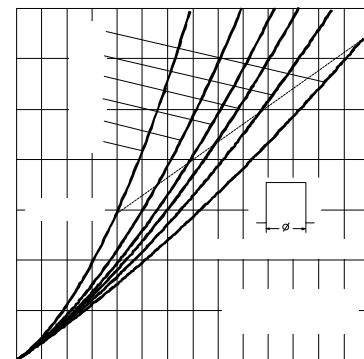


Fig. 6 - On-state Power Loss Characteristics

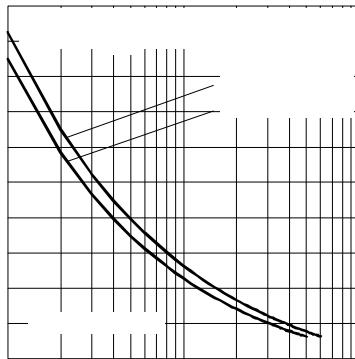


Fig. 7 - Maximum Non-repetitive Surge Current
Single and Double Side Cooled

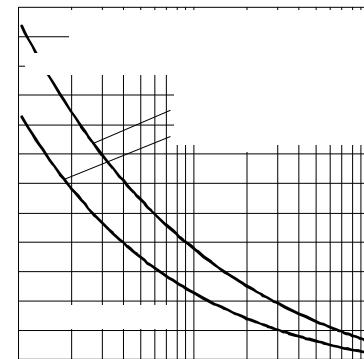


Fig. 8 - Maximum Non-repetitive Surge Current
Single and Double Side Cooled

ST183C..C Series

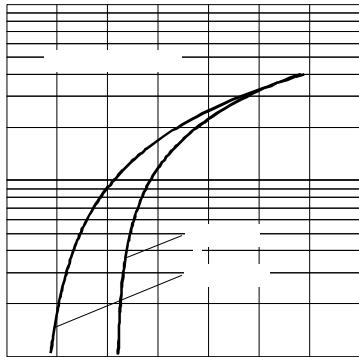


Fig. 9 - On-state Voltage Drop Characteristics

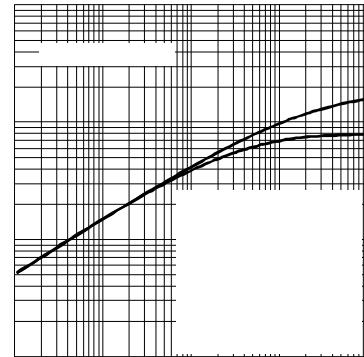


Fig. 10 - Thermal Impedance Z_{thJ-hs} Characteristics

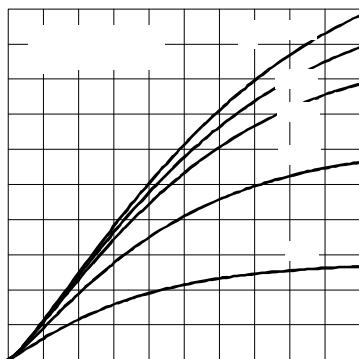


Fig. 11 - Reverse Recovered Charge Characteristics

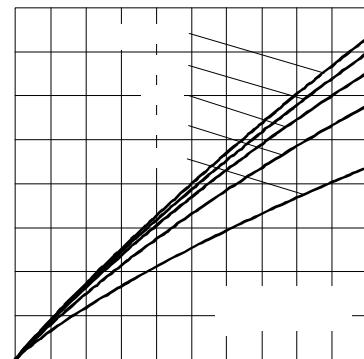


Fig. 12 - Reverse Recovery Current Characteristics

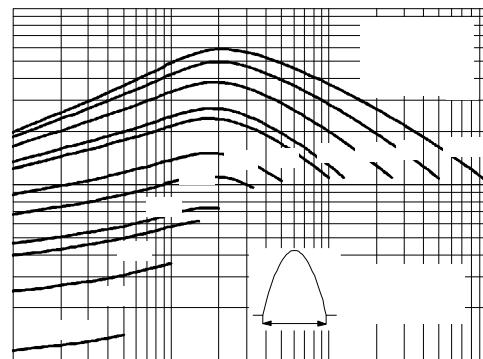
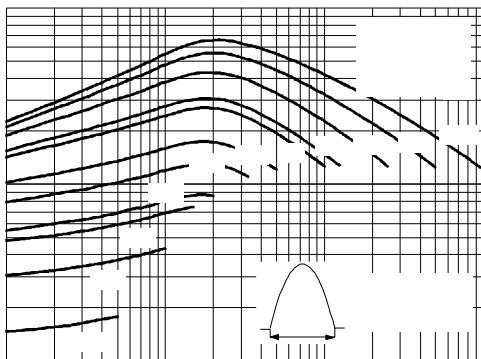


Fig. 13 - Frequency Characteristics

ST183C..C Series

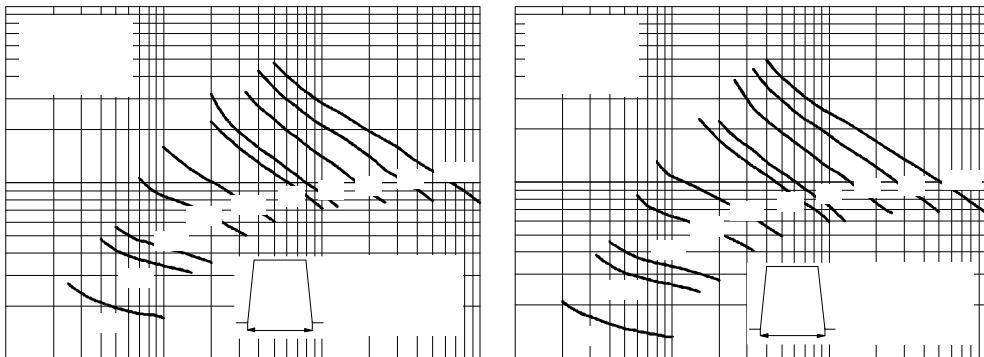


Fig. 14 - Frequency Characteristics

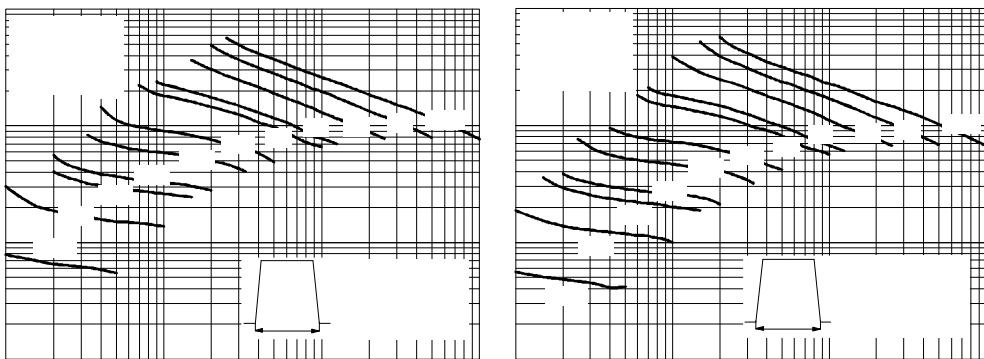


Fig. 15 - Frequency Characteristics

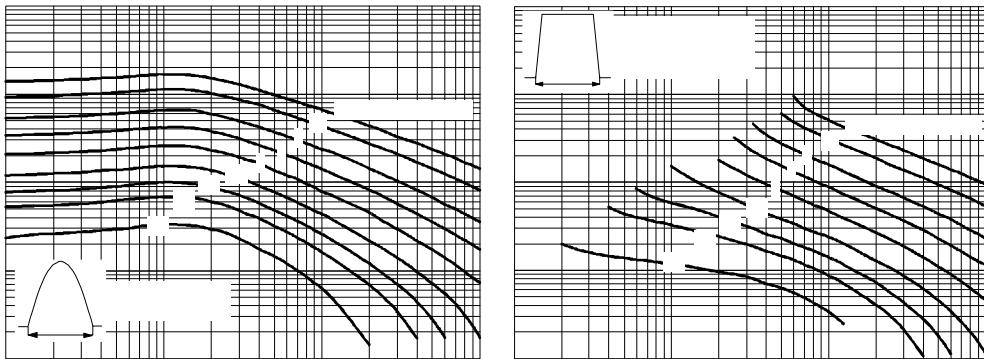


Fig. 16 - Maximum On-state Energy Power Loss Characteristics

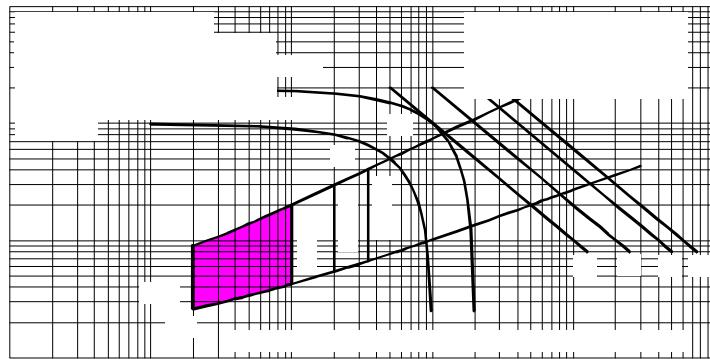


Fig. 17 - Gate Characteristics

ST183C..C Series

On-state Conduction

Parameter	ST183C..C	Units	Conditions	
V_{TM}	Max. peak on-state voltage	1.80	V	$I_{TM} = 600A, T_J = T_J \text{ max}, t_p = 10\text{ms sine wave pulse}$
$V_{T(TO)1}$	Low level value of threshold voltage	1.40		$(16.7\% \times \pi \times I_{T(AV)} < I < \pi \times I_{T(AV)}, T_J = T_J \text{ max.})$
$V_{T(TO)2}$	High level value of threshold voltage	1.45		$(I > \pi \times I_{T(AV)}), T_J = T_J \text{ max.}$
r_{t1}	Low level value of forward slope resistance	0.67	$\text{m}\Omega$	$(16.7\% \times \pi \times I_{T(AV)} < I < \pi \times I_{T(AV)}, T_J = T_J \text{ max.})$
r_{t2}	High level value of forward slope resistance	0.58		$(I > \pi \times I_{T(AV)}), T_J = T_J \text{ max.}$
I_H	Maximum holding current	600	mA	$T_J = 25^\circ\text{C}, I_T > 30\text{A}$
I_L	Typical latching current	1000		$T_J = 25^\circ\text{C}, V_A = 12\text{V}, R_a = 6\Omega, I_G = 1\text{A}$

Switching

Parameter	ST183C..C	Units	Conditions	
di/dt	Max. non-repetitive rate of rise of turned-on current	1000	$\text{A}/\mu\text{s}$	$T_J = T_J \text{ max}, V_{DRM} = \text{rated } V_{DRM}$ $I_{TM} = 2 \times di/dt$
t_d	Typical delay time	1.1		$T_J = 25^\circ\text{C}, V_{DM} = \text{rated } V_{DRM}, I_{TM} = 50\text{A DC}, t_p = 1\mu\text{s}$ Resistive load, Gate pulse: 10V, 5Ω source
t_q	Max. turn-off time	Min 10 Max 20		$T_J = T_J \text{ max}, I_{TM} = 300\text{A}, \text{commutating } di/dt = 20\text{A}/\mu\text{s}$ $V_R = 50\text{V}, t_p = 500\mu\text{s}, dv/dt: \text{see table in device code}$

Blocking

Parameter	ST183C..C	Units	Conditions
dv/dt	Maximum critical rate of rise of off-state voltage	500	$T_J = T_J \text{ max. linear to } 80\% V_{DRM}, \text{ higher value available on request}$
I_{RRM} I_{DRM}	Max. peak reverse and off-state leakage current	40	$T_J = T_J \text{ max, rated } V_{DRM}/V_{RRM} \text{ applied}$

Triggering

Parameter	ST183C..C	Units	Conditions	
P_{GM}	Maximum peak gate power	60	W	$T_J = T_J \text{ max, } f = 50\text{Hz, d\% = 50}$
$P_{G(AV)}$	Maximum average gate power	10		
I_{GM}	Max. peak positive gate current	10	A	$T_J = T_J \text{ max, } t_p \leq 5\text{ms}$
$+V_{GM}$	Maximum peak positive gate voltage	20		
$-V_{GM}$	Maximum peak negative gate voltage	5	V	$T_J = T_J \text{ max, } t_p \leq 5\text{ms}$
I_{GT}	Max. DC gate current required to trigger	200		
V_{GT}	Max. DC gate voltage required to trigger	3	V	$T_J = 25^\circ\text{C}, V_A = 12\text{V, Ra = 6\Omega}$
I_{GD}	Max. DC gate current not to trigger	20	mA	
V_{GD}	Max. DC gate voltage not to trigger	0.25	V	$T_J = T_J \text{ max, rated } V_{DRM} \text{ applied}$

ST183C..C Series

Thermal and Mechanical Specification

Parameter	ST183C..C	Units	Conditions
T _J	Max. operating temperature range	°C	-40 to 125
T _{stg}	Max. storage temperature range		-40 to 150
R _{thJ-hs}	Max. thermal resistance, junction to heatsink	K/W	DC operation single side cooled
	0.17 0.08		DC operation double side cooled
R _{thC-hs}	Max. thermal resistance, case to heatsink	K/W	DC operation single side cooled
	0.033 0.017		DC operation double side cooled
F	Mounting force, ± 10%	N (Kg)	4900 (500)
wt	Approximate weight	g	50
Case style	TO - 200AB (A-PUK)	See Outline Table	

ΔR_{thJ-hs} Conduction

(The following table shows the increment of thermal resistance R_{thJ-hs} when devices operate at different conduction angles than DC)

Conduction angle	Sinusoidal conduction		Rectangular conduction		Units	Conditions
	Single Side	Double Side	Single Side	Double Side		
180°	0.015	0.016	0.011	0.011	K/W	T _J = T _J max.
120°	0.018	0.019	0.019	0.019		
90°	0.024	0.024	0.026	0.026		
60°	0.035	0.035	0.036	0.037		
30°	0.060	0.060	0.060	0.061		

Ordering Information Table

Device Code	ST	18	3	C	08	C	H	K	1	
	1	2	3	4	5	6	7	8	9	10
1	- Thyristor									
2	- Essential part number									
3	- 3 = Fast turn off									
4	- C = Ceramic Puk									
5	- Voltage code: Code x 100 = V _{RRM} (See Voltage Rating Table)									
6	- C = Puk Case TO-200AB (A-PUK)									
7	- Reapplied dv/dt code (for t _q test condition)									
8	- t _q code									
9	- 0 = Eyelet term. (Gate and Aux. Cathode Unsoldered Leads) 1 = Fast-on term. (Gate and Aux. Cathode Unsoldered Leads) 2 = Eyelet term. (Gate and Aux. Cathode Soldered Leads) 3 = Fast-on term. (Gate and Aux. Cathode Soldered Leads)									
10	- Critical dv/dt: None = 500V/μsec (Standard value) L = 1000V/μsec (Special selection)									

dv/dt - t _q combinations available					
dv/dt (V/μs)	20	50	100	200	400
10	CN	DN	EN	FN*	HN
12	CM	DM	EM	FM	HM
15	CL	DL	EL	FL*	HL
18	CP	DP	EP	FP	HP
20	CK	DK	EK	FK	HK

*Standard part number.
All other types available only on request.

ST183C..C Series

Outline Table

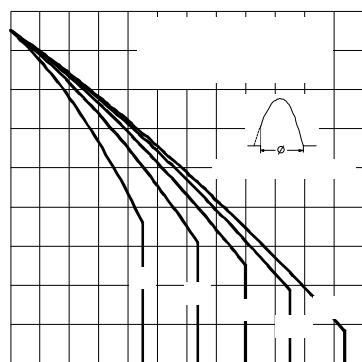
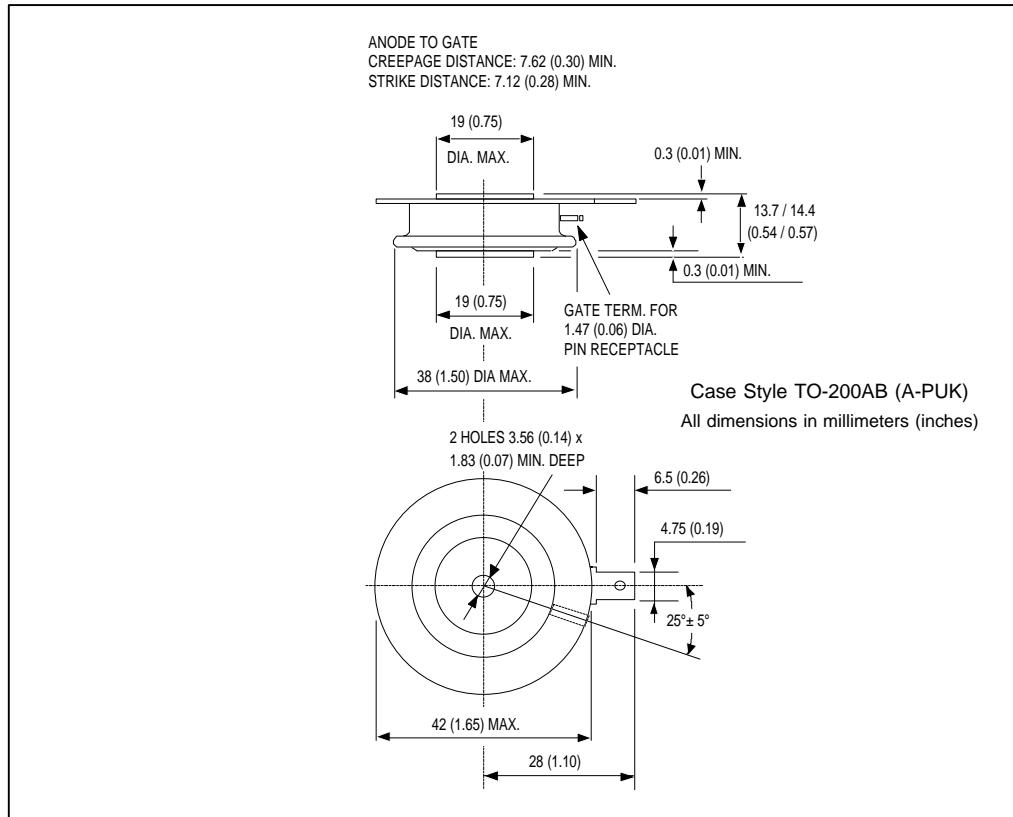


Fig. 1 - Current Ratings Characteristics

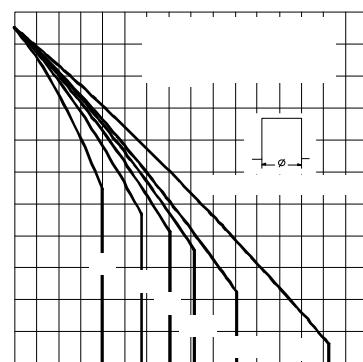


Fig. 2 - Current Ratings Characteristics