Small switching (–20V, –1.5A)

QS5U26

Features

- The QS5U26 conbines Pch Treueh MOSFET with a Schottky barrier diode in a single TSMT5 package.
- 2) Pch Treueh MOSFET have a low on-state resisternce with a fast switching.
- 3) Pch Treueh MOSFET is neucted a low voltage drive (2.5V).
- 4) The independently connected Schottky barrier diode have a low forward voltage.

Applications

load switch, DC/DC conversion

●Structure

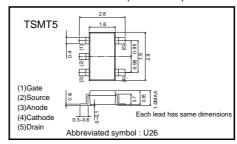
- •Silicon P-channel MOS FET
- Schottky Barrier DIODE

Packaging specifications

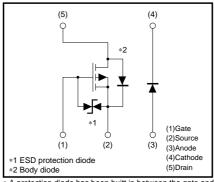
	Package	Taping		
Туре	Code	TR		
	Basic ordering unit (pieces)	3000		
QS5U26		0		

●Absolute maximum ratings (Ta=25°C)

●External dimensions (Units: mm)



●Equivalent circuit



^{*} A protection diode has been built in between the gate and the source to protect against static electricity when the product is in use. Use the protection circuit when rated voltages are exceeded.

<mosfet></mosfet>							
Parameter	Symbol	Limits	Unit				
Drain-source voltage		VDSS	-20	V			
Gate-source voltage		Vgss	±12	V			
Drain current	Continuous	ΙD	±1.5	Α			
	Pulsed	IDP	±6.0	A*1			
Source current	Continuous	Is	-0.75	Α			
(Body diode)	Pulsed	Isp	-3.0	A*1			
Channel temperature		Tch	150	°C			
⟨Di⟩							
Parameter		Symbol	Limits	Unit			
Repetitive peak reverse voltage		VRM	30	V			
Reverse voltage		VR	20	V			
Forward current		lF	0.5	Α			
Forward current surge peak		Iгsм	2.0	A *2			
Junction temperature		Tj	125	°C			
<mosfet and="" di=""></mosfet>							
Parameter		Symbol	Limits	Unit			
Total power dissipatino		P□	1.0	W/TOTAL*3			
Range of strage temperature		Tstg	-40~+125	°C			

*1 Pw≤10µs, Duty cycle≤1% *2 60Hz 1cyc. *3 Mounted on a ceramic board.

ROHM

●Electrical characteristics (Ta=25°C)

<n< th=""><th>O</th><th>S</th><th>F</th><th>E</th><th>T></th></n<>	O	S	F	E	T>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	-	±10	μА	Vgs=±12V, Vps=0V
Drain-source breakdown voltage	V(BR) DSS	-20	-	-	٧	In=-1mA, Vgs=0V
Zero gate voltage drain current	IDSS	-	-	-1	μΑ	Vps=-20V, Vgs=0V
Gate threshold voltage	VGS (th)	-0.7	-	-2.0	V	Vps=-10V, Ip=-1mA
Static drain-source on-starte resistance	RDS (on)	-	160	200	mΩ	In=-1.5A, Vgs=-4.5V
		-	180	240	mΩ	In=-1.5A, Vgs=-4V *
		-	260	340	mΩ	In=-0.75A, Vgs=-2.5V
Forward transfer admittance	Yfs	1.0	-	-	S	Vps=-10V, Ip=-0.75A*
Input capacitance	Ciss	-	325	-	pF	Vps=-10V
Output capacitance	Coss	-	60	-	pF	Vgs=0V
Reverse transfer capacitance	Crss	-	40	-	pF	f=1MHz
Tum-on delay time	td (on)	-	10	-	ns	ID=-0.75A *
Rise time	tr	-	10	-	ns	VDD≒-15V *
Tum-off delay time	td (off)	-	35	-	ns	Vgs=-4.5V R _I =20Ω *
Fall time	tr	-	10	-	ns	R _G s=10Ω *
Total gate charge	Qg	-	4.2	-	nC	V _{DD} ≒−15V
Gate-source charge	Qgs	-	1.0	-	nC	Vgs=-4.5V
Gate-drain charge	Qgd	-	1.1	-	nC	In=-1.5A

^{*} Pulead

●Body diode (Source-drain)

<MOSFET>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp	-	-	-1.2	V	Is=-0.75A, Vgs=0V
<di></di>						
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	VF	-	-	0.36	V	I _F =0.1A
	VF	-	-	0.47	V	I==0.5A
Reverse leakage	l _R	-	-	100	μА	V _R =20V

•Electrical characteristic curves

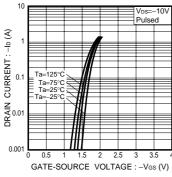


Fig.1 Typical Transfer Characteristics

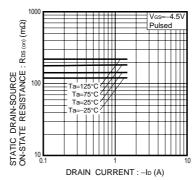


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current (I)

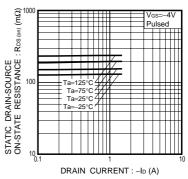


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current (II)

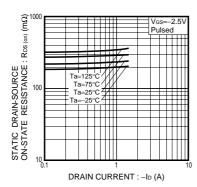


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current (III)

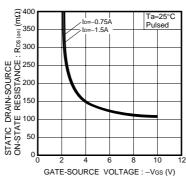


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

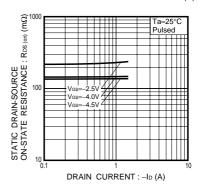


Fig.6 Static Drain-Source On-State Resistance vs. Drain Current (IV)

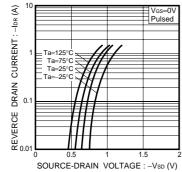


Fig.7 Reverse Drain Current vs. Source-Drain Current

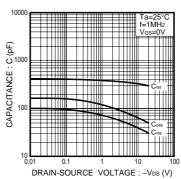


Fig.8 Typical Capacitance vs. Drain-Source Voltage

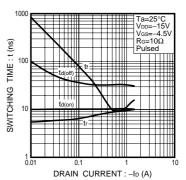
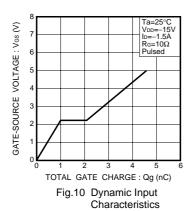


Fig.9 Switching Characteristics



Measurement circuits

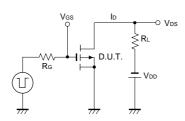


Fig.11 Switching Time Measurement Circuit

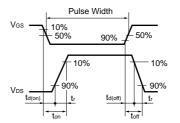


Fig.12 Switching Waveforms

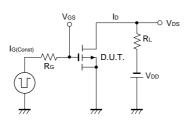


Fig.13 Gate Charge Measurement Circuit

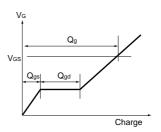


Fig.14 Gate Charge Waveforms

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