NEC

NPN SILICON TRANSISTOR | NE856M23

FEATURES

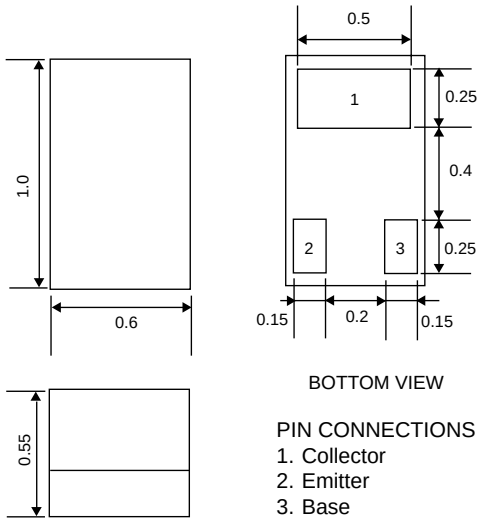
- **NEW MINIATURE M23 PACKAGE:**
  - World's smallest transistor package footprint — leads are completely underneath package body
  - Low profile/0.55 mm package height
  - Ceramic substrate for better RF performance
- **LOW NOISE FIGURE:**
  NF = 1.4 dB at 1 GHz
- **HIGH COLLECTOR CURRENT:**
  I_C MAX = 100 mA

DESCRIPTION

The NE856M23 transistor is designed for low cost amplifier and oscillator applications. Low noise figure, high gain and high current capability equate to wide dynamic range and excellent linearity. NEC's new low profile/ceramic substrate style "M23" package is ideal for today's portable wireless applications. The NE856 is also available in chip, Micro-x, and eight different low cost plastic surface mount package styles.

OUTLINE DIMENSIONS (Units in mm)

PACKAGE OUTLINE M23



BOTTOM VIEW

PIN CONNECTIONS
1. Collector
2. Emitter
3. Base

ELECTRICAL CHARACTERISTICS (T_A = 25°C)

PART NUMBER EIAJ[1] REGISTERED NUMBER PACKAGE OUTLINE		NE856M23 2SC5649 M23			
SYMBOLS	**PARAMETERS AND CONDITIONS**	**UNITS**	**MIN**	**TYP**	**MAX**
f_T	Gain Bandwidth at V_{CE} = 3 V, I_C = 7 mA, f = 1 GHz	GHz	3	4.5	
NF	Noise Figure at V_{CE} = 3 V, I_C = 7 mA, f = 1 GHz	dB		1.4	2.5
$\|S_{21E}\|^2$	Insertion Power Gain at V_{CE} = 3 V, I_C = 7 mA, f = 1 GHz	dB	7	10.0	
h_{FE}[2]	Forward Current Gain at V_{CE} = 3 V, I_C = 7 mA		80		145
I_{CBO}	Collector Cutoff Current at V_{CB} = 10 V, I_E = 0	μA			1
I_{EBO}	Emitter Cutoff Current at V_{EB} = 1 V, I_C = 0	μA			1
C_{RE}[3]	Feedback Capacitance at V_{CB} = 3 V, I_E = 0, f = 1 MHz	pF		0.7	1.5

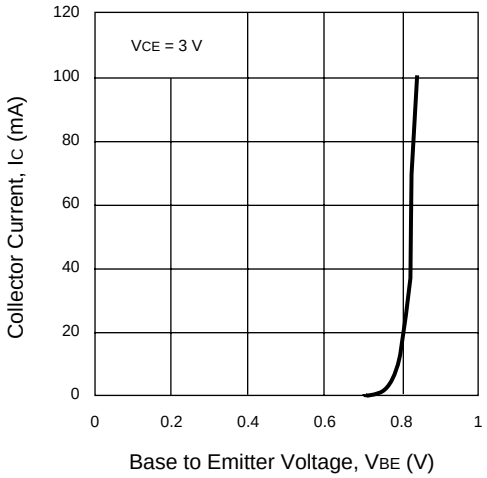
Notes:
1. Electronic Industrial Association of Japan.
2. Pulsed measurement, pulse width ≤ 350 μs, duty cycle ≤ 2 %.
3. Capacitance is measured with emitter and case connected to the guard terminal at the bridge.

ABSOLUTE MAXIMUM RATINGS[1] (T_A = 25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
V_{CBO}	Collector to Base Voltage	V	20
V_{CEO}	Collector to Emitter Voltage	V	12
V_{EBO}	Emitter to Base Voltage	V	3
I_C	Collector Current	mA	100
P_T	Total Power Dissipation	mW	TBD
T_J	Junction Temperature	°C	150
T_{STG}	Storage Temperature	°C	-65 to +150

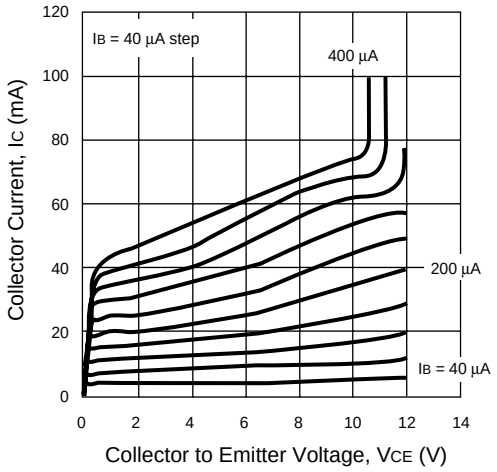
Note:
1. Operation in excess of any one of these parameters may result in permanent damage.

COLLECTOR CURRENT vs. BASE TO EMITTER VOLTAGE

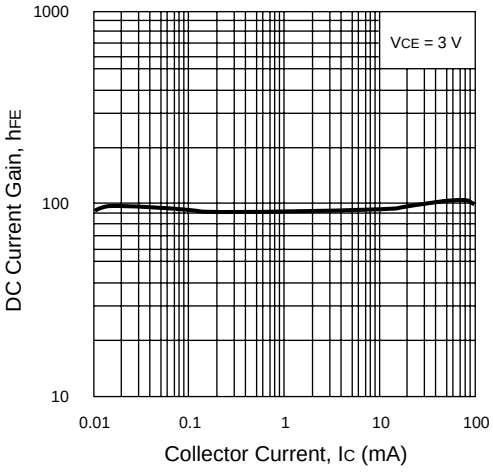


TYPICAL PERFORMANCE CURVES (T_A = 25°C)

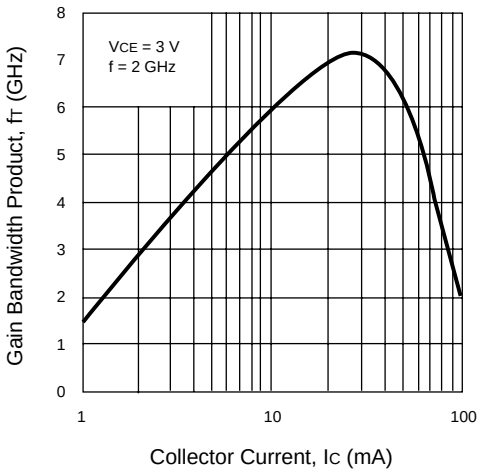
COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



DC CURRENT GAIN vs. COLLECTOR CURRENT



GAIN BANDWIDTH PRODUCT vs. COLLECTOR CURRENT



NOISE FIGURE/ASSOCIATED GAIN vs. COLLECTOR CURRENT