

## KVR400X64C3A/512

### 512MB 64M x 64-Bit DDR400

### CL3 184-Pin DIMM

#### DESCRIPTION:

This document describes ValueRAM's 64M x 64-bit (512MB) DDR400 CL3 SDRAM (Synchronous **DRAM**) memory module. The components on this module include sixteen 32M x 8-bit (8M x 8-bit x 4 Bank) DDR400 SDRAM in TSOP packages. This 184-pin DIMM uses gold contact fingers and requires +2.6V. The electrical and mechanical specifications are as follows:

#### FEATURES:

- |                                 |                               |
|---------------------------------|-------------------------------|
| ✓ Clock Cycle Time (tCK)        | 5ns (min.) / 10ns (max.)      |
| ✓ Row Cycle Time (tRC)          | 55ns (min.)                   |
| ✓ Refresh Row Cycle Time (tRFC) | 70ns (min.)                   |
| ✓ Row Active Time (tRAS)        | 40ns (min.) / 70,000ns (max.) |
| ✓ Single Power Supply of        | +2.6V (+/- .1V)               |
| ✓ Power                         | 3.224 W (operating)           |
| ✓ UL Rating                     | 94 V - 0                      |

**Note:** The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 1.250" (31.75mm).

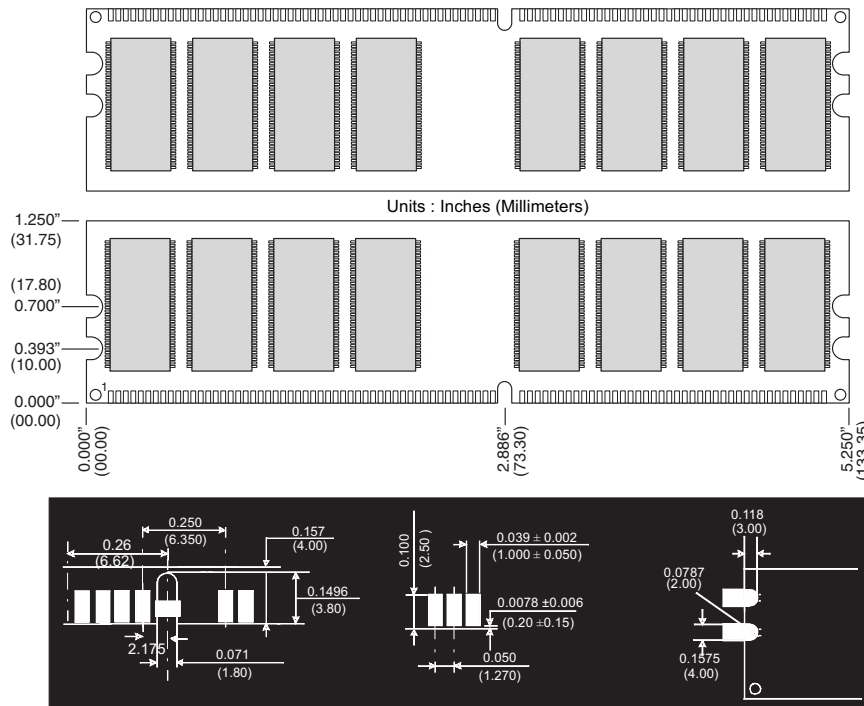


Figure 1: KVR400X64C3A/512 184-Pin DIMM