# RENESAS

# HAF2012(L), HAF2012(S)

Silicon N Channel MOS FET Series Power Switching

> REJ03G1139-0400 Rev.4.00 Jul 13, 2007

#### Description

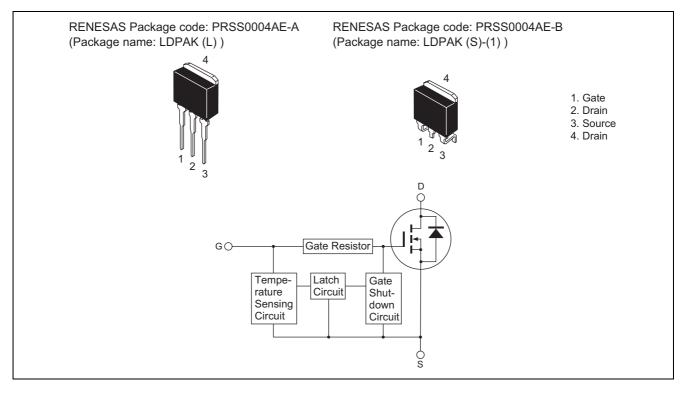
This FET has the over temperature shut-down capability sensing to the junction temperature.

This FET has the built-in over temperature shut-down circuit in the gate area. And this circuit operation to shut-down the gate voltage in case of high junction temperature like applying over power consumption, over current etc.

### Features

- Logic level operation (4 to 6 V Gate drive)
- High endurance capability against to the short circuit
- Built-in the over temperature shut-down circuit
- Latch type shut-down operation (Need 0 voltage recovery)

### Outline



### **Absolute Maximum Ratings**

Symbol	Malua	
	Value	Unit
V <sub>DSS</sub>	60	V
V <sub>GSS</sub>	16	V
V <sub>GSS</sub>	-2.8	V
ID	20	А
D (pulse) Note 1	40	А
I <sub>DR</sub>	20	А
Pch Note 2	50	W
Tch	150	°C
Tstg	-55 to +150	°C
	V <sub>GSS</sub> I <sub>D</sub> I <sub>D (pulse)</sub> <sup>Note 1</sup> I <sub>DR</sub> Pch <sup>Note 2</sup> Tch	V <sub>GSS</sub> -2.8           I <sub>D</sub> 20           I <sub>D (pulse)</sub> Note 1         40           I <sub>DR</sub> 20           Pch Note 2         50           Tch         150

Notes: 1.  $PW \le 10 \ \mu s$ , duty cycle  $\le 1\%$ 

2. Value at Ta =  $25^{\circ}C$ 

## **Typical Operation Characteristics**

						$(Ta = 25^{\circ}C)$
ltem	Symbol	Min	Тур	Max	Unit	Test Conditions
Input voltage	VIH	3.5		—	V	
	V <sub>IL</sub>	—		1.2	V	
Input current	I <sub>IH1</sub>	—		100	μA	$Vi = 8 V, V_{DS} = 0$
(Gate non shut down)	I <sub>IH2</sub>	—		50	μA	$Vi = 3.5 V, V_{DS} = 0$
	IIL	—		1	μA	$Vi = 1.2 V, V_{DS} = 0$
Input current	I <sub>IH (sd) 1</sub>	—	0.8	—	mA	$Vi = 8 V, V_{DS} = 0$
(Gate shut down)	I <sub>IH (sd) 2</sub>	—	0.35	—	mA	$Vi = 3.5 V, V_{DS} = 0$
Shut down temperature	Tsd	—	175	—	°C	Channel temperature
Gate operation voltage	V <sub>OP</sub>	3.5		13	V	

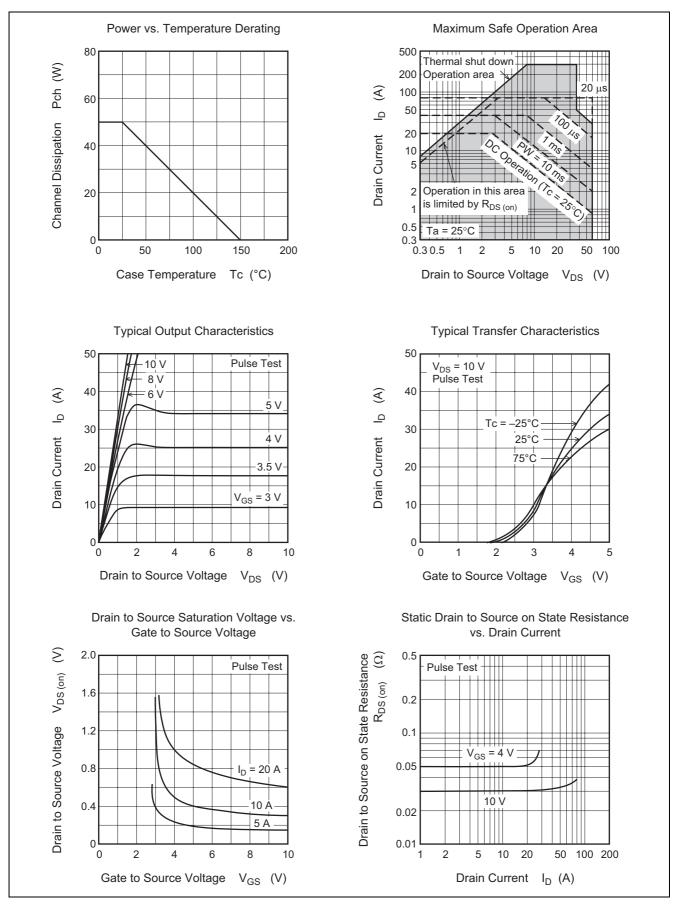
### **Electrical Characteristics**

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain current	I <sub>D1</sub>	10			А	$V_{GS} = 3.5 \text{ V}, V_{DS} = 2 \text{ V}$
	I <sub>D2</sub>	—		10	mA	$V_{GS} = 1.2 \text{ V}, V_{DS} = 2 \text{ V}$
Drain to source breakdown voltage	V (BR) DSS	60			V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V (BR) GSS	16			V	$I_G = 100 \ \mu A, \ V_{DS} = 0$
	V (BR) GSS	-2.8			V	$I_G = -100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS1</sub>	—		100	μΑ	$V_{GS} = 8 V, V_{DS} = 0$
	I <sub>GSS2</sub>	—		50	μΑ	$V_{GS} = 3.5 V, V_{DS} = 0$
	I <sub>GSS3</sub>	—		1	μΑ	$V_{GS} = 1.2 \text{ V}, V_{DS} = 0$
	I <sub>GSS4</sub>	—		-100	μA	$V_{GS} = -2.4 \text{ V}, V_{DS} = 0$
Input current (shut down)	I <sub>GS (op) 1</sub>	—	0.8		mA	$V_{GS} = 8 V, V_{DS} = 0$
	I <sub>GS (op) 2</sub>	—	0.35		mA	$V_{GS} = 3.5 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	—		250	μΑ	$V_{DS} = 50 \text{ V}, \text{ V}_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	1.0		2.25	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	R <sub>DS (on)</sub>	—	50	65	mΩ	$I_D = 10 \text{ A}, V_{GS} = 4 \text{ V}^{Note 3}$
	R <sub>DS (on)</sub>	—	30	43	mΩ	$I_D = 10 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 3}}$
Forward transfer admittance	y <sub>fs</sub>	6	12		S	$I_D = 10 \text{ A}, V_{DS} = 10 \text{ V}^{Note 3}$
Output capacitance	Coss	—	630		pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0$
						f = 1 MHz
Turn-on delay time	t <sub>d (on)</sub>	—	7.5		μs	I <sub>D</sub> = 5 A
Rise time	tr	—	29	_	μs	$V_{GS} = 5 V$
Turn-off delay time	t <sub>d (off)</sub>	—	34		μs	$R_L = 6 \Omega$
Fall time	t <sub>f</sub>	—	26		μs	
Body-drain diode forward voltage	V <sub>DF</sub>	—	1.0		V	$I_F = 20 \text{ A}, V_{GS} = 0$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	110		ns	$I_F = 20 \text{ A}, V_{GS} = 0$
						di <sub>F</sub> /dt = 50 A/µs
Over load shut down operation time Note4	t <sub>os1</sub>	—	1.8	—	ms	$V_{GS}$ = 5 V, $V_{DD}$ = 12 V
	t <sub>os2</sub>	—	0.7		ms	$V_{GS}$ = 5 V, $V_{DD}$ = 24 V

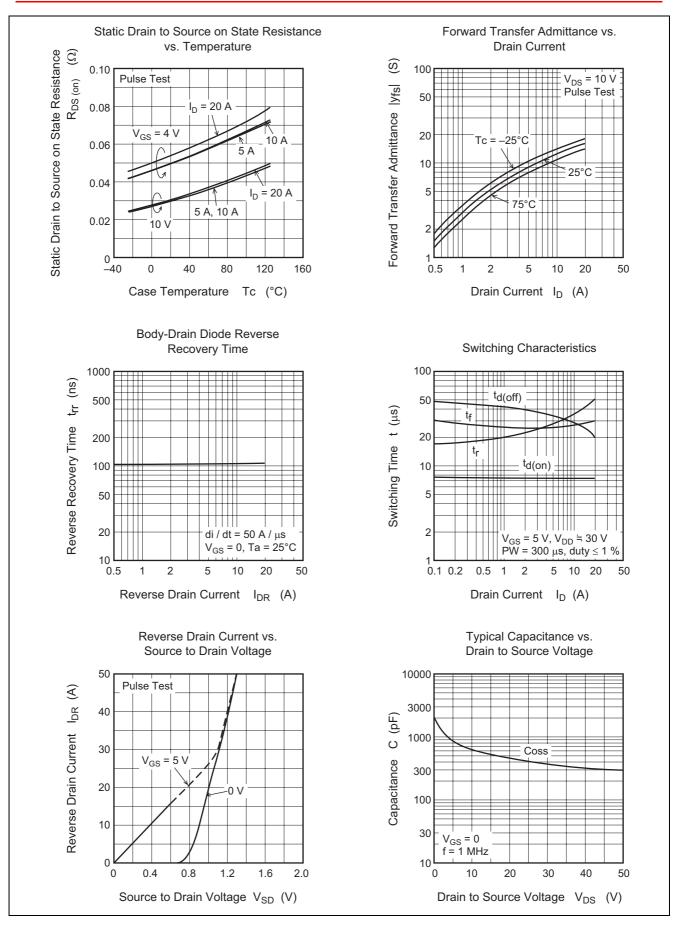
Notes: 3. Pulse test

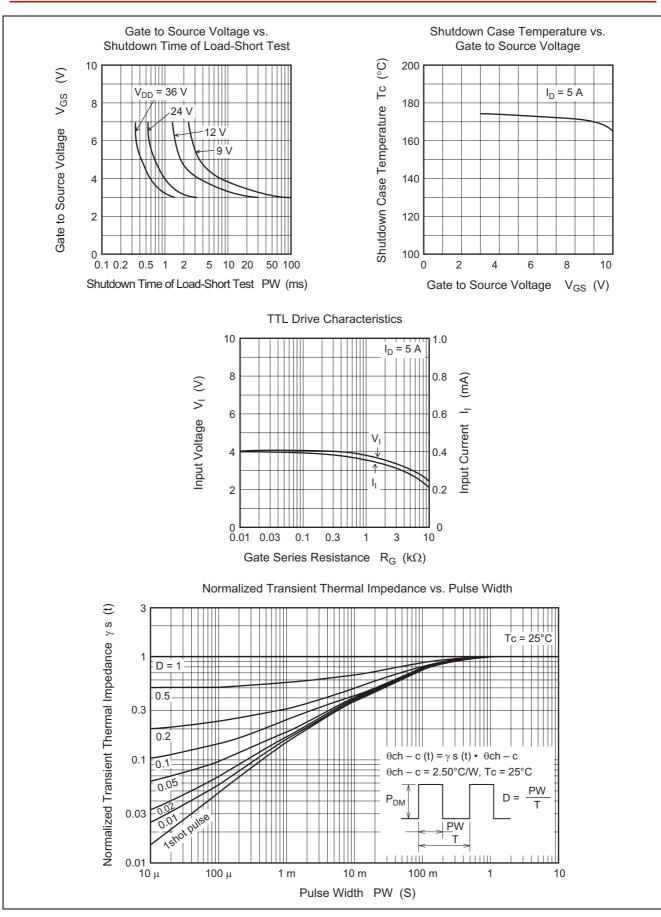
4. Including the junction temperature rise of the over loaded condition.

### **Main Characteristics**

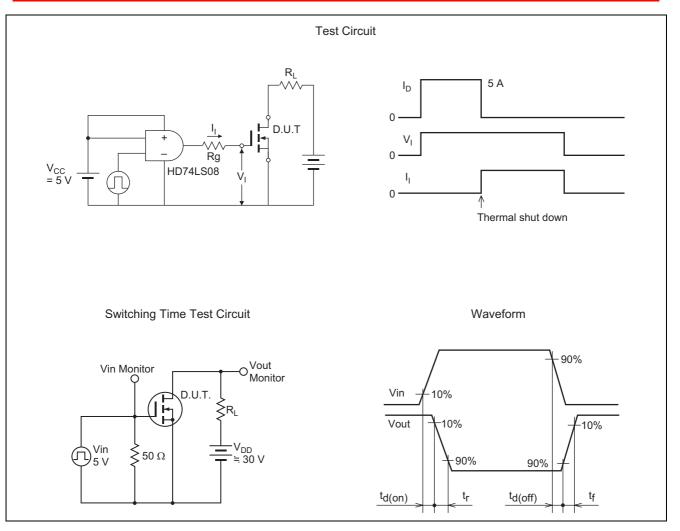


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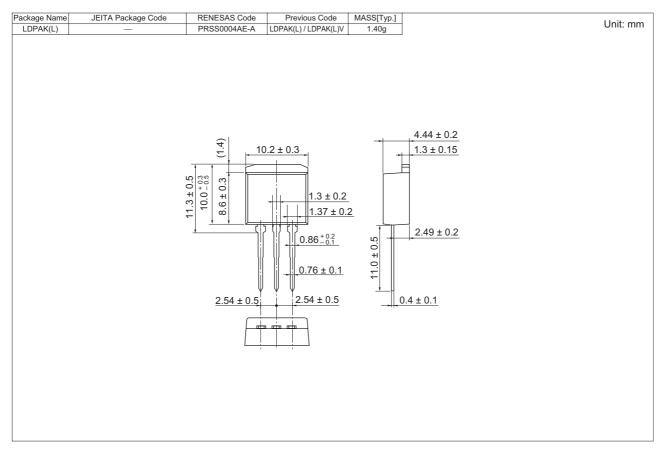


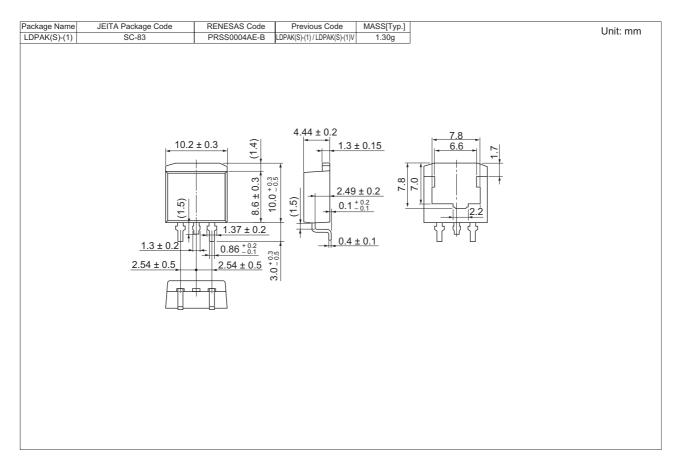


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### **Package Dimensions**





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## **Ordering Information**

Part Name	Quantity	Shipping Container
HAF2012-90L	Max: 50 pcs/sack	Sack
HAF2012-90S	Max: 50 pcs/sack	Sack
HAF2012-90STL	1000 pcs/Reel	Embossed tape
HAF2012-90STR	1000 pcs/Reel	Embossed tape

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