

# GP1A16R

## OPIC Photointerrupter with Encoder Function

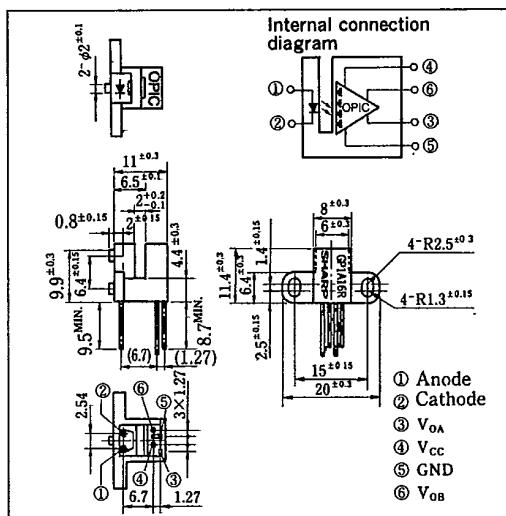
### ■ Features

1. 2-phase (A, B) digital output
2. High sensing accuracy  
(Disk slit pitch: 0.7mm)
3. TTL compatible output
4. Compact

### ■ Applications

1. Electronic typewriters, printers
2. Robots
3. Numerical control machines

### ■ Outline Dimensions (Unit : mm)



\* OPIC is a registered trademark of Sharp and stands for Optical IC. It has a light detecting element and signal processing circuitry integrated onto a single chip.

### ■ Absolute Maximum Ratings

(Ta=25°C)

Parameter		Symbol	Rating	Unit
Input	Forward current	I <sub>F</sub>	50	mA
	*1 Peak forward current	I <sub>FM</sub>	1	A
	Reverse voltage	V <sub>R</sub>	6	V
Output	Power dissipation	P	75	mW
	Supply voltage	V <sub>cc</sub>	7	V
	Low level output current	I <sub>OL</sub>	20	mA
	Power dissipation	P <sub>O</sub>	250	mW
	Operating temperature	T <sub>opr</sub>	0 ~ +70	°C
	Storage temperature	T <sub>stg</sub>	-40 ~ +80	°C
	*2 Soldering temperature	T <sub>sol</sub>	260	°C

\*1 Pulse width ≤ 100 μs, Duty ratio = 0.01

\*2 For 5 seconds

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### ■ Electro-optical Characteristics

(Unless otherwise specified, Ta=0 ~ +70°C)

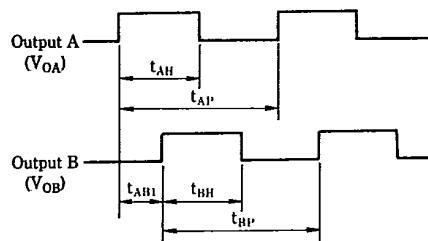
Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input	Forward voltage	V <sub>F</sub>	Ta=25°C, I <sub>F</sub> =20mA	—	1.2	1.4	V
	Reverse current	I <sub>R</sub>	Ta=25°C, V <sub>R</sub> =3V	—	—	10	μA
Output	Operating supply voltage	V <sub>cc</sub>		4.5	5.0	5.5	V
	High level output voltage	V <sub>OH</sub>	V <sub>cc</sub> =5V, I <sub>F</sub> =20mA *3	2.4	4.9	—	V
	Low level output voltage	V <sub>OL</sub>	I <sub>OL</sub> =8mA, V <sub>cc</sub> =5V, I <sub>F</sub> =20mA *3	—	0.1	0.4	V
Transfer characteristics	Supply current	I <sub>cc</sub>	I <sub>F</sub> =20mA, V <sub>cc</sub> =5V *4	—	5	20	mA
	Duty ratio	D <sub>A</sub> *5	V <sub>cc</sub> =5V, I <sub>F</sub> =20mA *3	0.20	0.50	0.80	—
		D <sub>B</sub> *5	f=2.5kHz	0.20	0.50	0.80	—
	Response frequency	f <sub>MAX.</sub>	V <sub>cc</sub> =5V, I <sub>F</sub> =20mA *3	—	—	10	kHz

\*3 Measured under the condition shown in Measurement Conditions

\*5  $D_A = \frac{t_{AH}}{t_{AP}}$ ,  $D_B = \frac{t_{BH}}{t_{BP}}$ 

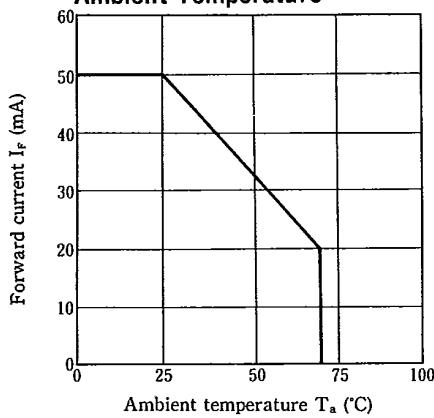
\*4 In the condition that outputs A and B are low level.

### ■ Output Waveforms

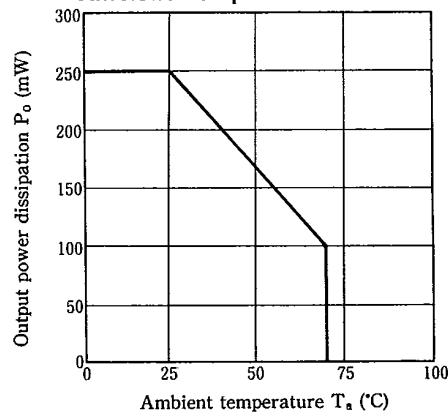


Rotational direction: Counterclockwise when seen from OPIC light detector

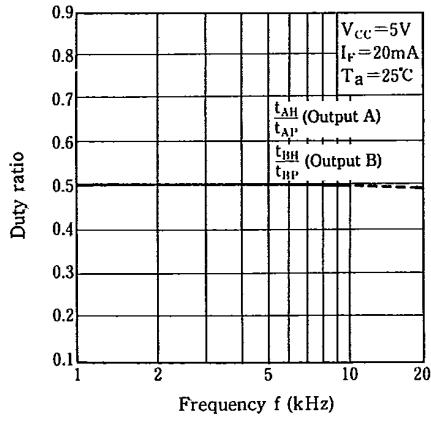
**Fig. 1 Forward Current vs. Ambient Temperature**



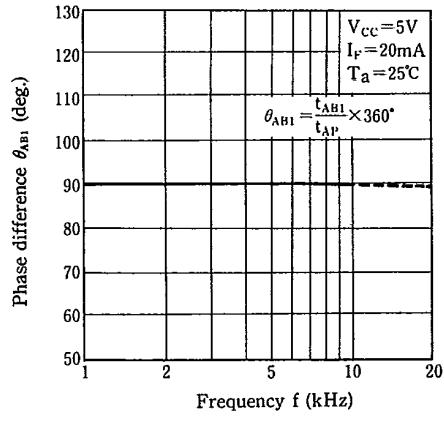
**Fig. 2 Output Power Dissipation vs. Ambient Temperature**



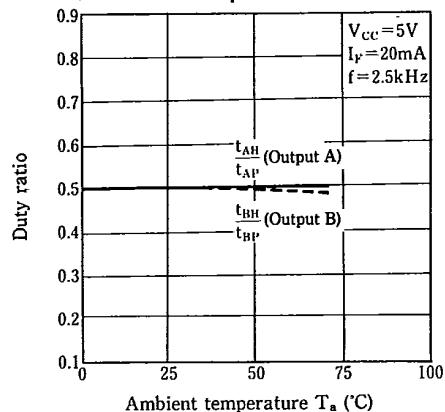
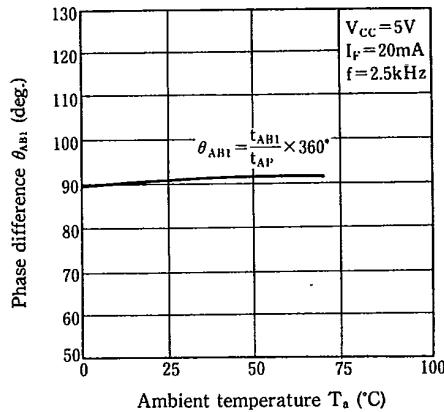
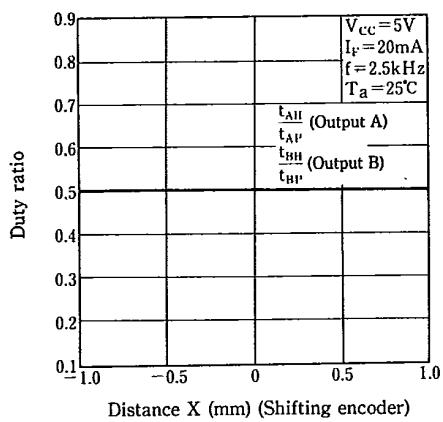
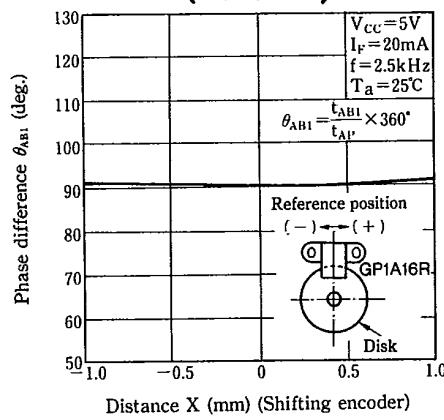
**Fig. 3 Duty Ratio vs. Frequency**



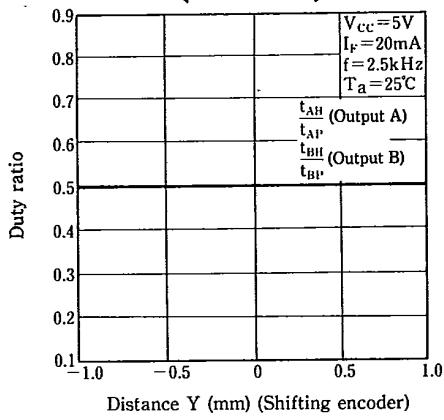
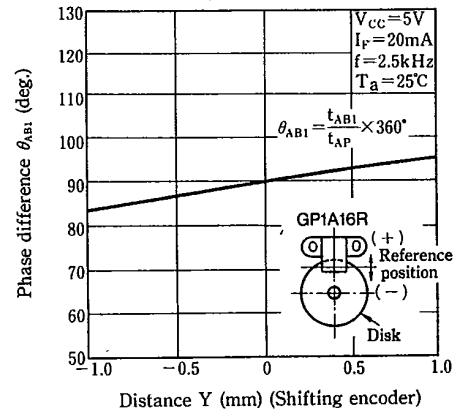
**Fig. 4 Phase Difference vs. Frequency**



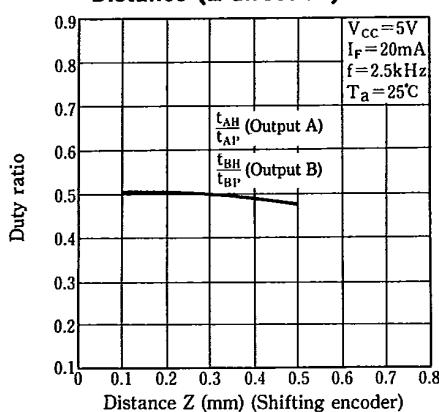
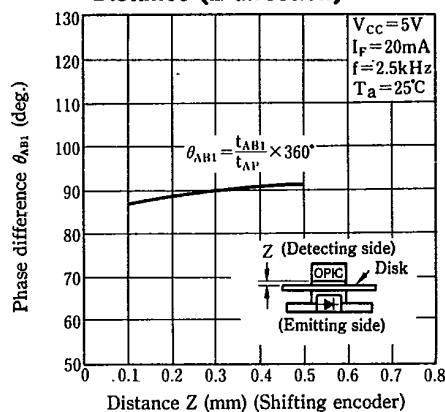
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**Fig. 5 Duty Ratio vs. Ambient Temperature****Fig. 6 Phase Difference vs. Ambient Temperature****Fig. 7 Duty Ratio vs. Distance (X direction)****Fig. 8 Phase Difference vs. Distance (X direction)**

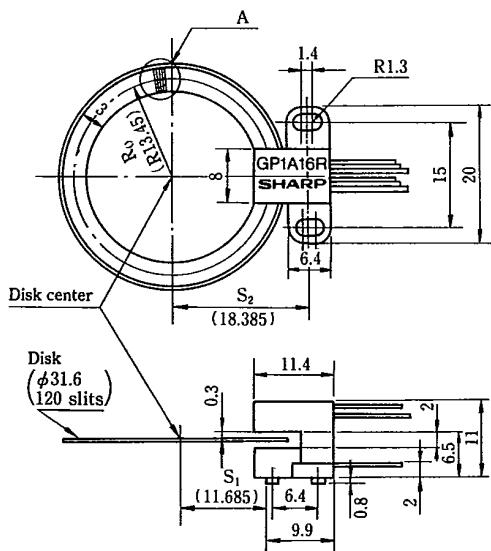
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**Fig. 9 Duty Ratio vs. Distance (Y direction)****Fig. 10 Phase Difference vs. Distance (Y direction)**

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**Fig. 11 Duty Ratio vs.  
Distance (Z direction)****Fig. 12 Phase Difference vs.  
Distance (Z direction)**

## ■ Measurement Conditions



### <Basic Design>

R<sub>0</sub> (distance between the disk center and half point of a slit), P (slit pitch), S<sub>1</sub> and S<sub>2</sub> (installing position of photointerrupter) will be provided by the following equations.

Slit pitch: P (slit center)

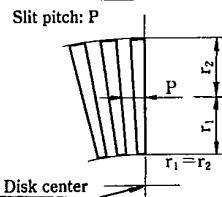
$$R_0 = \frac{N}{120} \times 13.45 \text{ (mm)} \quad N: \text{number of slits}$$

$$P = \frac{2\pi R_0}{N}$$

$$S_1 = R_0 - 1.765 \text{ (mm)} \quad S_2 = S_1 + 6.7 \text{ (mm)}$$

Note) When the number of slits is changed, values in parenthesis are also changed according to the number.

### Enlarged drawing of A portion



(Ex.) In the case of 200P/R

$$R_0 = \frac{200}{120} \times 13.45$$

$$= 22.42 \text{ mm}$$

$$P = \frac{2 \times \pi \times 22.42}{200}$$

$$= 0.704 \text{ mm}$$

$$S_1 = 22.42 - 1.765$$

$$= 20.655 \text{ mm}$$

$$S_2 = 20.655 + 6.7$$

$$= 27.355 \text{ mm}$$

### (Precautions for Use)

Note 1) In order to stabilize power supply line, connect a by-pass capacitor of more than  $0.01\mu\text{F}$  between  $V_{cc}$  and GND near the device.

Note 2) This module is designed to be operated at  $I_F = 20\text{mA TYP.}$