



Ultra320 Multi-mode LVD/SE SCSI Terminator

Features

- Auto-selectable multi-mode Single-Ended or LVD Termination
- Meets SCSI-1, SCSI-2, SCSI-3 SPI Ultra (Fast-20), Ultra 2(SPI-2 LVD), Ultra160 (SPI-3 LVD), and Ultra320 (SPI-4 LVD) standards
- Supports active negation
- 3pF channel capacitance
- 2.9V to 5.5V operation
- Thermal protection
- Hot-Swap compatible
- 5% tolerance on SE and LVD termination resistance
- SE/LVD mode indicators
- 28 pin TSSOP package

Applications

- Server and workstation
- High-end PC
- Industrial PC
- Disk Array (RAID)
- SCSI cable

General Description

The G220 Multi-mode LVD/SE SCSI terminator provides a smooth transition into the next generation of the SCSI Parallel Interface (SPI-4). It automatically senses the bus, via DIFFSENS, and switches the termination to either single-ended (SE) or low voltage differential (LVD) SCSI, dependent on which type of devices are connected to the bus. If the G220 detects a HVD SCSI device, it switches to a high impedance state. For a 16-bit Wide SCSI bus to be operational, three G220 terminators are needed.

The Multi-mode terminator contains all functions required to terminate and auto detect and switch modes for SCSI Parallel Interface (SPI) bus architectures. For the SE termination, one regulator and nine precision 110ohm resistors are used. For the LVD termination, the G220 integrates 18 regulated supplies with nine precision resistor strings.

Ordering Information

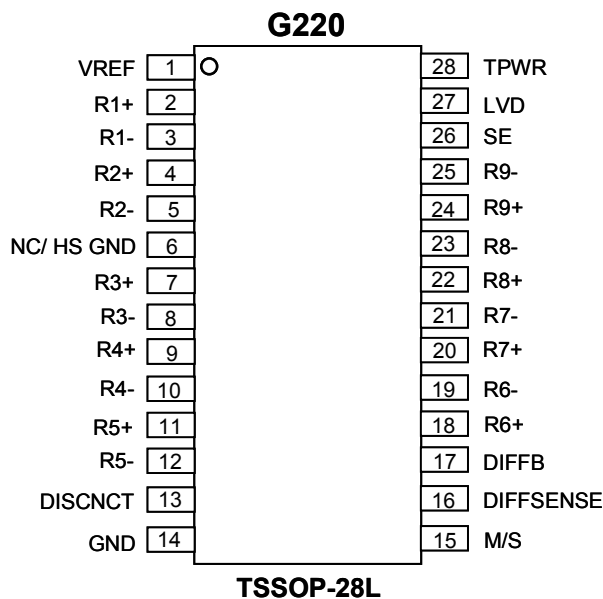
ORDER NUMBER	MARKING	TEMP. RANGE	PACKAGE
G220D3X	G220	0°C to +70°C	TSSOP-28L

Note: X Specify the packing type

U: Tape & Reel

T: Tube

Pin Configuration



Description

The G220 is used in multi-mode active termination applications, where single ended (SE) and low voltage differential (LVD) devices might coexist. The LVD termination section consists of 18 source/sink amplifiers (VTOP, VBOT), biasing circuitry and nine precision resistor strings (RTOP, RMID, RBOT). The SE termination section consists of a 2.85V source/sink regulator with 9 precision 110ohm resistor. The DIFFSENSE section consists of a 1.3V, 5mA driver and a sensing circuit (Figure 1)

DIFFSENS is used to identify which types of SCSI devices are present on the bus. If the voltage on DIFFSENS is between 0-0.5V the bus is single-ended; if it is between 0.7-1.9V the bus is LVD; and if it is greater than 2.4V, the bus is HVD.

The G220 DIFFB pin monitors the DIFFSENS line to determine the proper operating mode of the device.

HVD Isolation Mode The G220 identifies that there is an HVD (high voltage differential) device on the SCSI bus and isolates the termination pins from the bus.

When DISCNCT is pulled high, the termination pins are isolated from the SCSI bus, and VREF is grounded. During thermal shutdown, the termination pins are isolated from the SCSI bus and VREF is grounded. The DIFFSENSE driver is shut down during either of these two events.

To ensure proper operation, the TPWR pin should be connected to the SCSI bus TERMPWR line. As with all analog circuitry, the TERMPWR lines should be bypassed locally. A 2.2 μ F capacitor and a 0.01 μ F high frequency capacitor is recommended between TPWR and ground and placed as close as possible to the G220. The G220 should be placed as close as possible to the SCSI connector to minimize signal and

power trace length, thereby resulting in less input capacitance and reflections which can degrade the bus signals.

The DIFFSENSE pin can drive the SCSI DIFFSENS line (when M/S is pulled high) to determine the SCSI bus operating mode. The G220 switches to the termination mode that is appropriate for the bus based on the value of the DIFFSENS voltage. These modes are:

LVD mode LVD termination is provided by a precision resistor string with two amplifiers. This configuration yields a 105 Ω differential and 150 Ω common mode impedance. A fail safe bias of 112mV is maintained when no drivers are connected to the SCSI bus.

SE mode When the external driver for a given signal line turns off, the active terminator will pull that signal line to 2.85 volts (quiescent state). When used with an active negation driver, the power amp can sink 22mA per line while keeping the voltage reference in regulation. The terminating resistors maintain their 110 Ω value.

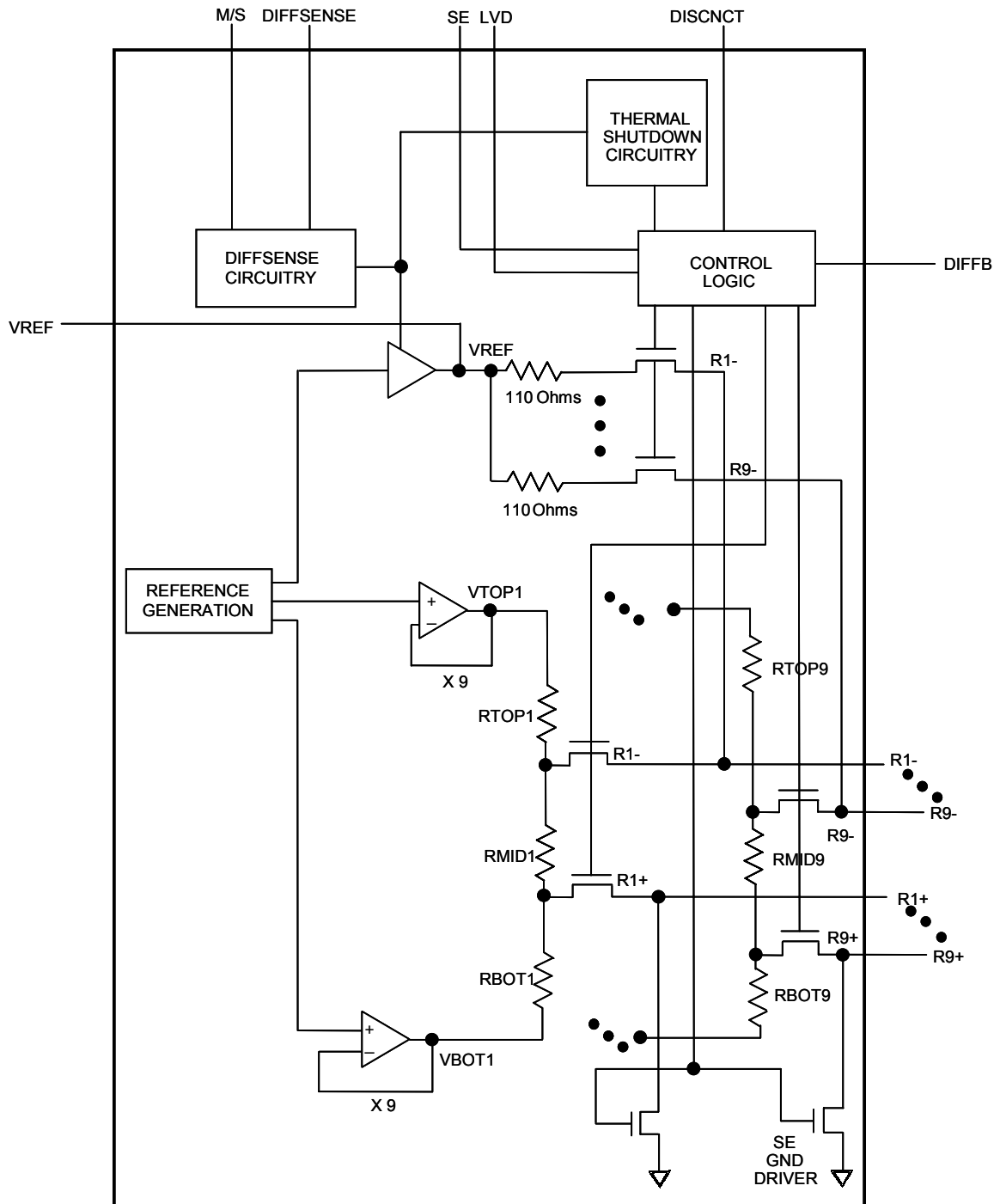
To maintain the specified regulation, a 4.7 μ F capacitor is required between the VREF pin and ground of each G220. A high frequency cap (0.1 μ F ceramic recommended) can also be placed on the VREF pin in applications that use fast rise/fall time drivers. A typical SCSI bus configuration is shown in Figure2.

An internal pull down resistor assures that the G220 will be terminating the bus if the DISCNCT pin is left floating.

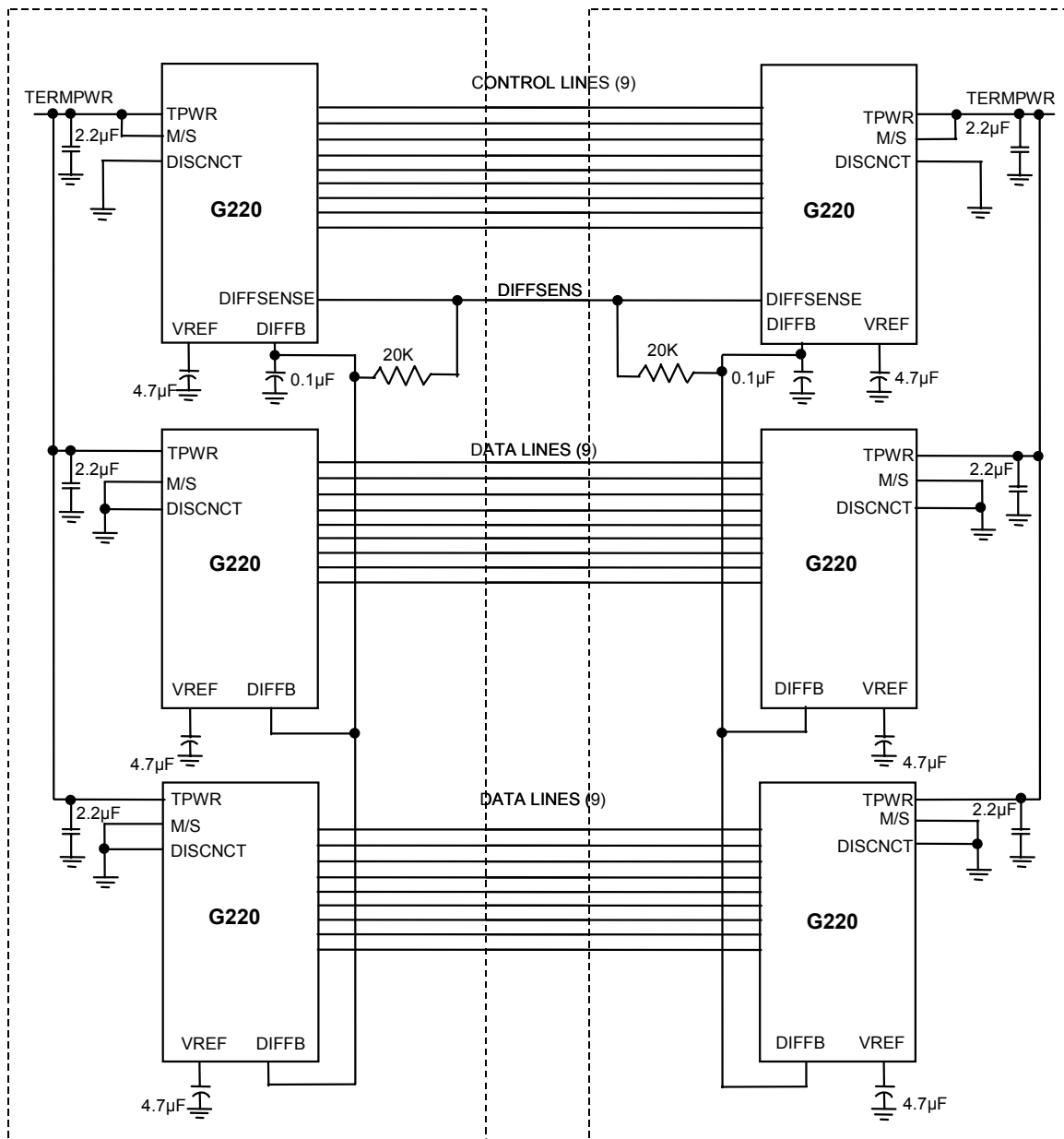
DIFFSENS noise filtering

The G220 incorporates a internal digital filter to remove the noise signal on the DIFFSENS control line, thereby eliminating erroneous switching between modes. The external filter may be used in addition to the internal digital filter.

Block Diagram (Figure 1)



SCSI Bus Configuration (Figure 2)





Recommended Operating Conditions

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS
Tempower Voltage	$V_{tpwr}(SE)$	4.0	5	5.5	V
	$V_{tpwr}(LVD)$	2.9	5	5.5	
Logic 0	V_{il}	-0.3		+0.8	V
Logic 1	V_{ih}	2.0		$V_{tpwr}+0.3$	V
Operating Temperature	T_{amb}	0		70	°C

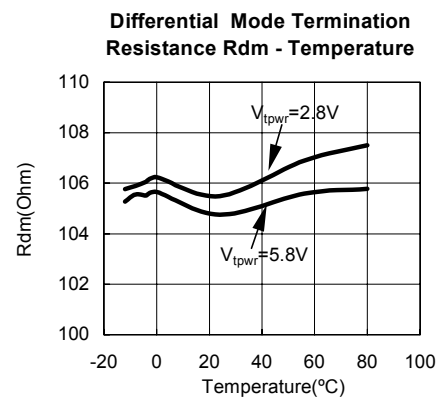
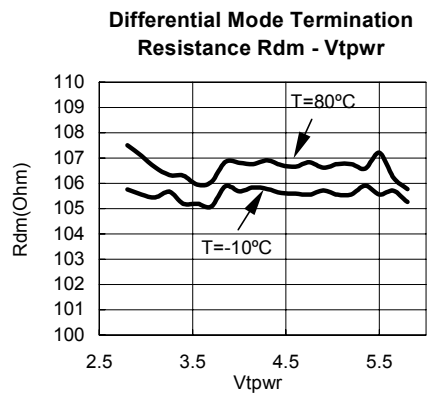
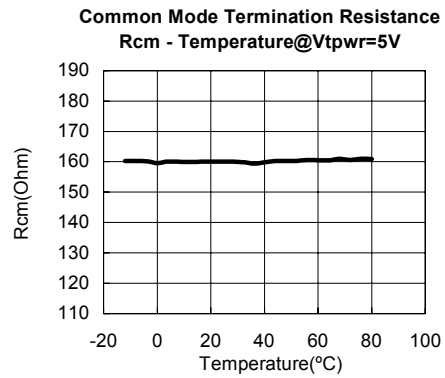
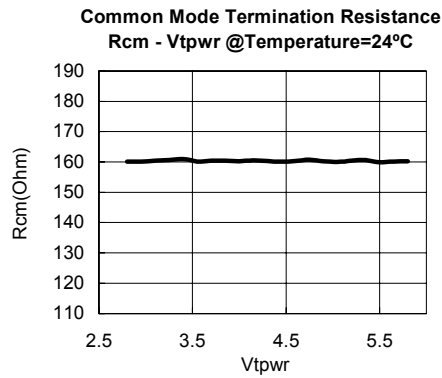
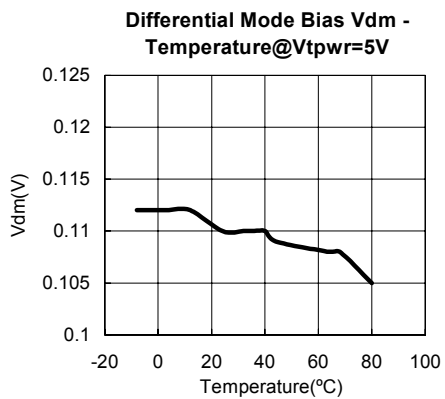
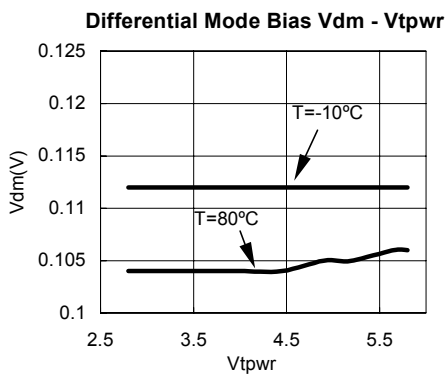
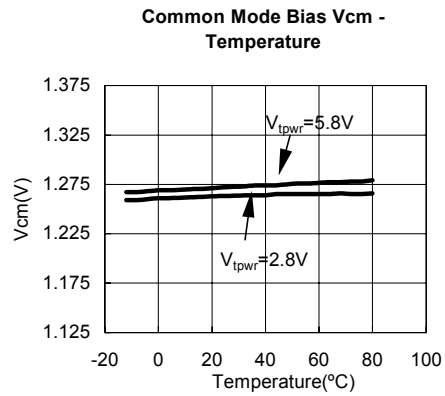
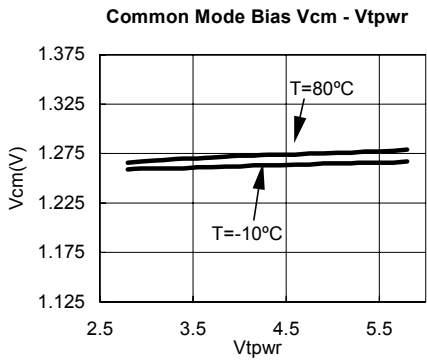
Electrical Characteristics

Unless otherwise specified, these specifications apply over the operating ambient temperature range of $0^{\circ}\text{C} \leq T_A \leq 70^{\circ}\text{C}$.

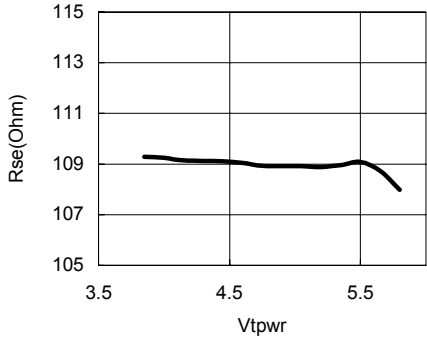
PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNITS
LVD Terminator Section						
Differential Mode Termination Resistance	Rdm		100	105	110	Ohms
Common Mode Termination Resistance	Rcm		110	150	190	Ohms
Differential Mode Bias	Vdm	All lines Open	100	112	125	mV
Common Mode Bias	Vcm		1.125	1.25	1.375	V
Single Ended Terminator Section						
Output Capacitance	Cout	Note			3	pF
SE Termination Resistance	Rse	Vline = 0-3.0 volts	104.5	110	115.5	Ohms
SE Voltage Reference	Vref		2.79	2.85	2.93	Volts
SE output Current	Iose	Vline = 0.2 volts			25.4	mA
Regulator Section						
Line Regulation	L _{REG}			1.0	2.5	%
Load Regulation	L _{OREG}			1.3	3.5	%
Current Limit	I _{LIM}			350		mA
Sink Current	I _{SINK}		200			mA
DC Section						
Tempower Current	I _{tpmr}	SE mode (No Load)		4		mA
		LVD mode (No Load)		20		
Input Leakage High	I _{ih}		-1.0			μA
Input Leakage Low	I _{il}				1.0	μA
Output Current High	I _{oh}	V _{out} = 2.4 volts; SE/LVD Pins only	-1.0			mA
Output Current Low	I _{ol}	V _{out} = 0.4 volts; SE/LVD Pins only	4.0			mA
DIFFSENS Section						
DIFFSENS SE Operating Range	V _{seor}		-0.3		0.5	V
DIFFSENS LVD Operating Range	V _{lvdor}		0.7		1.9	V
DIFFSENS HVD Operating Range	V _{hvdor}		2.4		$V_{tpwr}+0.3$	V
DIFFSENSE Driver Output Voltage	V _{dso}	M/S=1; I _{ds} = 0-5mA	1.2		1.4	V
DIFFSENSE Driver Source Current	I _{dsh}	M/S = 1; V _{dso} =0V		5	15	mA
DIFFSENSE Driver Sink Current	I _{dsl}	M/S=1; V _{dso} =2.75V	20		200	μA

Note: Guaranteed by Design.

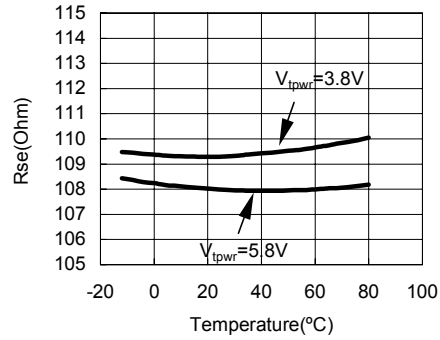
Typical Operating Characteristics



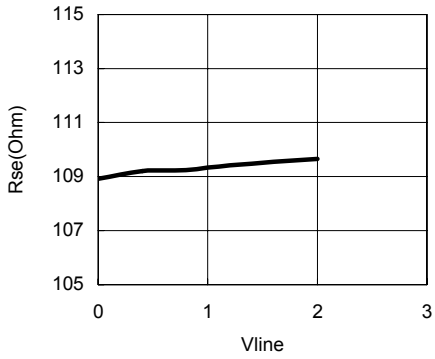
SE Termination Resistance Rse -
Vtpwr @T=24°C;Vline=0V



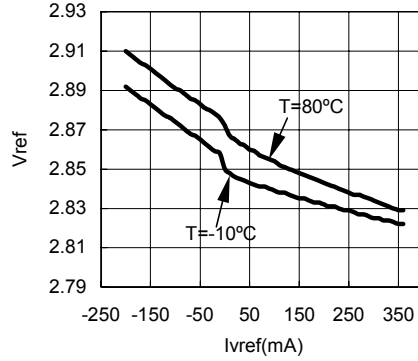
SE Termination Resistance Rse -
Vtpwr @Vline=0V



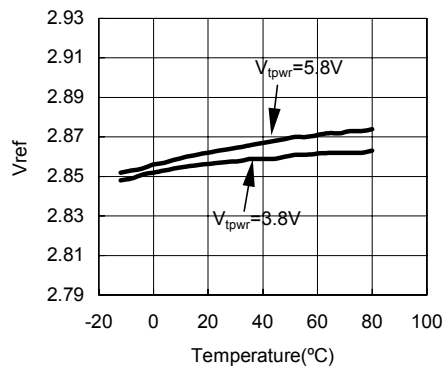
SE Termination Resistance Rse -
Vtpwr @Vtpwr=5V;T=24°C



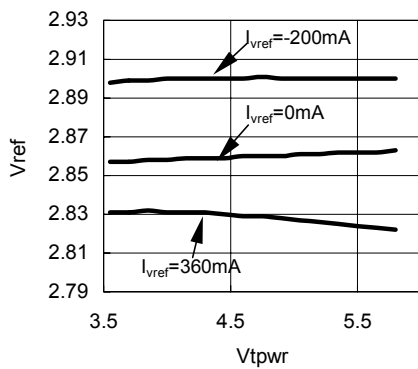
Vref - Ivref@Vtpwr=5V



Vref - Temperature@Ivref=0mA



Vref - Vtpwr@T=24°C



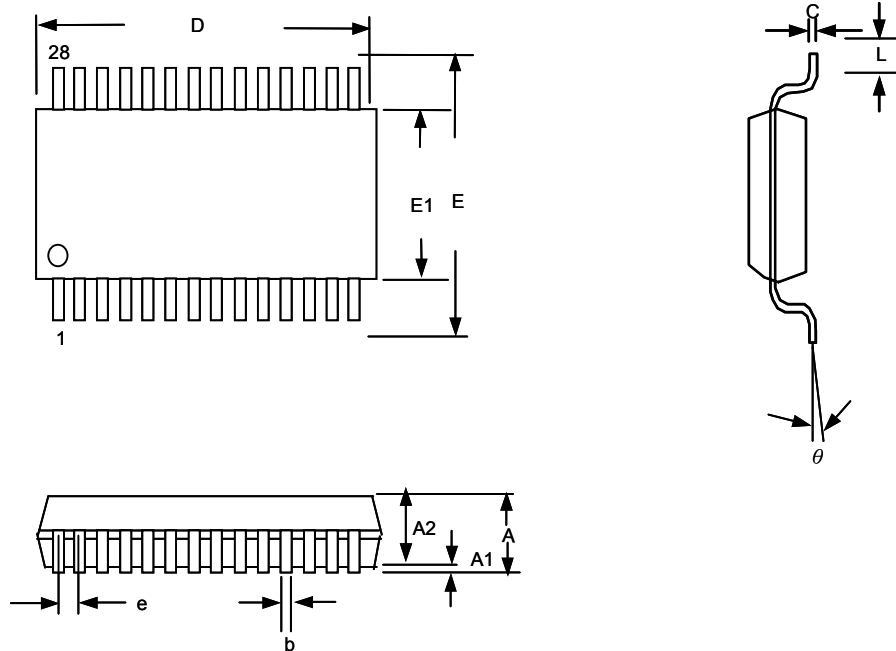


Pin Description

PIN	SYMBOL	DESCRIPTION
1	VREF	Reference Voltage. 2.85-volt reference in SE mode and 1.25V in LVD mode; must be connected to a 4.7 μ F capacitor.
2-5,7-12, 18-25	Rx+, Rx-	Signal Termination. (X=1,2,3,.....9) Rx+: Ground line for single-ended or positive line for differential applications for the SCSI bus. Rx-: Signal line/active line for single-ended or negative line in differential applications for the SCSI bus.
6	NC/ HS GND	No Connect/Heat Sink Ground. Should be grounded for heat sinking purpose
13	DISCNCT	Disconnect. When pulled high, the G220 isolates its bus pins (Rx+, Rx-) from the SCSI bus.
14	GND	Ground. Terminator ground pin. Connected to ground.
15	M/S	Master/slave. Used to select which terminator is the controlling device. M/S pin high enables the DIFFSENSE driver.
16	DIFFSENSE	Output to drive the SCSI bus DIFFSENS line.
17	DIFFB	DIFFB pin should be connected to a 0.1 μ F capacitor to GND and 20k resistor to SCSI/Bus DIFFSENS line. An input pin to detect the type of device (differential or single-ended) on the SCSI bus.
26	SE	Single ended mode indicator. It is HIGH when terminator is operating in SE mode.
27	LVD	Low Voltage Differential mode indicator. It is HIGH when terminator is operating in LVD mode.
28	TPWR	Terminator Power. Connect to the SCSI TERMPWR line and de-couple with a 2.2 μ F capacitor.

*The VREF keeps the level at 2.85 volt, despite the DISCNCT pin is asserted or not; even the termination mode is changed.

Package Information

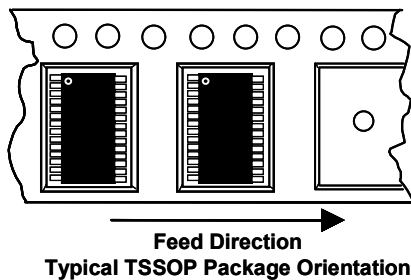


Note:

1. Package body sizes exclude mold flash protrusions or gate burrs
2. Tolerance $\pm 0.1\text{mm}$ unless otherwise specified
3. Coplanarity : 0.1mm
4. Controlling dimension is in millimeter. Converted inch dimensions are not necessarily accurate.
5. Follow JEDEC MO-153

SYMBOL	DIMENSION IN MM			DIMENSION IN INCH		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	----	----	1.20	----	----	0.048
A1	0.05	----	0.15	0.002	----	0.006
A2	0.80	1.00	1.05	0.031	0.039	0.041
b	0.19	----	0.30	0.007	----	0.012
C	0.09	----	0.20	0.004	----	0.008
D	9.60	9.70	9.80	0.378	0.382	0.386
E	----	6.40	----	----	0.252	----
E1	4.30	4.40	4.50	0.169	0.173	0.177
e	----	0.65	----	----	0.026	----
L	0.45	0.60	0.75	0.018	0.024	0.030
y	----	----	0.10	----	----	0.004
θ	0°	----	8°	0°	----	8°

Taping Specification



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