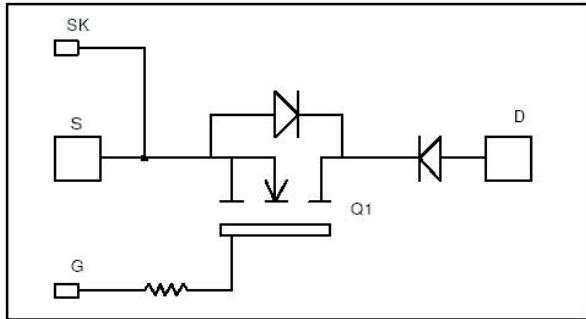


*Single switch
with Series diode
MOSFET Power Module*

V_{DSS} = 1000V
R_{DSon} = 65mΩ max @ T_j = 25°C
I_D = 145A @ T_c = 25°C



Application

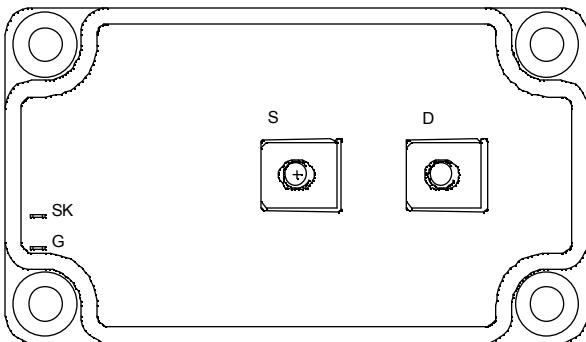
- Zero Current Switching resonant mode

Features

- Power MOS 7® MOSFETs
 - Low R_{DSon}
 - Low input and Miller capacitance
 - Low gate charge
 - Avalanche energy rated
 - Very rugged
- Kelvin source for easy drive
- Very low stray inductance
 - Symmetrical design
 - M5 power connectors
- High level of integration
- AlN substrate for improved thermal performance

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Low profile



Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
V _{DSS}	Drain - Source Breakdown Voltage	1000	V
I _D	Continuous Drain Current	T _c = 25°C	A
		T _c = 80°C	
I _{DM}	Pulsed Drain current	580	
V _{GS}	Gate - Source Voltage	±30	V
R _{DSon}	Drain - Source ON Resistance	65	mΩ
P _D	Maximum Power Dissipation	T _c = 25°C	W
I _{AR}	Avalanche current (repetitive and non repetitive)	30	A
E _{AR}	Repetitive Avalanche Energy	50	mJ
E _{AS}	Single Pulse Avalanche Energy	3200	

 **CAUTION:** These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

All ratings @ $T_j = 25^\circ\text{C}$ unless otherwise specified

Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
BV_{DSS}	Drain - Source Breakdown Voltage	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = 1\text{mA}$		1000			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 1000\text{V}$	$T_j = 25^\circ\text{C}$			400	μA
		$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 800\text{V}$	$T_j = 125^\circ\text{C}$			2	mA
$R_{\text{DS(on)}}$	Drain – Source on Resistance	$V_{\text{GS}} = 10\text{V}, I_{\text{D}} = 75\text{A}$				65	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}} = V_{\text{DS}}, I_{\text{D}} = 20\text{mA}$		3		5	V
I_{GSS}	Gate – Source Leakage Current	$V_{\text{GS}} = \pm 30\text{ V}, V_{\text{DS}} = 0\text{V}$				± 400	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
C_{iss}	Input Capacitance	$V_{\text{GS}} = 0\text{V}$ $V_{\text{DS}} = 25\text{V}$ $f = 1\text{MHz}$		28.5			nF
C_{oss}	Output Capacitance			5.08			
C_{rss}	Reverse Transfer Capacitance			0.9			
Q_g	Total gate Charge	$V_{\text{GS}} = 10\text{V}$ $V_{\text{Bus}} = 500\text{V}$ $I_{\text{D}} = 145\text{A}$		1068			nC
Q_{gs}	Gate – Source Charge			136			
Q_{gd}	Gate – Drain Charge			692			
$T_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{GS}} = 15\text{V}$ $V_{\text{Bus}} = 500\text{V}$ $I_{\text{D}} = 145\text{A}$ $R_G = 0.75\Omega$		18			ns
T_r	Rise Time			14			
$T_{\text{d(off)}}$	Turn-off Delay Time			140			
T_f	Fall Time			55			
E_{on}	Turn-on Switching Energy ①	Inductive switching @ 25°C $V_{\text{GS}} = 15\text{V}, V_{\text{Bus}} = 670\text{V}$ $I_{\text{D}} = 145\text{A}, R_G = 0.75\Omega$		4.8			mJ
E_{off}	Turn-off Switching Energy ②			2.9			
E_{on}	Turn-on Switching Energy ①			8			mJ
E_{off}	Turn-off Switching Energy ②	Inductive switching @ 125°C $V_{\text{GS}} = 15\text{V}, V_{\text{Bus}} = 670\text{V}$ $I_{\text{D}} = 145\text{A}, R_G = 0.75\Omega$		3.9			

① E_{on} includes diode reverse recovery.

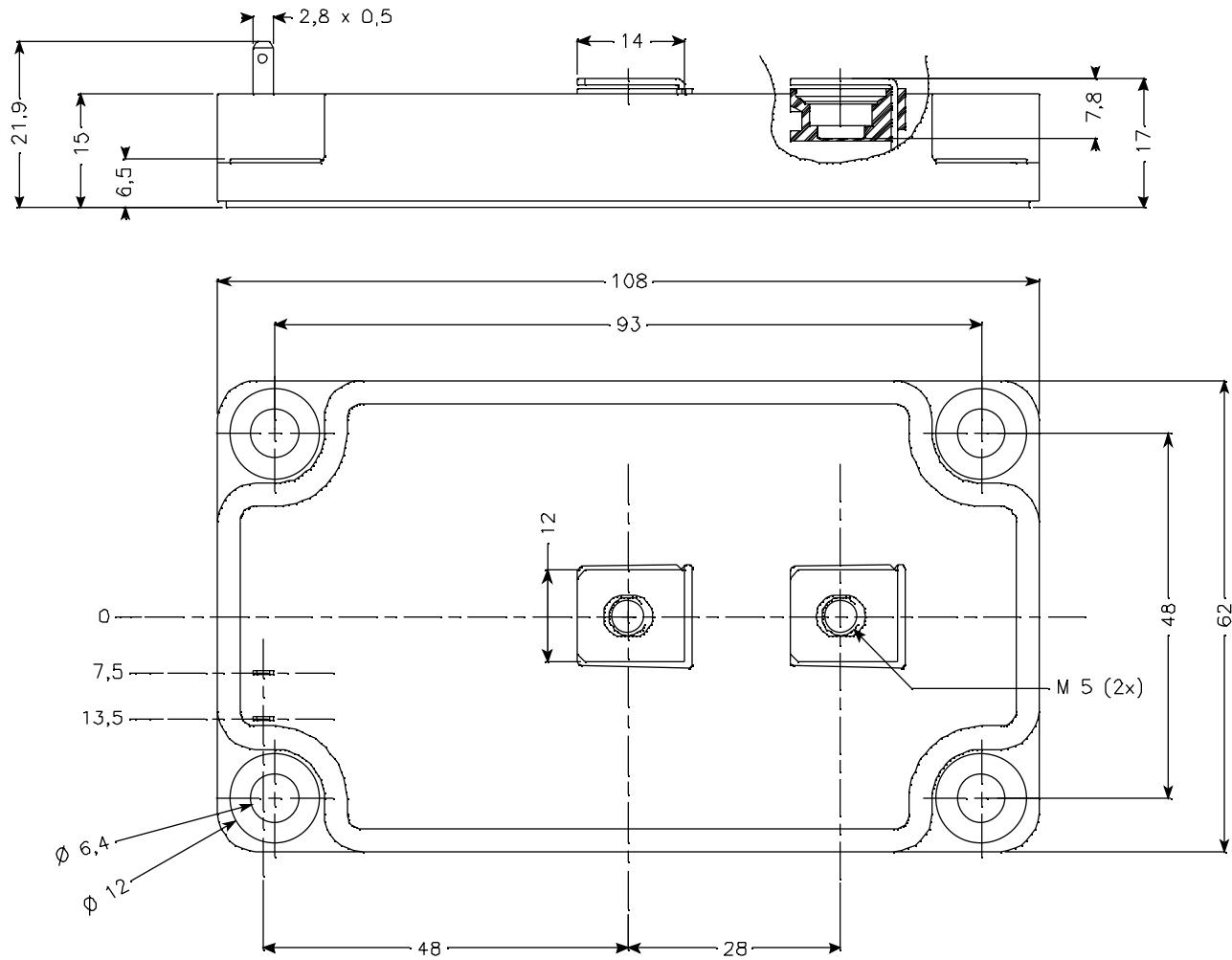
② In accordance with JEDEC standard JESD24-1.

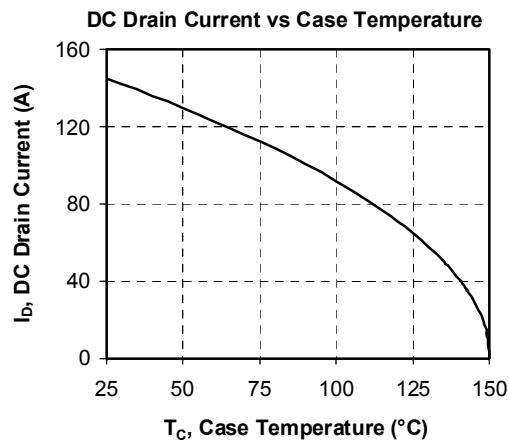
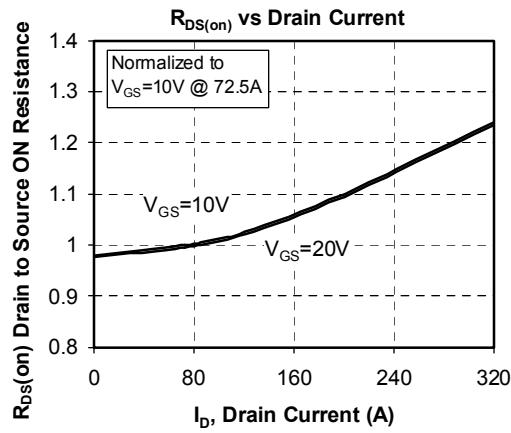
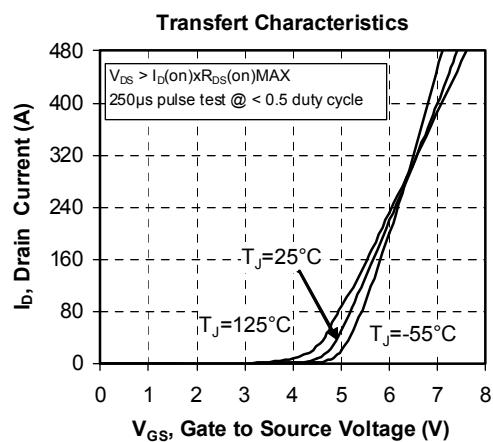
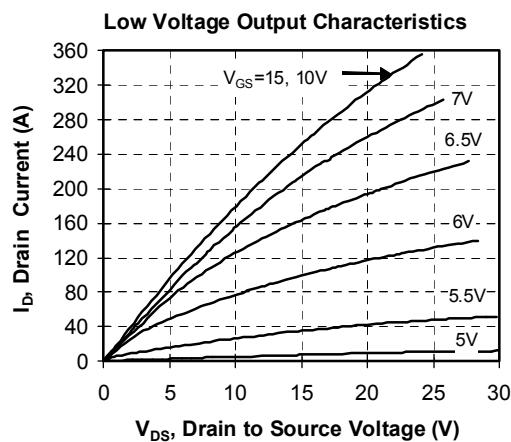
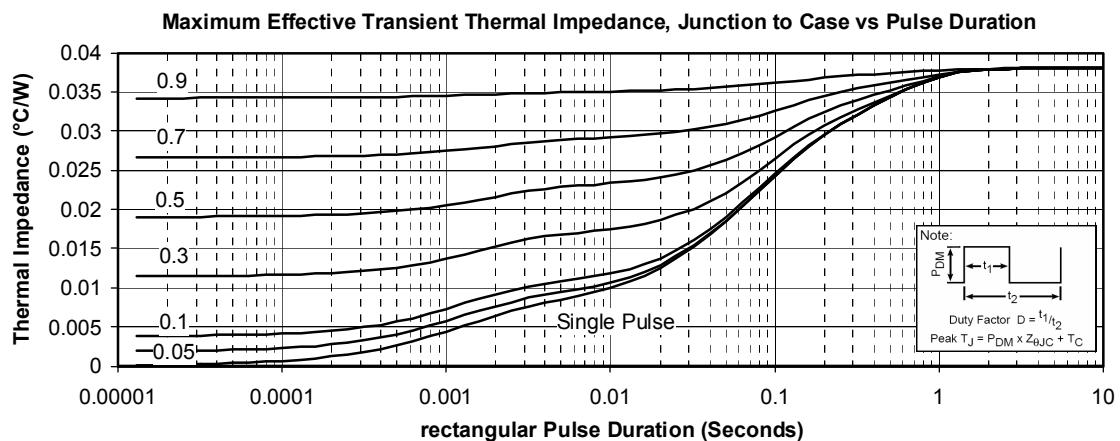
Series diode ratings and characteristics

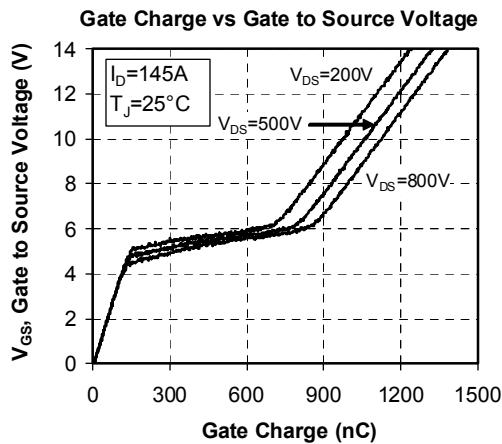
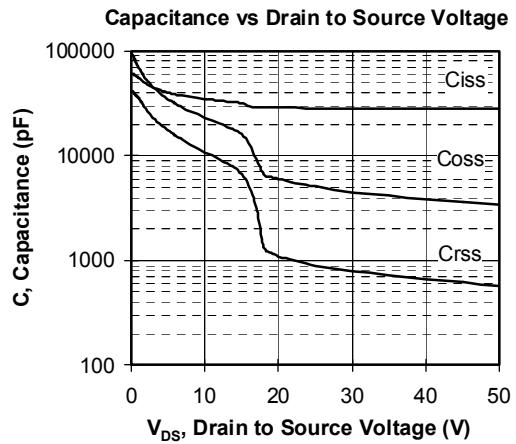
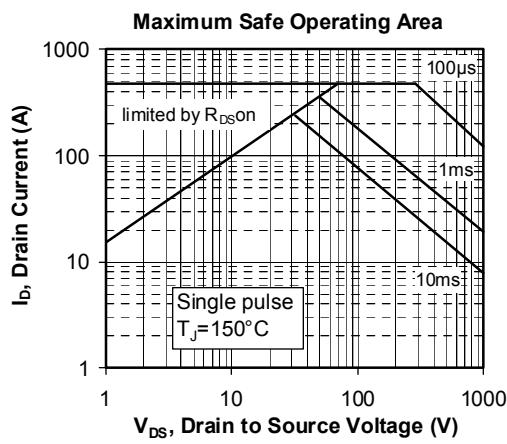
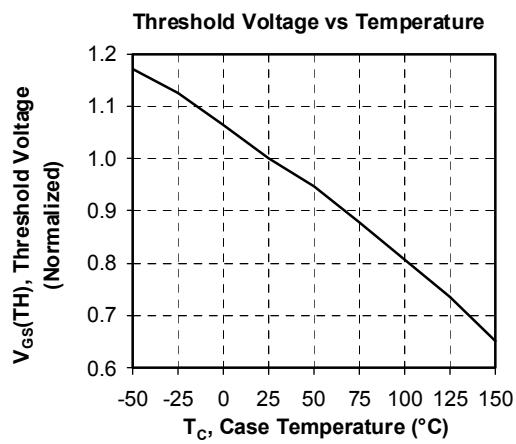
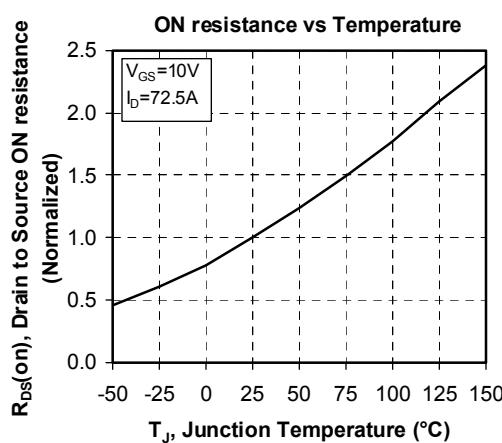
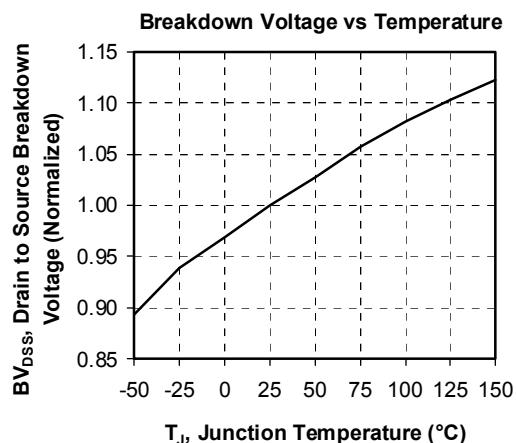
Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
V_{RRM}	Maximum Repetitive Reverse Voltage			1000			V
I_{RM}	Maximum Reverse Leakage Current	$V_R = 1000\text{V}$	$T_j = 125^\circ\text{C}$			2	mA
$I_{\text{F(AV)}}$	Maximum Average Forward Current	50% duty cycle		240			A
V_F	Diode Forward Voltage	$I_F = 240\text{A}$			1.9	2.5	V
		$I_F = 480\text{A}$			2.2		
		$I_F = 240\text{A}$	$T_j = 125^\circ\text{C}$		1.7		
t_{rr}	Reverse Recovery Time	$I_F = 240\text{A}$	$T_j = 25^\circ\text{C}$		280		ns
		$V_R = 667\text{V}$	$di/dt = 800\text{A}/\mu\text{s}$		350		
Q_{rr}	Reverse Recovery Charge	$I_F = 240\text{A}$	$T_j = 25^\circ\text{C}$		3		μC
		$V_R = 667\text{V}$	$di/dt = 800\text{A}/\mu\text{s}$		14.4		

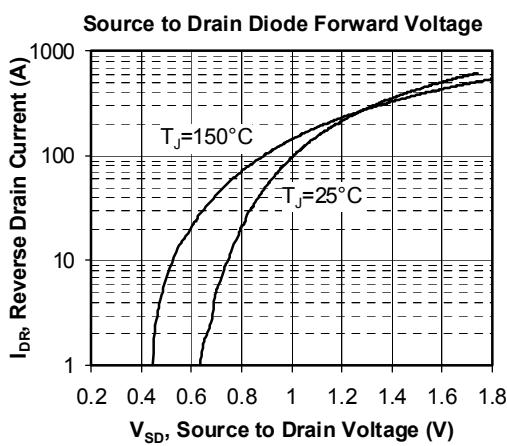
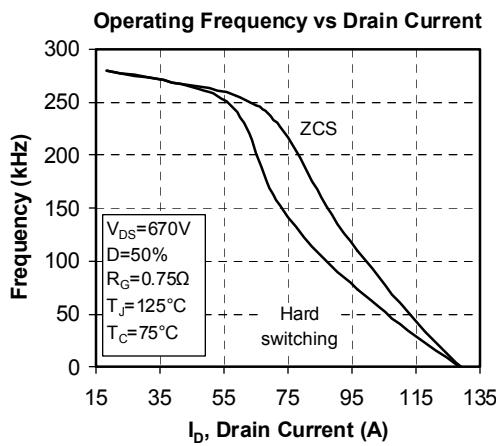
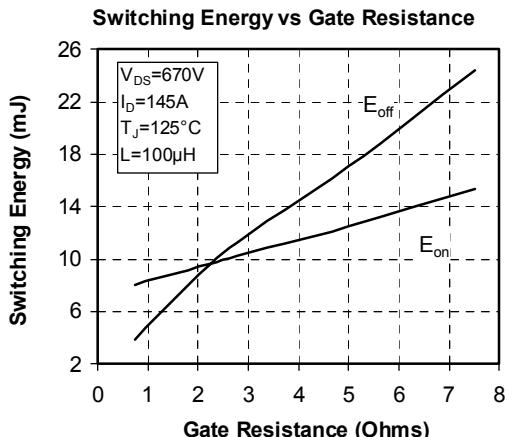
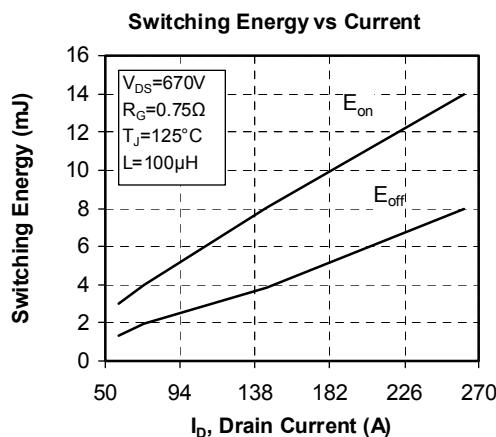
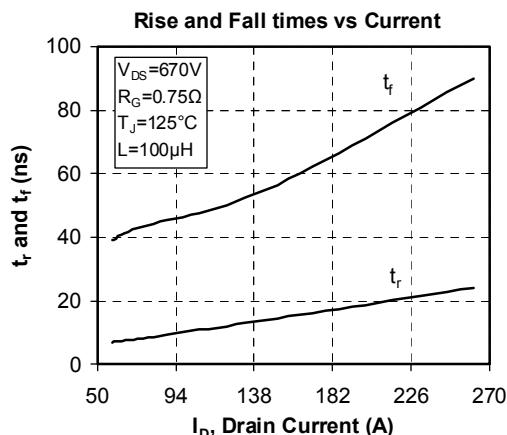
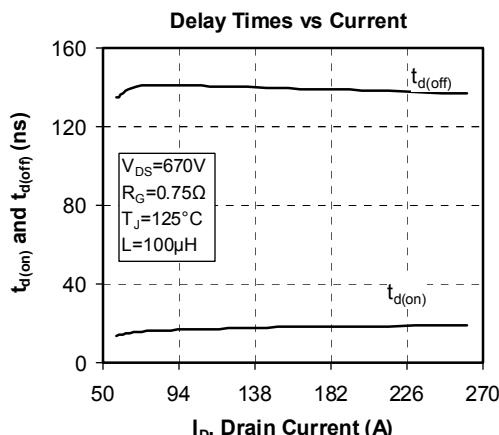
Thermal and package characteristics
Symbol **Characteristic**

			Min	Typ	Max	Unit
R_{thJC}	Junction to Case	Transistor			0.038	°C/W
		Series diode			0.23	
V_{ISOL}	RMS Isolation Voltage, any terminal to case t=1 min, I isol<1mA, 50/60Hz		2500			V
T_J	Operating junction temperature range		-40		150	°C
T_{STG}	Storage Temperature Range		-40		125	
T_C	Operating Case Temperature		-40		100	
Torque	Mounting torque	To Heatsink	M6	3	5	N.m
		For terminals	M5	2	3.5	
Wt	Package Weight				280	g

Package outline


Typical Performance Curve






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APT's products are covered by one or more of U.S patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. U.S and Foreign patents pending. All Rights Reserved.