

P-Channel Enhancement Mode Power MOSFET

■ Features

- Lower On-Resistance
- Simple Drive Requirement
- Fast Switching Characteristic
- RoHS Compliant
- Pb Free Plating Product

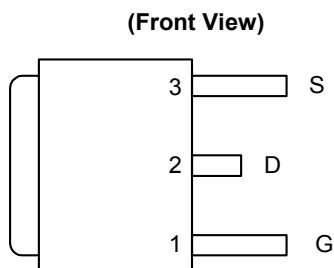
■ General Description

The TO-252 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

■ Product Summary

BV_{DSS} (V)	$R_{DS(ON)}$ (m Ω)	I_D (A)
-30	28	-30

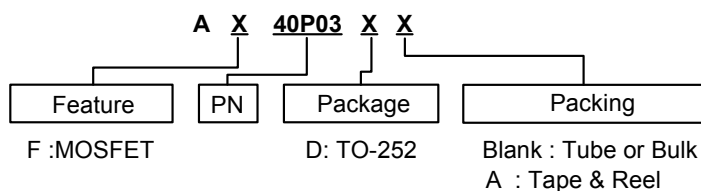
■ Pin Assignments



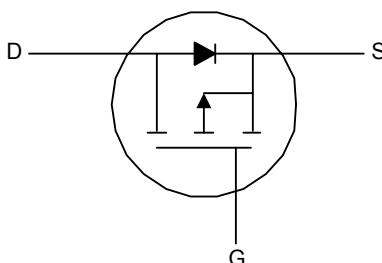
■ Pin Descriptions

Pin Name	Description
S	Source
G	Gate
D	Drain

■ Ordering information



■ Block Diagram



**AF40P03****P-Channel Enhancement Mode Power MOSFET****■ Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current, $V_{GS}=10V$	$T_C=25^\circ C$	A
		$T_C=100^\circ C$	
I_{DM}	Pulsed Drain Current (Note 1)	-120	A
P_D	Power Dissipation	$T_C=25^\circ C$	W
	Linear Derating Factor	0.25	W/ $^\circ C$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

■ Thermal Data

Symbol	Parameter	Maximum	Units
$R_{\theta JC}$	Thermal Resistance Junction-Case	Max.	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance Junction- Ambient	Max.	$^\circ C/W$

■ Electrical Characteristics ($T_J=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Limits			Unit
			Min.	Typ.	Max.	
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-30	-	-	V
$\Delta BV_{DSS}/\Delta T_J$	Breakdown Voltage Temperature Coefficient	Reference to $25^\circ C$, $I_D=-1mA$	-	-0.02	-	V/ $^\circ C$
$R_{DS(ON)}$	Static Drain-Source On-Resistance (Note 2)	$V_{GS}=-10V, I_D=-18A$	-	-	28	m Ω
		$V_{GS}=-4.5V, I_D=-14A$	-	-	50	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1	-	-	V
g_{fs}	Forward Transconductance	$V_{DS}=-10V, I_D=-18A$	-	20	-	S
I_{DSS}	Drain-Source Leakage Current($T_J=25^\circ C$)	$V_{DS}=-30V, V_{GS}=0V$	-	-	-1	μA
	Drain-Source Leakage Current($T_J=150^\circ C$)	$V_{DS}=-24V, V_{GS}=0V$	-	-	-25	
I_{GSS}	Gate Source Leakage	$V_{GS}=\pm 20V$	-	-	± 100	nA
Q_g	Total Gate Charge (Note 2)	$I_D=-18A$	-	14	22	nC
Q_{gs}	Gate-Source Charge	$V_{DS}=-24V$	-	3	-	
Q_{gd}	Gate-Drain ("Miller") Charge	$V_{GS}=-4.5V$	-	9	-	
$t_{d(on)}$	Turn-On Delay Time (Note 2)	$V_{DS}=-15V$	-	12	-	nS
t_r	Rise Time	$I_D=-18A$	-	56	-	
$t_{d(off)}$	Turn-Off Delay Time	$R_G=3.3\Omega, V_{GS}=-10V$	-	30	-	
t_f	Fall-Time	$R_D=0.8\Omega$	-	57	-	
C_{iss}	Input Capacitance	$V_{GS}=0V$	-	915	1465	pF
C_{oss}	Output Capacitance	$V_{DS}=-25V, f=1.0MHz$	-	280	-	
C_{rss}	Reverse Transfer Capacitance		-	195	-	

■ Source-Drain Diode

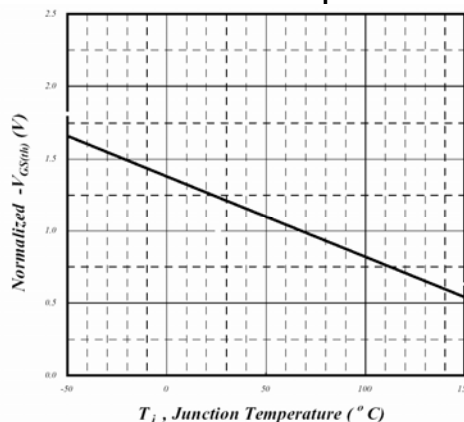
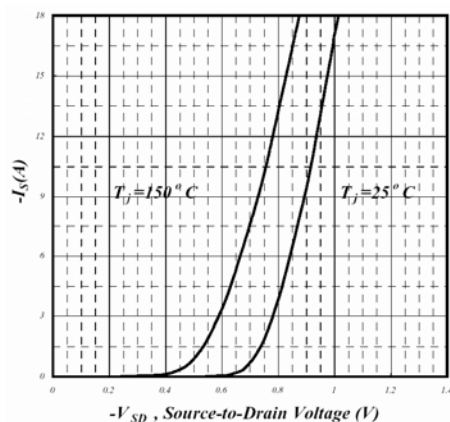
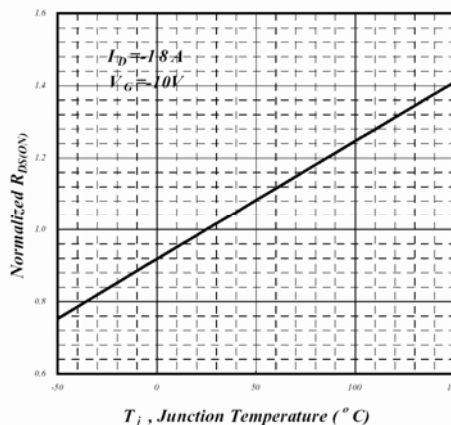
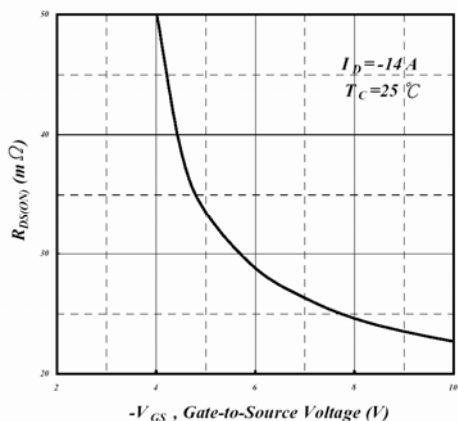
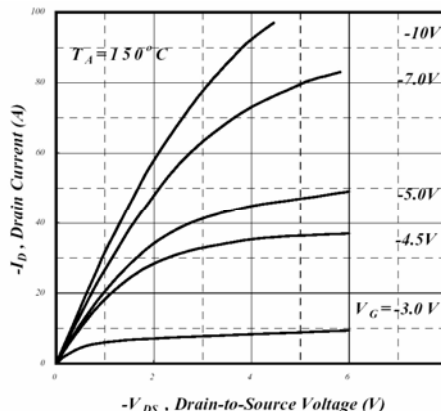
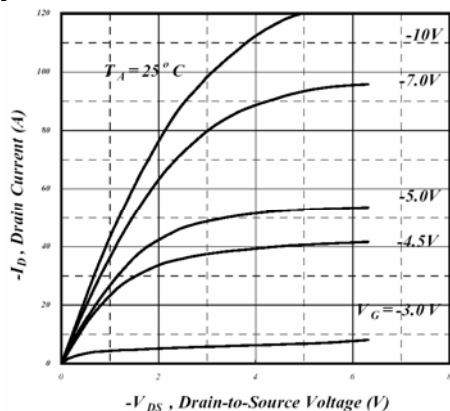
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_{SD}	Forward On Voltage (Note 2)	$I_S=-18A, V_{GS}=0V$	-	-	-1.2	V
t_{rr}	Reverse Recovery Time (Note 2)	$I_S=-18A, V_{GS}=0V,$	-	30	-	ns
Q_{rr}	Reverse Recovery Charge	$dI/dt=-100A/\mu s$	-	21	-	nC

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Note 1: Pulse width limited by safe operating area.

Note 2: Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

Typical Performance Characteristics



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■ Typical Performance Characteristics

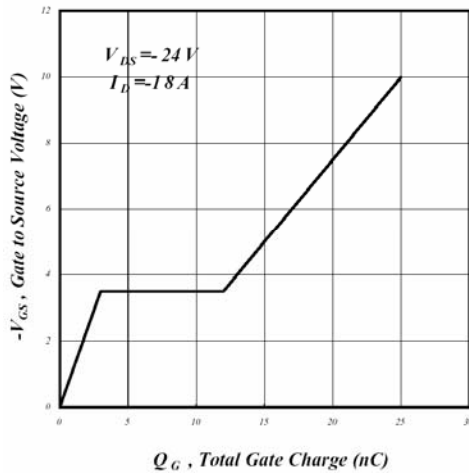


Fig 7. Gate Charge Characteristics

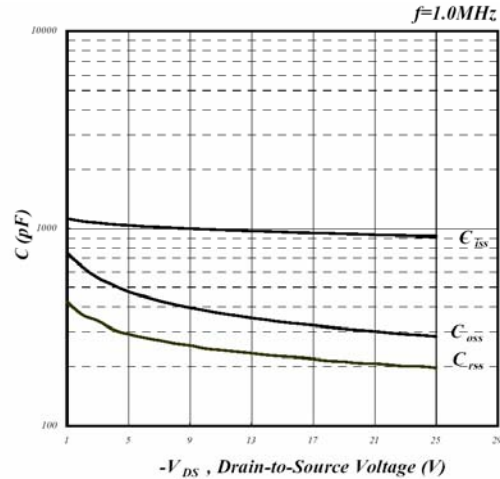


Fig 8. Typical Capacitance Characteristics

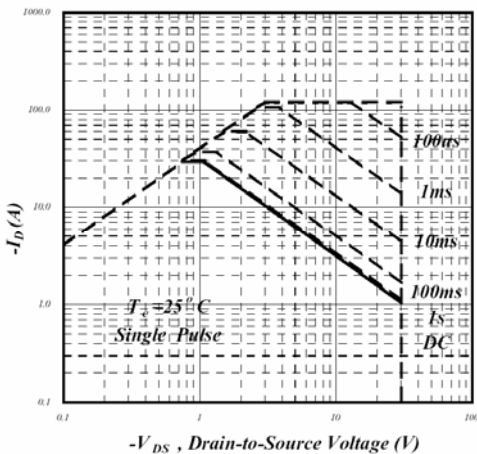


Fig 9. Maximum Safe Operating Area

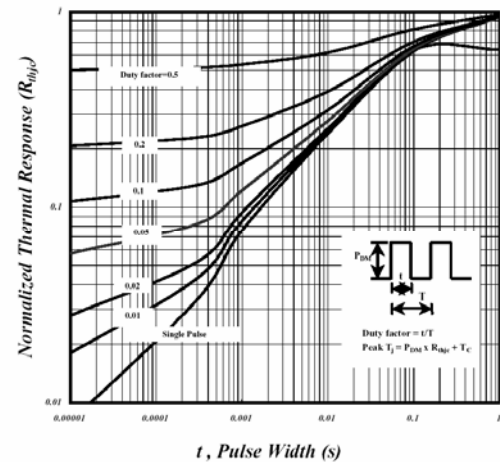


Fig10. Effective Transient Thermal Impedance

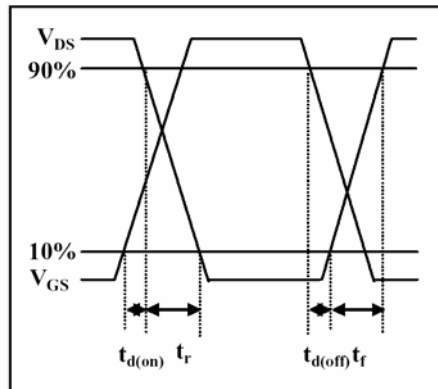


Fig 11. Switching Time Waveform

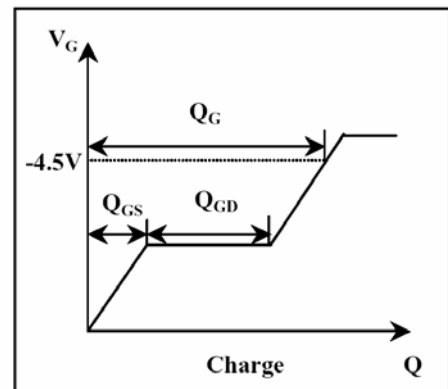
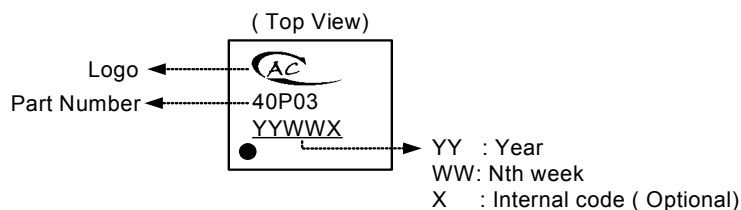


Fig 12. Gate Charge Waveform

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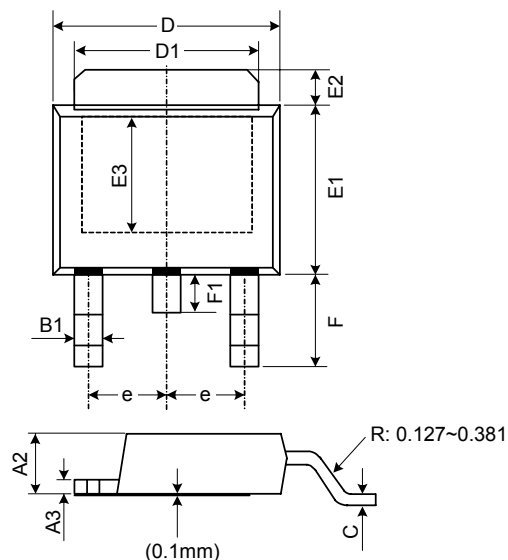
■ Marking Information

TO-252



■ Package Information

Package Type: TO-252



1. All Dimensions Are in Millimeters.
2. Dimension Does Not Include Mold Protrusions.

Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max.
A2	1.80	2.30	2.80
A3	0.40	0.50	0.60
B1	0.40	0.70	1.00
D	6.00	6.50	7.00
D1	4.80	5.35	5.90
F	2.20	2.63	3.05
F1	0.50	0.85	1.20
E1	5.10	5.70	6.30
E2	0.50	1.10	1.70
E3	3.50	4.00	4.50
e	-	2.30	-
C	0.35	0.50	0.65