

SANYO

No.3572

2SK1434

N-Channel MOS Silicon FET

Very High-Speed
Switching Applications**Features**

- Low ON-state resistance.
- Very high-speed switching.
- Converters.

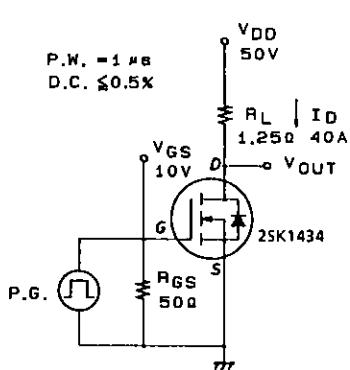
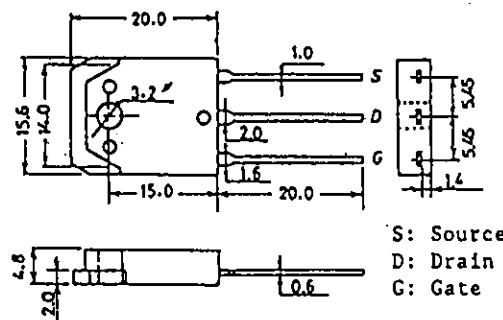
Absolute Maximum Ratings at Ta = 25°C

		unit
Drain to Source Voltage	V _{DSS}	100 V
Gate to Source Voltage	V _{GSS}	±20 V
Drain Current(DC)	I _D	60 A
Drain Current(Pulse)	I _{DP}	PW ≤ 10μs, duty cycle ≤ 1% 240 A
Allowable Power Dissipation	P _D	T _c = 25°C 150 W 2.5 W
Channel Temperature	T _{ch}	150 °C
Storage Temperature	T _{stg}	-55 to +150 °C

Electrical Characteristics at Ta = 25°C

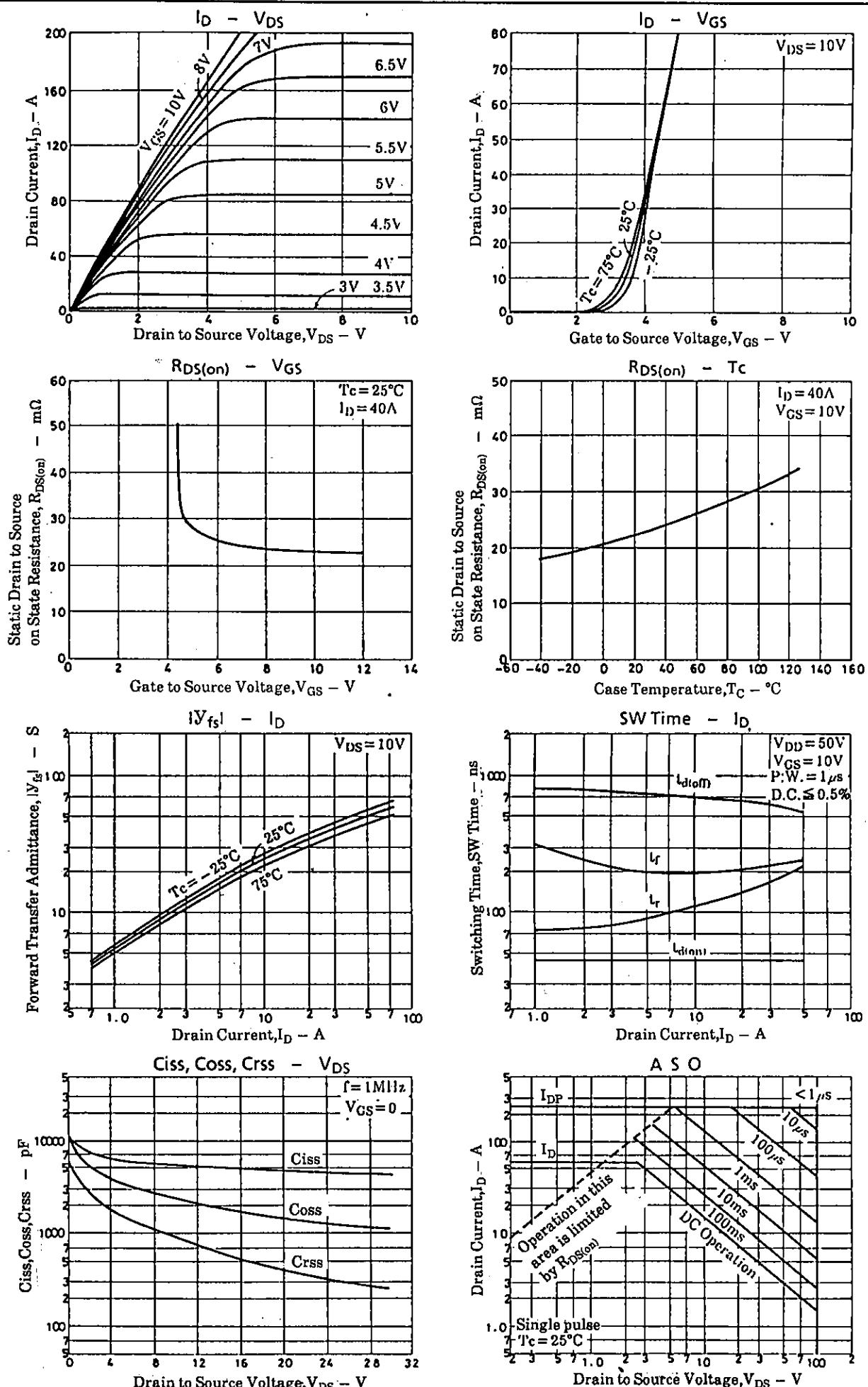
			min	typ	max	unit
D-S Breakdown Voltage	V _{(BR)DSS}	I _D = 1mA, V _{GS} = 0	100			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100V, V _{GS} = 0		100		μA
Gate to Source Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0		±100		nA
Cutoff Voltage	V _{GS(off)}	V _{DS} = 10V, I _D = 1mA	1.5		2.5	V
Forward Transfer Admittance	V _f	V _{DS} = 10V, I _D = 40A	27	45		S
Static Drain to Source on State Resistance	R _{DS(on)}	I _D = 40A, V _{GS} = 10V	0.023	0.035		Ω
Input Capacitance	C _{iss}	V _{DS} = 20V, f = 1MHz	4800			pF
Output Capacitance	C _{oss}	V _{DS} = 20V, f = 1MHz	1400			pF
Reverse Transfer Capacitance	C _{rss}	V _{DS} = 20V, f = 1MHz	400			pF
Turn-ON Delay Time	t _{d(on)}			45		ns
Rise Time	t _r			195		ns
Turn-OFF Delay Time	t _{d(off)}	I _D = 40A, V _{GS} = 10V V _{DD} = 50V, R _{GS} = 50Ω		560		ns
Fall Time	t _f			240		ns
Diode Forward Voltage	V _{SD}	I _S = 60A, V _{GS} = 0		1.8		V

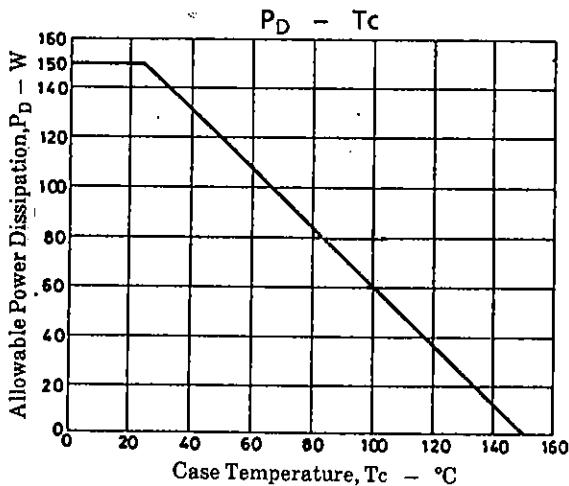
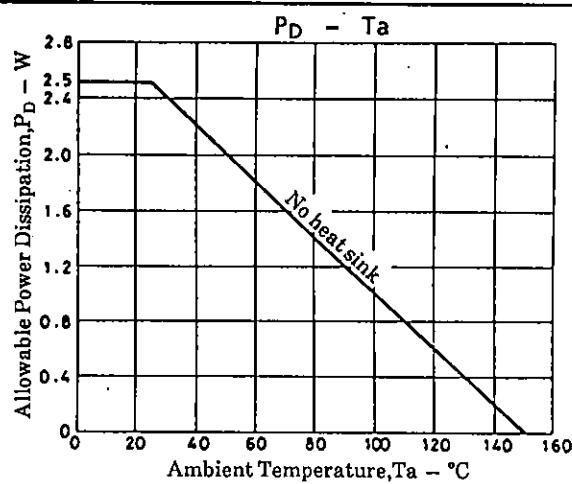
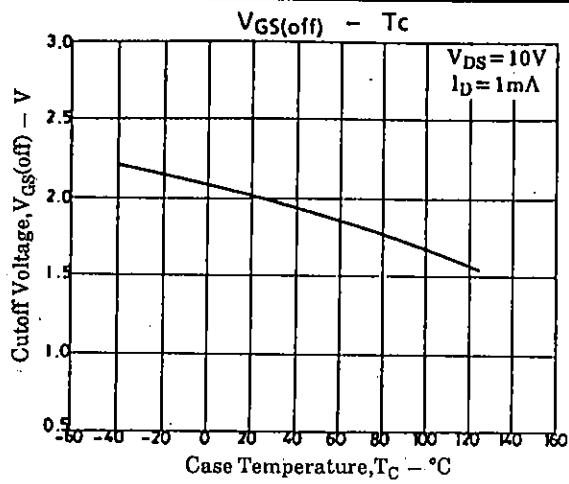
(Note) Be careful in handling the 2SK1434 because it has no protection diode between gate and source.

Switching Time Test Circuit**Package Dimensions 2056**
(unit : mm)

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