UNISONIC TECHNOLOGIES CO., LTD

DTD123Y

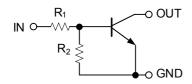
NPN EPITAXIAL SILICON TRANSISTOR

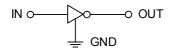
DIGITAL TRANSISTORS (BUILT-IN RESISTORS)

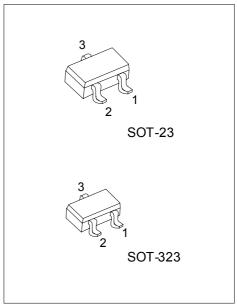
FEATURES

- * Built-in bias resistors that implies easy ON/OFF applications.
- * The bias resistors are thin-film resistors with complete isolation to allow negative input.

EQUIVALENT CIRCUIT





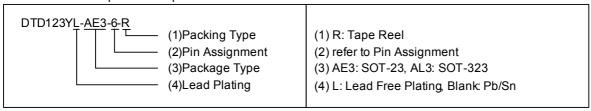


*Pb-free plating product number: DTD123YL

ORDERING INFORMATION

Order Number		Dookogo	Pin Assignment			Dooking	
Normal	Lead Free Plating	Package	1	2	3	Packing	
DTD123Y-AE3-6-R	DTD123YL-AE3-6-R	SOT-23	G	I	0	Tape Reel	
DTD123Y-AL3-6-R	DTD123YL-AL3-6-R	SOT-323	G	I	0	Tape Reel	

Note: G: GND I: Input O: Output



MARKING



www.unisonic.com.tw 1 of 3 QW-R206-087,A

ABSOLUTE MAXIMUM RATING (Ta=25 $^{\circ}$ C)

PARAMETER		RATINGS	UNIT
Supply voltage	V_{CC}	50	V
Input voltage	V_{IN}	-5 ~ + 12	V
Output current	Ic	500	mA
Power dissipation	P_{D}	200	mW
Junction Temperature	T _J	+150	$^{\circ}\mathbb{C}$
Storage Temperature	T _{STG}	-55 ~ +150	$^{\circ}\mathbb{C}$

Note Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
Input Voltage	V _{IN(OFF)}	V _{CC} =5V, I _{OUT} =100μA			0.3	V	
	V _{IN(ON)}	V _{OUT} =0.3V, I _{OUT} =20mA	2			V	
Output Voltage	V _{OUT(ON)}	I _O /I _I =50mA/2.5mA		0.1	0.3	V	
Input Current	I _{IN}	V _{IN} =5V			3.6	mA	
Output Current	I _{O(OFF)}	V _{CC} =50V, V _{IN} =0V			0.5	μΑ	
DC Current Gain	Gı	V _{OUT} =5V, I _{OUT} =50mA	56				
Input Resistance	R ₁		1.54	2.2	2.86	ΚΩ	
Resistance Ratio	R ₂ /R ₁		3.6	4.5	5.5		
Transition Frequency	f⊤	V _{CE} =10V, I _E = −50mA, f=100MHz *		200		MHz	

^{*} Transition frequency of the device

TYPICAL CHARACTERISTICS

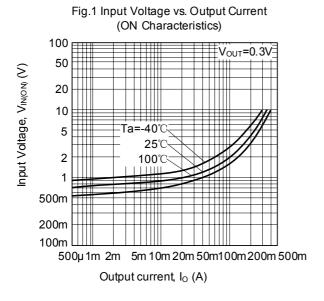


Fig. 2 Output Current vs. Input Voltage (OFF Characteristics) 10m 2m 1m Output Current, Iour (A) 500µ Γa=100°C 200μ 25℃ 100µ 50µ 20μ 10µ 5μ 0 1.5 2.5 3.0 0.5 1.0 2.0 Input Voltage, V_{I(OFF)} (V)

Fig.3 DC Current Gain vs. Output Current

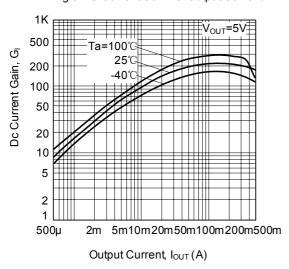
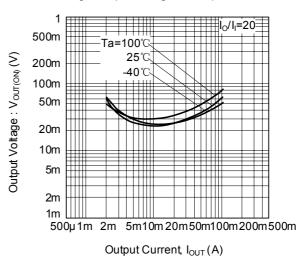


Fig.4 Output Voltage vs. Output Current



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