

RGB Video Amplifier for Monitors Monolithic IC MM1375

Outline

This IC is a wideband RGB video amplifier with DC control, developed for use in monitors. It has a differential input comparator for brightness adjustment, and three matched DC control attenuators for contrast adjustment. All DC control input is high impedance, and the operating range is set for easy interface with serial bus control systems, at 0~4V. Also, the building-in blanking circuit clamps video output to less than 0.2V during blanking, enabling blanking on the CRT cathode.

Features

1. Low power consumption (V_{cc}=8V, I_{cc}=68mA)
2. Smaller capacity for clamping capacitor (0.1μF)
3. High band video amp 100MHZ @-3dB
4. Matched (± 0.1 dB) contrast adjustment attenuators built in
5. Built-in cutoff and brightness adjustment input comparators using external gate control
6. Built-in high input impedance DC contrast control, 0~4V
7. Output blanking function
8. Output can drive hybrid or discrete CRT driver directly

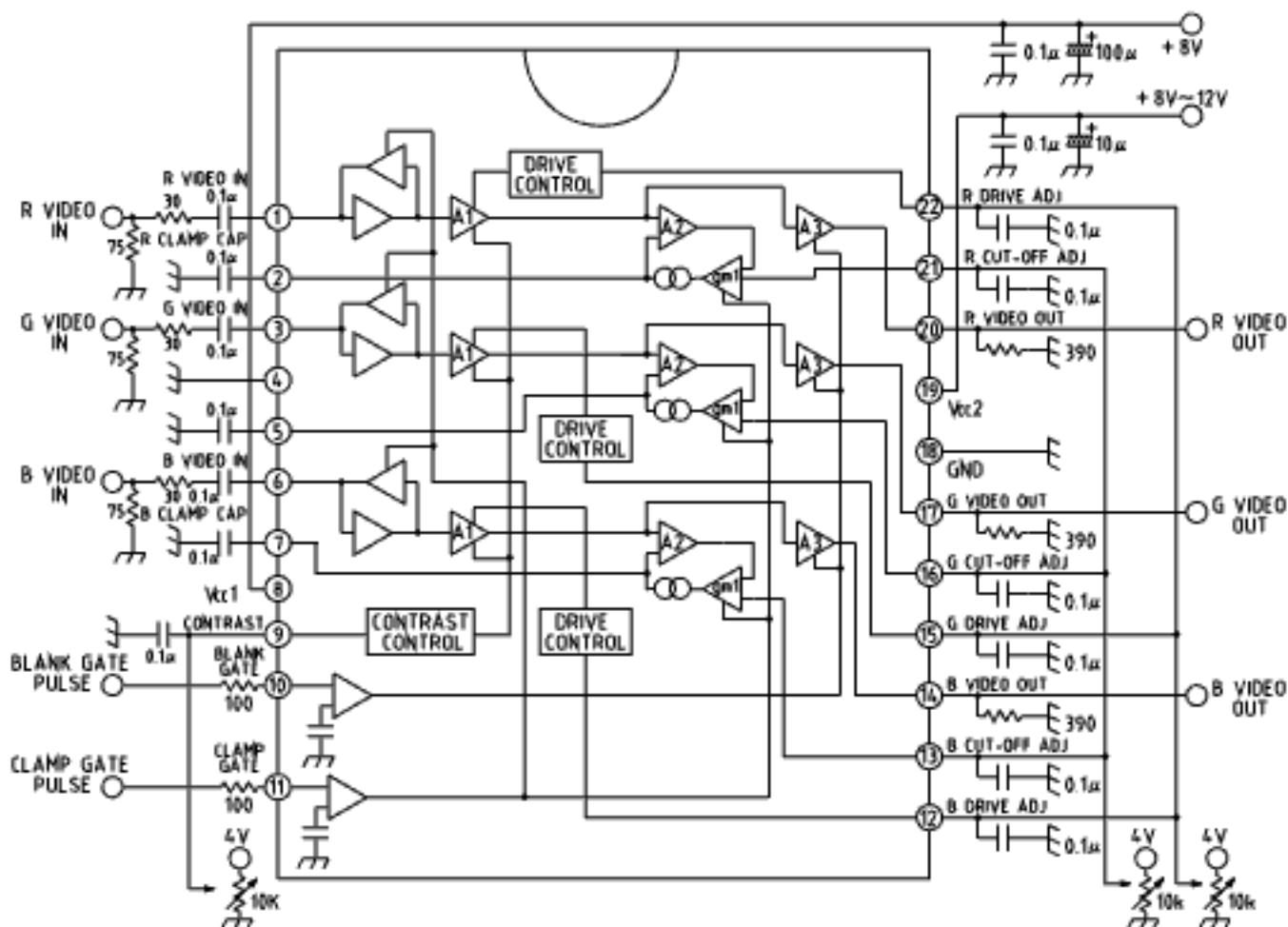
Package

SDIP-22A

Applications

1. High resolution RGB CRT monitors
2. AGC amps for video
3. Gain and DC offset control high bandwidth amps
4. Interface amps for LCD or CCD systems

Block Diagram



Pin Description

Pin no.	Pin name	Internal equivalent circuit diagram
1 3 6	R VIDEO IN G VIDEO IN B VIDEO IN	<p>Circuit diagram for pins 1, 3, and 6. The input signal is connected to the base of a transistor. The collector of this transistor is connected to a diode and then to the base of another transistor. The collector of this second transistor is connected to a diode and then to the base of a third transistor. The collector of the third transistor is connected to Vcc1 and the emitter is connected to GND. A 300 ohm resistor is connected between the collector of the third transistor and GND.</p>
2 5 7	R CLAMP CAP G CLAMP CAP B CLAMP CAP	<p>Circuit diagram for pins 2, 5, and 7. The input signal is connected to the base of a transistor. The collector of this transistor is connected to a diode and then to the base of another transistor. The collector of this second transistor is connected to a diode and then to the base of a third transistor. The collector of the third transistor is connected to Vcc1 and the emitter is connected to GND. A 300 ohm resistor is connected between the collector of the third transistor and GND.</p>
8	Vcc1	
4, 18	GND	
9 22 15 12	CONTRAST R DRIVE G DRIVE B DRIVE	<p>Circuit diagram for pins 9, 22, 15, and 12. The input signal is connected to the base of a transistor. The collector of this transistor is connected to a diode and then to the base of another transistor. The collector of this second transistor is connected to a diode and then to the base of a third transistor. The collector of the third transistor is connected to Vcc1 and the emitter is connected to GND. A 10k ohm resistor is connected between the collector of the third transistor and GND.</p>
10 11	BLANK GATE CLAMP GATE	<p>Circuit diagram for pins 10 and 11. The input signal is connected to the base of a transistor. The collector of this transistor is connected to a diode and then to the base of another transistor. The collector of this second transistor is connected to a diode and then to the base of a third transistor. The collector of the third transistor is connected to Vcc1 and the emitter is connected to GND. A 300 ohm resistor is connected between the collector of the third transistor and GND.</p>
20 17 14	R VIDEO OUT G VIDEO OUT B VIDEO OUT	<p>Circuit diagram for pins 20, 17, and 14. The input signal is connected to the base of a transistor. The collector of this transistor is connected to a diode and then to the base of another transistor. The collector of this second transistor is connected to a diode and then to the base of a third transistor. The collector of the third transistor is connected to Vcc2 and the emitter is connected to GND. A 40 ohm resistor is connected between the collector of the third transistor and GND.</p>
19	Vcc2	<p>Circuit diagram for pin 19. The input signal is connected to the base of a transistor. The collector of this transistor is connected to a diode and then to the base of another transistor. The collector of this second transistor is connected to Vcc1 and the emitter is connected to GND. A 40 ohm resistor is connected between the collector of the second transistor and GND.</p>
21 16 13	R CUT-OFF ADJ G CUT-OFF ADJ B CUT-OFF ADJ	<p>Circuit diagram for pins 21, 16, and 13. The input signal is connected to the base of a transistor. The collector of this transistor is connected to a diode and then to the base of another transistor. The collector of this second transistor is connected to a diode and then to the base of a third transistor. The collector of the third transistor is connected to Vcc1 and the emitter is connected to GND. A 500 ohm resistor is connected between the collector of the third transistor and GND.</p>

Absolute Maximum Ratings (Ta=25°C)

Item	Symbol	Ratings	Units
Storage temperature	T _{STG}	-40~+125	°C
Operating temperature	T _{OPR}	-20~+80	°C
Power supply voltage 1	V _{CC1}	10	V
Power supply voltage 2	V _{CC2}	15	V
Input voltage range	V _{IN}	GND ≤ V _{IN} ≤ V _{CC1}	V
Video output current	I _O	28	mA
Allowable loss	P _D	1.6	W
Electrostatic breakdown		2	kV
Pin temperature		265 *	°C

Note : Solder for 10S

Recommended Operating Conditions (Ta=25°C)

Item	Symbol	Min.	Typ.	Max.	Units
Operating power supply voltage 1	V _{CC1}	7.6	8.0	8.4	V
Operating power supply voltage 2	V _{CC2}	7.6		12.6	V

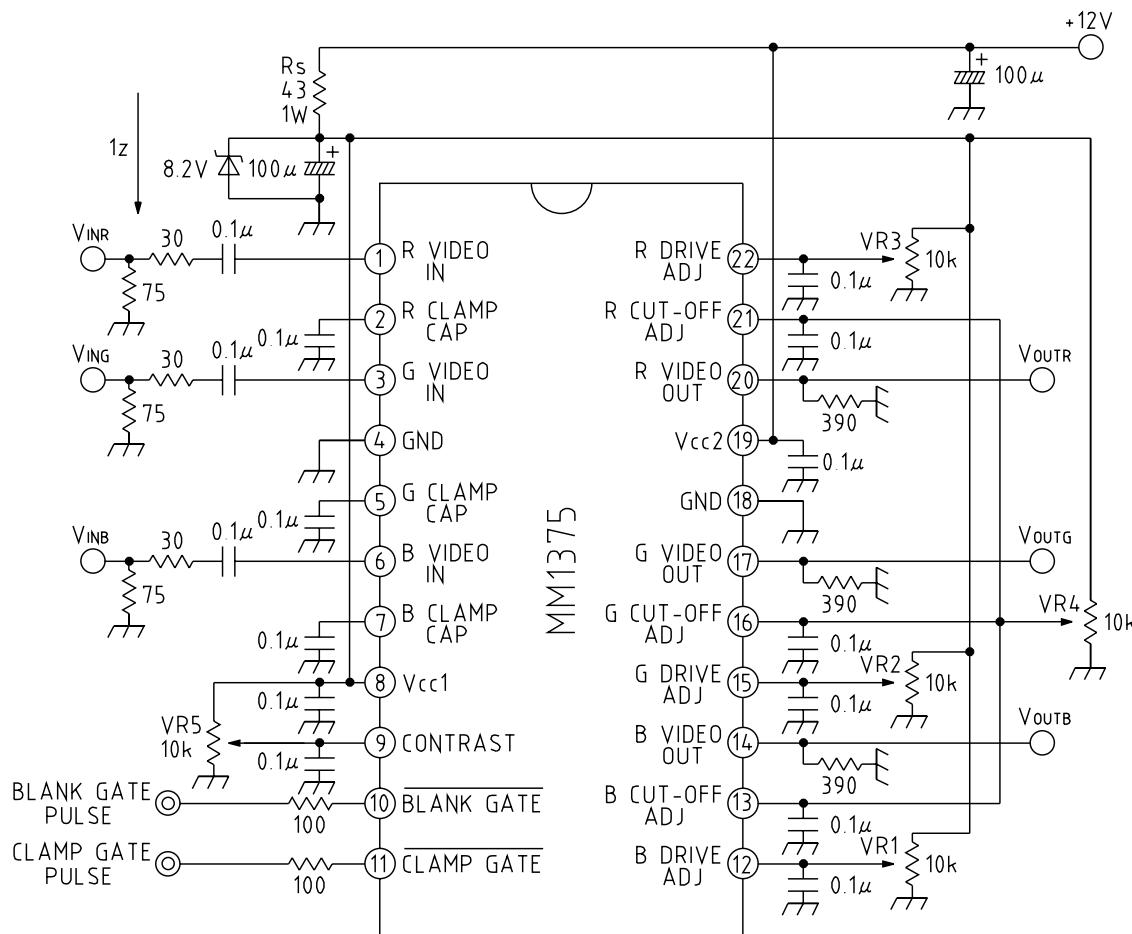
DC Electrical Characteristics(Except where noted otherwise, Ta=25°C, V_{CC1}=8V, V_{CC2}=8~12V, V₉=4V, V₁₀=4V, V₁₁=0V, V_{C-O}=1.0V, V_{DRV}=4V)

Item	Symbol	Measurement conditions	Min.	Typ.	Max.	Units
Power supply current	I _{CC1}	V _{CC1} +V _{CC2}		68	80	mA
Video amp input voltage	V _B			2.2		V
Video input resistance	R _B			100		kΩ
Clamp gate input voltage L	V _{CGL}		0.8	1.2		V
Clamp gate input voltage H	V _{CGH}			1.6	2.0	V
Clamp gate input current L	I _{CGL}	V ₁₁ =0V		-0.5	-5.0	μA
Clamp gate input current H	I _{CGH}	V ₁₁ =8V		0.01	1.0	μA
Input charging current	I _{IN+}		0.75	1.0		mA
Input discharge current	I _{IN-}		-0.75	-1.0		mA
Clamp cap charge current	I _{CL+}		0.75	1.0		mA
Clamp cap discharge current	I _{CL-}		-0.75	-1.0		mA
Clamp cap bias discharge current	I _{CLB}			50		nA
Blanking gate input voltage L	V _{BGL}		0.8	1.2		V
Blanking gate input voltage H	V _{BGH}			1.6	2.0	V
Blanking gate input current L	I _{BGL}	V ₁₀ =0V		-1.0	-5.0	μA
Blanking gate input current H	I _{BGH}	V ₁₀ =8V		0.01	1.0	μA
Video output voltage L	V _{OL}	V _{C-O} =0V		0.15	0.5	V
Video output voltage H	V _{OH}	V _{C-O} =6V	5.0	5.5		V
Black level output voltage	V _O	V _{C-O} =1V		1.0		V
△ Black level output voltage	△V _O	V _{C-O} =1V		±100		mV
Output blanking voltage	V _{OBLK}	V ₁₀ =0V		100	300	mV
Contrast control input current	I ₉	V ₉ =4V		190		μA
Drive control input current	I _{DBV}	V _{DBV} =4V		190		μA
Cutoff control input current	I _{C-O}	V _{C-O} =0~4V		-500		nA

AC Electrical Characteristics (Except where noted otherwise, Ta=25°C, Vcc1=8V, Vcc2=8~12V)

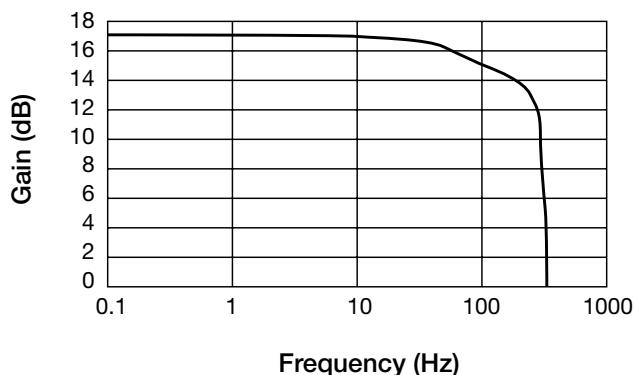
Item	Symbol	Measurement conditions	Min.	Typ.	Max.	Units
Video amp gain	Av max.	V _{IN} =635mV _{P-P} , V ₉ =4V	5.4	7.0		V/V
		V _{DRV} =4V	14.6	16.9		dB
Gain attenuation 1	ΔAv1	V _{IN} =635mV _{P-P} , V ₉ =2V		-6		dB
Gain attenuation 2	ΔAv2	V _{IN} =635mV _{P-P} , V ₉ =0.5V		-38		dB
Drive control range	ΔAVDRV	V _{DRV} =0~4V, V ₉ =4V		6		dB
Gain matching	AVMAT	V ₉ =V _{DRV} =4V		0.3		dB
Gain change between amps	ΔAVMAT	V ₉ =4~2V		0.1		dB
Video amp distortion	THD	V _{OUT} =1V _{P-P}		1		%
Video amp frequency bandwidth	fbw	V _{OUT} =4V _{P-P} , V ₉ =V _{DRV} =4V		100		MHz
Output rise time	tr	V _{OUT} =4V _{P-P}		3		nS
Output fall time	tf	V _{OUT} =4V _{P-P}		4		nS
Video amp isolation 1 (f_{IN}=10kHz)	V _{SEP1}	V ₉ =4V		-70		dB
Video amp isolation 2 (f_{IN}=10MHz)	V _{SEP2}	V ₉ =4V		-50		dB
Blanking output fall time	tr	V _{OUT} =1V _{P-P}		7		nS
Blanking output fall time	tf	V _{OUT} =1V _{P-P}		7		nS
Back-porch clamping pulse width	tpw			200		nS

Application Circuits



Characteristics

Gain vs Frequency



Pulse reaction

